Hewlett-Packard Company
ProLiant ML370 G5
(1.86 GHz, Intel Xeon processor E5320)

SPEC® CFP2006 Result

| SPECfp®_rate2006 | 43.7 |
| SPECfp_rate_base2006 | 43.0 |

CPU2006 license: 3
Test sponsor: Hewlett-Packard Company
Tested by: Hewlett-Packard Company

Hewlett-Packard Company
(1.86 GHz, Intel Xeon processor E5320)

SPECfp_rate2006 = 43.7
SPECfp_rate_base2006 = 43.0

CPU Name: Intel Xeon E5320
CPU Characteristics: 1.86 GHz, 2x4 MB L2 shared, 1066 MHz system bus
CPU MHz: 1860
FPU: Integrated
CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip
CPU(s) orderable: 1,2 chips
Primary Cache: 32 KB I + 32 KB D on chip per core
Secondary Cache: 8 MB 1+0 on chip per core, 4 MB shared / 2 cores

Operating System: SuSE Linux Enterprise Server 10 (x86_64)
kernel 2.6.16.21-0.8-smp
Compiler: Intel C++ Compiler for Intel EM64T-based applications, Version 9.1
Intel Fortran Compiler for Intel EM64T-based applications, Version 9.1
Auto Parallel: No
## Results Table

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Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Platform Notes

Power Regulator set to Static High Performance Mode in BIOS.
Adjacent Sector Prefetch Disabled in BIOS.
"/usr/bin/taskset" used to bind processes to CPUs.
Environment stack size set to 'unlimited'

## Base Compiler Invocation

C benchmarks:
```
icc
```

C++ benchmarks:
```
icpc
```

Continued on next page
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CPU2006 license: 3
Test sponsor: Hewlett-Packard Company
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Test date: Feb-2007
Hardware Availability: Nov-2006
Software Availability: Nov-2006

Base Compiler Invocation (Continued)

Fortran benchmarks:
ifort

Benchmarks using both Fortran and C:
icc ifort

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416games: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDtd: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks: -fast

C++ benchmarks: -fast

Fortran benchmarks: -fast

Benchmarks using both Fortran and C: -fast

Peak Compiler Invocation

C benchmarks: icc

Continued on next page
Peek Compiler Invocation (Continued)

C++ benchmarks:
icpc

Fortran benchmarks:
ifort

Benchmarks using both Fortran and C:
icc ifort

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:
- prof_gen(pass 1) - prof_use(pass 2) - fast - auto_ilp32

C++ benchmarks:
- prof_gen(pass 1) - prof_use(pass 2) - fast - auto_ilp32

Fortran benchmarks:
- prof_gen(pass 1) - prof_use(pass 2) - fast

Benchmarks using both Fortran and C:
- prof_gen(pass 1) - prof_use(pass 2) - fast - auto_ilp32

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/hp-ic91-flags.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/hp-ic91-flags.xml

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For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.0.