### SPEC® CFP2006 Result

**Fujitsu Siemens Computers**

PRIMERGY RX200 S3, Intel Xeon processor E5320, 1.86 GHz

| SPECfp_rate2006 | 45.5 |
| SPECfp_rate_base2006 | 44.0 |

**CPU2006 license:** 22  
**Test sponsor:** Fujitsu Siemens Computers  
**Tested by:** Fujitsu Siemens Computers  
**Test date:** Mar-2007  
**Hardware Availability:** Nov-2006  
**Software Availability:** Jan-2007

**Hardware**

- **CPU Name:** Intel Xeon E5320  
- **CPU Characteristics:** E5320  
- **CPU MHz:** 1867  
- **FPU:** Integrated  
- **CPU(s) enabled:** 8 cores, 2 chips, 4 cores/chip  
- **CPU(s) orderable:** 1.2 chips  
- **Primary Cache:** 32 KB I + 32 KB D on chip per core  
- **Secondary Cache:** 8 MB I+D on chip per core, 4 MB shared / 2 cores

**Software**

- **Operating System:** 64-Bit SUSE LINUX Enterprise Server 10, Kernel 2.6.16.21-0.8-smp on an x86_64  
- **Compiler:** Intel C++ Compiler for IA32/EM64T application, Version 9.1 - built 20070109, Package-ID: l_cc_p_9.1.046  
- **Auto Parallel:** No  
- **File System:** ReiserFS

---

**Copies**

```
8  102  46.8  85.2  53.2
8  23.7  24.0  88.1
8  46.7
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8
8  52.9
8
8
8  77.7
8  28.0
8  87.4
8  25.5
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```

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L3 Cache: None
Other Cache: None
Memory: 16 GB (8x2 GB DDR2 PC2-5300F, 2 rank, CAS 5-5-5, with ECC)
Disk Subsystem: SAS (73GB 15400 rpm)
Other Hardware: None

Results Table

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</table>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run '/usr/bin/taskset' used to bind processes to CPUs

General Notes

The system bus runs at 1067 MHz

All binaries were built with 64-bit Intel compiler except: 433.milc, 434.zeusmp, 450.soplex, 470.lbm and 482.sphinx3 in peak were built with 32-bit Intel compiler by changing the path for include and library files.

BIOS configuration:

Continued on next page

Standard Performance Evaluation Corporation
info@spec.org
http://www.spec.org/
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Hardware Availability: Nov-2006
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General Notes (Continued)

Hardware Prefetch = Disable, Adjacent Sector Prefetch = Disable
For information about Fujitsu Siemens Computers in your country please see:
http://www.fujitsu-siemens.com/countries

Base Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc

Fortran benchmarks:
ifort

Benchmarks using both Fortran and C:
icc ifort

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:
-fast

C++ benchmarks:
-fast

Continued on next page
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Base Optimization Flags (Continued)

Fortran benchmarks:
- fast
Benchmarks using both Fortran and C:
- fast

Peak Compiler Invocation

C benchmarks:
```
/opt/intel/cc/9.1.046/bin/ icc -I/opt/intel/cc/9.1.046/include
-L/opt/intel/cc/9.1.046/lib
```

C++ benchmarks (except as noted below):
```
icpc
```
```
450.so4plex: /opt/intel/cc/9.1.046/bin/ icpc
-I/opt/intel/cc/9.1.046/include -L/opt/intel/cc/9.1.046/lib
```

Fortran benchmarks (except as noted below):
```
ifort
```
```
434.zeusmp: /opt/intel/fc/9.1.041/bin/ ifort
-I/opt/intel/fc/9.1.041/include -L/opt/intel/fc/9.1.041/lib
```

Benchmarks using both Fortran and C:
```
icc ifort
```

Peak Portability Flags
Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:
```
433.milc: -prof_gen(pass 1) -prof_use(pass 2) -fast
```
```
470.lbm: Same as 433.milc
```
```
482.sphinx3: -fast
```

C++ benchmarks:

Continued on next page
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Peak Optimization Flags (Continued)

444.namd: basepeak = yes
447.dealII: -prof_gen(pass 1) -prof_use(pas 2) -fast
450.soplex: Same as 447.dealII
453.povray: Same as 447.dealII

Fortran benchmarks:
410.bwaves: basepeak = yes
416.gamess: basepeak = yes
434.zeusmp: -fast
437.leslie3d: basepeak = yes
459.GemsFDTD: basepeak = yes
465.tonto: -prof_gen(pass 1) -prof_use(pass 2) -fast

Benchmarks using both Fortran and C:
435.gromacs: -prof_gen(pass 1) -prof_use(pass 2) -fast
436.cactusADM: basepeak = yes
454.calculix: Same as 435.gromacs
481.wrf: basepeak = yes

The flags file that was used to format this result can be browsed at

You can also download the XML flags source by saving the following link:

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For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.0.
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