## Dell Inc.

PowerEdge 1955 (Intel Xeon processor X5355, 2.66 GHz)

<table>
<thead>
<tr>
<th>SPECint_rate2006</th>
<th>82.5</th>
</tr>
</thead>
<tbody>
<tr>
<td>SPECint_rate_base2006</td>
<td>80.4</td>
</tr>
</tbody>
</table>

**CPU2006 license:** 55  
**Test sponsor:** Dell Inc.  
**Tested by:** Dell Inc.  
**Test date:** Mar-2007  
**Hardware Availability:** Dec-2006  
**Software Availability:** Nov-2006

### Hardware

- **CPU Name:** Intel Xeon X5355  
- **CPU Characteristics:** 1333 MHz Bus Speed  
- **CPU MHz:** 2666  
- **FPU:** Integrated  
- **CPU(s) enabled:** 8 cores, 2 chips, 4 cores/chip  
- **CPU(s) orderable:** 1.2 chips  
- **Primary Cache:** 32 KB I + 32 KB D on chip per core  
- **Secondary Cache:** 8 MB I+D on chip per chip, 4 MB shared / 2 cores  
- **L3 Cache:** None  
- **Other Cache:** None  
- **Memory:** 8 GB (8 x 1 GB 667 MHz CL5 FR-DIMM DDR2 SDRAM)  
- **Disk Subsystem:** 1 x 36GB SAS 10000 RPM  
- **Other Hardware:** None

### Software

- **Operating System:** Windows Server 2003 Enterprise x64 Edition + SP1 (64-bit)  
- **Compiler:** Intel C++ Compiler 9.1 for IA32 (20061103Z)  
- **Auto Parallel:** No  
- **File System:** NTFS  
- **System State:** Default  
- **Base Pointers:** 32-bit  
- **Peak Pointers:** 32-bit  
- **Other Software:** MicroQuill SmartHeap Library 8.0
Dell Inc.

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SPECint_rate2006 = 82.5
SPECint_rate_base2006 = 80.4

Results Table

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</tbody>
</table>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Platform Notes

Hardware Prefetcher disabled in BIOS
Adjacent Cache Line Prefetch disabled in BIOS

Base Compiler Invocation

C benchmarks:
icl -Qc99

C++ benchmarks:
icl

Base Portability Flags

403.gcc: -DSPEC_CPU_WIN32
464.h264ref: -DSPEC_CPU_NO_INTPHYES -DWIN32
483.xalancbmk: -Qoption, cpp, --no_wchar_t_keyword

Base Optimization Flags

C benchmarks:
-fast /F512000000 shlw32m.lib

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SPEC CINT2006 Result

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Base Optimization Flags (Continued)

C++ benchmarks:
- fast -Qcxx_features /F512000000 shlw32M.lib
- link /FORCE:MULTIPLE

Base Other Flags

C benchmarks:
403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks:
icl -Qc99
C++ benchmarks:
icl

Peak Portability Flags

403.gcc: -DSPEC_CPU_WIN32
464.h264ref: -DSPEC_CPU_NO_INTTYPES -DWIN32
483.xalancbmk: -Qoption,cpp,--no_wchar_t_keyword

Peak Optimization Flags

C benchmarks:
400.perlbench: ONESTEP -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast /F512000000 shlw32M.lib -link /FORCE:MULTIPLE
401.bzip2: basepeak = yes
403.gcc: Same as 400.perlbench
429.mcf: ONESTEP -fast /F512000000 shlw32M.lib -link /FORCE:MULTIPLE
445.gobmk: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast /F512000000 shlw32M.lib -link /FORCE:MULTIPLE

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**Peak Optimization Flags (Continued)**

456.hmmer: basepeak = yes  
458.sjeng: Same as 400.perlbench  
462.libquantum: Same as 400.perlbench  
464.h264ref: Same as 400.perlbench

C++ benchmarks:

471.omnetpp: ONESTEP -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast  
-Qcxx_features /F512000000 shlw32M.lib  
-link /FORCE:MULTIPLE

473.astar: Same as 471.omnetpp  
483.xalancbmk: ONESTEP -fast -Qcxx_features /F512000000 shlw32M.lib  
-link /FORCE:MULTIPLE

**Peak Other Flags**

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at  

You can also download the XML flags source by saving the following link:  
http://www.spec.org/cpu2006/flags/dell.cpu2006.ic91.flags.xml

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For questions about this result, please contact the tester.  
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.0.  