Bull SAS

NovaScale R480 (3.40 GHz, Intel Xeon 7140M)

CPU2006 license: 3
Test sponsor: Bull SAS
Tested by: Bull SAS

Test date: Mar-2007
Hardware Availability: Sep-2006
Software Availability: Nov-2006

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NovaScale R480 (3.40 GHz, Intel Xeon 7140M)

CPU Name: Intel Xeon 7140M
CPU Characteristics: 3.4GHz, 800MHz bus
CPU MHz: 3400
FPU: Integrated
CPU(s) enabled: 1 core, 1 chip, 2 cores/chip
CPU(s) orderable: 1,2,4 chips
Primary Cache: 12 K micro-ops I + 16 KB D on chip per core
Secondary Cache: 1 MB I+D on chip per core
L3 Cache: 16 MB I+D on chip per chip
Other Cache: None
Memory: 16 GB (16X1GB 1Rx4 PC2-3200R-333 400MHz DDR2)
Disk Subsystem: 2x36GB SAS 15000 rpm
Other Hardware: None

Compiler: Intel C++ Compiler 9.1 for 32-bit
Build 20061103Z Package ID: W_CC_P_9.1.033
Microsoft Visual Studio .NET 2003 (libraries)
Auto Parallel: No
File System: NTFS
System State: Default
Base Pointers: 32-bit
Peak Pointers: 32-bit
Other Software: MicroQuill SmartHeap Library 8.0 (shlW32M.lib)

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SPECint_base2006 = 10.6

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Hardware

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SPEC CINT2006 Result

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Results Table

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<tr>
<th>Benchmark</th>
<th>Seconds</th>
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<td>422</td>
<td>16.4</td>
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</table>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

Other Configuration Notes
/NUMPROC=1 flag was added to boot.ini to invoke uniprocessor environment

The NovaScale T880 and the NovaScale R480 models are electronically equivalent. The results have been measured on a NovaScale R480 model.

Base Compiler Invocation

C benchmarks:
  icl -Qvc7.1 -Qc99

C++ benchmarks:
  icl -Qvc7.1

Base Portability Flags

403.gcc: -DSPEC_CPU_WIN32

Continued on next page
SPEC CINT2006 Result

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Base Portability Flags (Continued)

464.h264ref: -DSPEC_CPU_NO_INTTYPES -DWIN32

Base Optimization Flags

C benchmarks:
-fast /F512000000 shlw32m.lib

-C++ benchmarks:
-fast -Qcxx_features /F512000000 shlw32m.lib

Base Other Flags

C benchmarks:
403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks:
icl -Qvc7.1 -Qc99

-C++ benchmarks:
icl -Qvc7.1

Peak Portability Flags

403.gcc: -DSPEC_CPU_WIN32
464.h264ref: -DSPEC_CPU_NO_INTTYPES -DWIN32

Peak Optimization Flags

C benchmarks:
400.perlbench: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast /F512000000 shlw32m.lib

401.bzip2: Same as 400.perlbench

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Peak Optimization Flags (Continued)

403.gcc: Same as 400.perlbench
429.mcf: basepeak = yes
445.gobmk: Same as 400.perlbench
456.hmmer: Same as 400.perlbench
458.sjeng: Same as 400.perlbench
462.libquantum: Same as 400.perlbench
464.h264ref: Same as 400.perlbench

C++ benchmarks:
- Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qcxx_features
/F512000000 shlw32m.lib -link /FORCE:MULTIPLE

Peak Other Flags

C benchmarks:
403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/flags.20090714.00.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/flags.20090714.00.xml

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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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