SPEC® CINT2006 Result

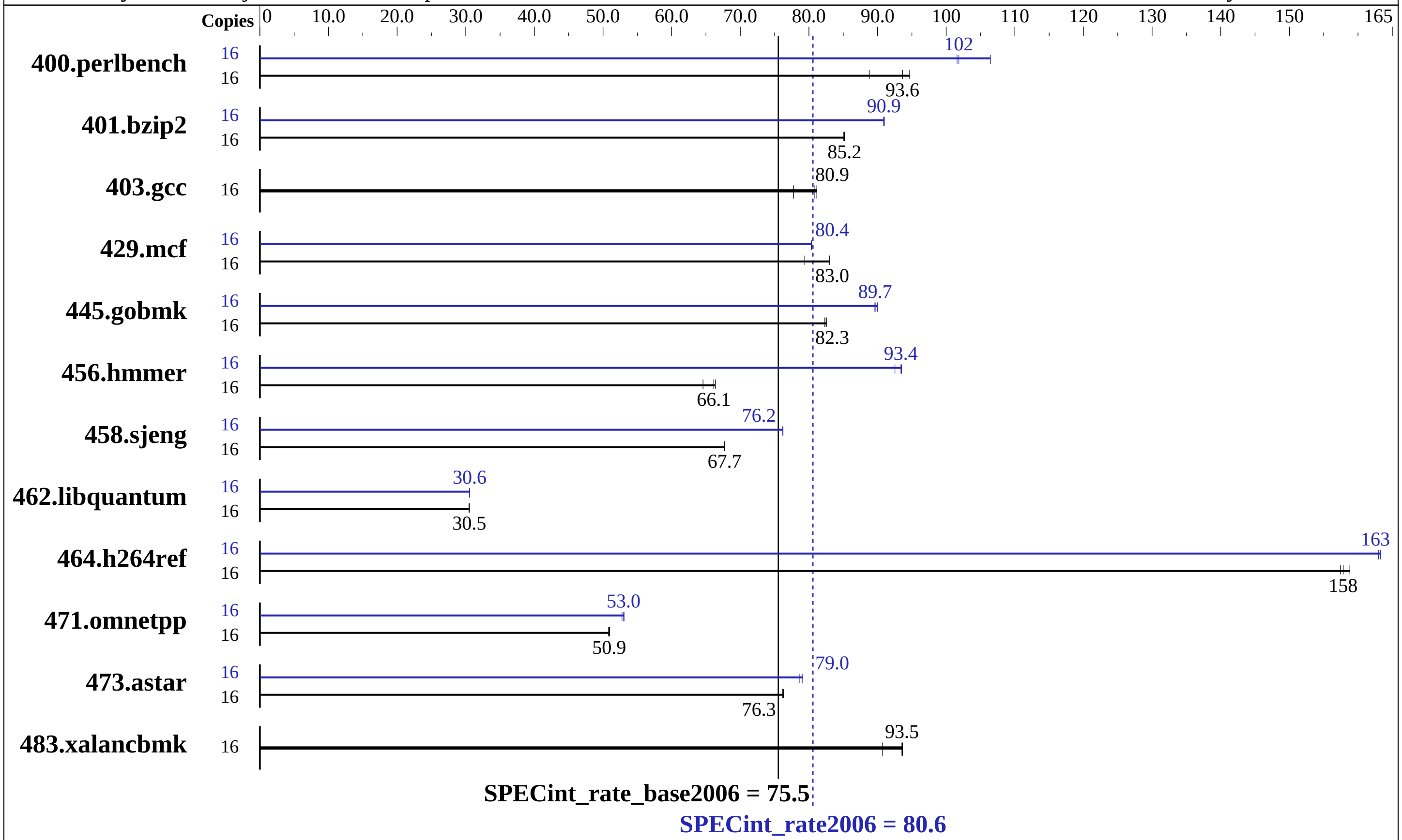
Fujitsu Siemens Computers

PRIMERGY RX600 S3, Intel Xeon processor 7140M, 3.40 GHz

SPECint®_rate2006 = 80.6

SPECint_rate_base2006 = 75.5

CPU2006 license: 22		**Test date:**	Mar-2007
Test sponsor:	Fujitsu Siemens Computers	**Hardware Availability:**	Dec-2006
Tested by:	Fujitsu Siemens Computers	**Software Availability:**	Feb-2007



Hardware

CPU Name:	Intel Xeon 7140M
CPU Characteristics:	800 MHz system bus
CPU MHz:	3400
FPU:	Integrated
CPU(s) enabled:	8 cores, 4 chips, 2 cores/chip, 2 threads/core
CPU(s) orderable:	1,2,4 chips
Primary Cache:	12 K micro-ops I + 16 KB D on chip per core
Secondary Cache:	1 MB I+D on chip per core
L3 Cache:	16 MB I+D on chip per chip
Other Cache:	None
Memory:	32 GB (16x2 GB DDR2 PC2-3200R, 1 rank, CAS 3-3-3, with ECC)
Disk Subsystem:	Fujitsu MAS3735NC (SCSI 73GB 15 krpm)
Other Hardware:	None

Software

Operating System:	64-Bit SUSE LINUX Enterprise Server 10, Kernel 2.6.16.21-0.8-smp on an x86_64
Compiler:	Intel C++ Compiler for IA32/EM64T application, Version 9.1 - Build 20070215, Package-ID: l_cc_p_9.1.047
Auto Parallel:	No
File System:	ReiserFS
System State:	Multiuser, Runlevel 3
Base Pointers:	32-bit
Peak Pointers:	32/64-bit
Other Software:	Smart Heap Library, Version 8.1

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Results Table

	Base							Peak						
Benchmark	**Copies**	**Seconds**	**Ratio**	**Seconds**	**Ratio**	**Seconds**	**Ratio**	**Copies**	**Seconds**	**Ratio**	**Seconds**	**Ratio**	**Seconds**	**Ratio**
400.perlbench	16	1651	94.7	1761	88.8	**1669**	**93.6**	16	1539	102	**1535**	**102**	1469	106
401.bzip2	16	1815	85.1	**1813**	**85.2**	1812	85.2	16	1697	91.0	1699	90.9	**1698**	**90.9**
403.gcc	16	1587	81.2	**1593**	**80.9**	1656	77.8	16	1587	81.2	**1593**	**80.9**	1656	77.8
429.mcf	16	**1758**	**83.0**	1757	83.0	1838	79.4	16	1816	80.3	1815	80.4	**1816**	**80.4**
445.gobmk	16	2039	82.3	2033	82.6	**2039**	**82.3**	16	1865	90.0	**1872**	**89.7**	1875	89.5
456.hmmer	16	**2257**	**66.1**	2249	66.4	2312	64.6	16	1596	93.5	**1598**	**93.4**	1613	92.5
458.sjeng	16	2858	67.7	**2858**	**67.7**	2861	67.7	16	2541	76.2	2540	76.2	**2540**	**76.2**
462.libquantum	16	10881	30.5	**10864**	**30.5**	10856	30.5	16	10849	30.6	**10843**	**30.6**	10839	30.6
464.h264ref	16	2249	157	**2243**	**158**	2229	159	16	2173	163	2168	163	**2172**	**163**
471.omnetpp	16	1969	50.8	1962	51.0	**1965**	**50.9**	16	1884	53.1	**1887**	**53.0**	1895	52.8
473.astar	16	1475	76.1	**1473**	**76.3**	1472	76.3	16	1420	79.1	**1422**	**79.0**	1429	78.6
483.xalancbmk	16	**1180**	**93.5**	1217	90.7	1179	93.6	16	**1180**	**93.5**	1217	90.7	1179	93.6

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

```
'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run
'/usr/bin/taskset' used to bind processes to CPUs
```

General Notes

```
The system bus runs at 800 MHz

All binaries were built with 32-bit Intel compiler except:
401.bzip2, 456.hmmer and 462.libquantum in peak were built with
64-bit Intel compiler by changing the path for include and library files.

BIOS configuration:
Hardware Prefetch = Enable

The PRIMERGY RX600 S3 and the PRIMERGY TX600 S3 are electronically equivalent.

For information about Fujitsu Siemens Computers in your country please see:
http://www.fujitsu-siemens.com/countries
```

Base Compiler Invocation

C benchmarks:
    icc

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Base Compiler Invocation (Continued)

C++ benchmarks:
`icpc`

Base Portability Flags

400.perlbench: `-DSPEC_CPU_LINUX_X64`
462.libquantum: `-DSPEC_CPU_LINUX`
483.xalancbmk: `-DSPEC_CPU_LINUX`

Base Optimization Flags

C benchmarks:
`-fast`

C++ benchmarks:
`-xP -O3 -ipo -no-prec-div -L/opt/SmartHeap_8_1/lib -lsmartheap`

Peak Compiler Invocation

C benchmarks (except as noted below):
`icc`

401.bzip2: `/opt/intel/cce/9.1.047/bin/icc -I/opt/intel/cce/9.1.047/include -L/opt/intel/cce/9.1.047/lib`

456.hmmer: `/opt/intel/cce/9.1.047/bin/icc -I/opt/intel/cce/9.1.047/include -L/opt/intel/cce/9.1.047/lib`

462.libquantum: `/opt/intel/cce/9.1.047/bin/icc -I/opt/intel/cce/9.1.047/include -L/opt/intel/cce/9.1.047/lib`

C++ benchmarks:
`icpc`

Peak Portability Flags

400.perlbench: `-DSPEC_CPU_LINUX_X64`
401.bzip2: `-DSPEC_CPU_LP64`

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Peak Portability Flags (Continued)

456.hmmer: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -prof_gen(pass 1) -prof_use(pass 2) -fast

401.bzip2: -fast

403.gcc: basepeak = yes

429.mcf: -prof_gen(pass 1) -prof_use(pass 2) -fast -L/opt/SmartHeap_8_1/lib -lsmartheap

445.gobmk: Same as 429.mcf

456.hmmer: Same as 400.perlbench

458.sjeng: Same as 429.mcf

462.libquantum: Same as 400.perlbench

464.h264ref: Same as 429.mcf

C++ benchmarks:

471.omnetpp: -prof_gen(pass 1) -prof_use(pass 2) -xP -O3 -ipo -no-prec-div -L/opt/SmartHeap_8_1/lib -lsmartheap

473.astar: -prof_gen(pass 1) -prof_use(pass 2) -fast -L/opt/SmartHeap_8_1/lib -lsmartheap

483.xalancbmk: basepeak = yes

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/CPU2006_flags.20090714.09.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/CPU2006_flags.20090714.09.xml

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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