SPEC® CINT2006 Result

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Dell Inc.

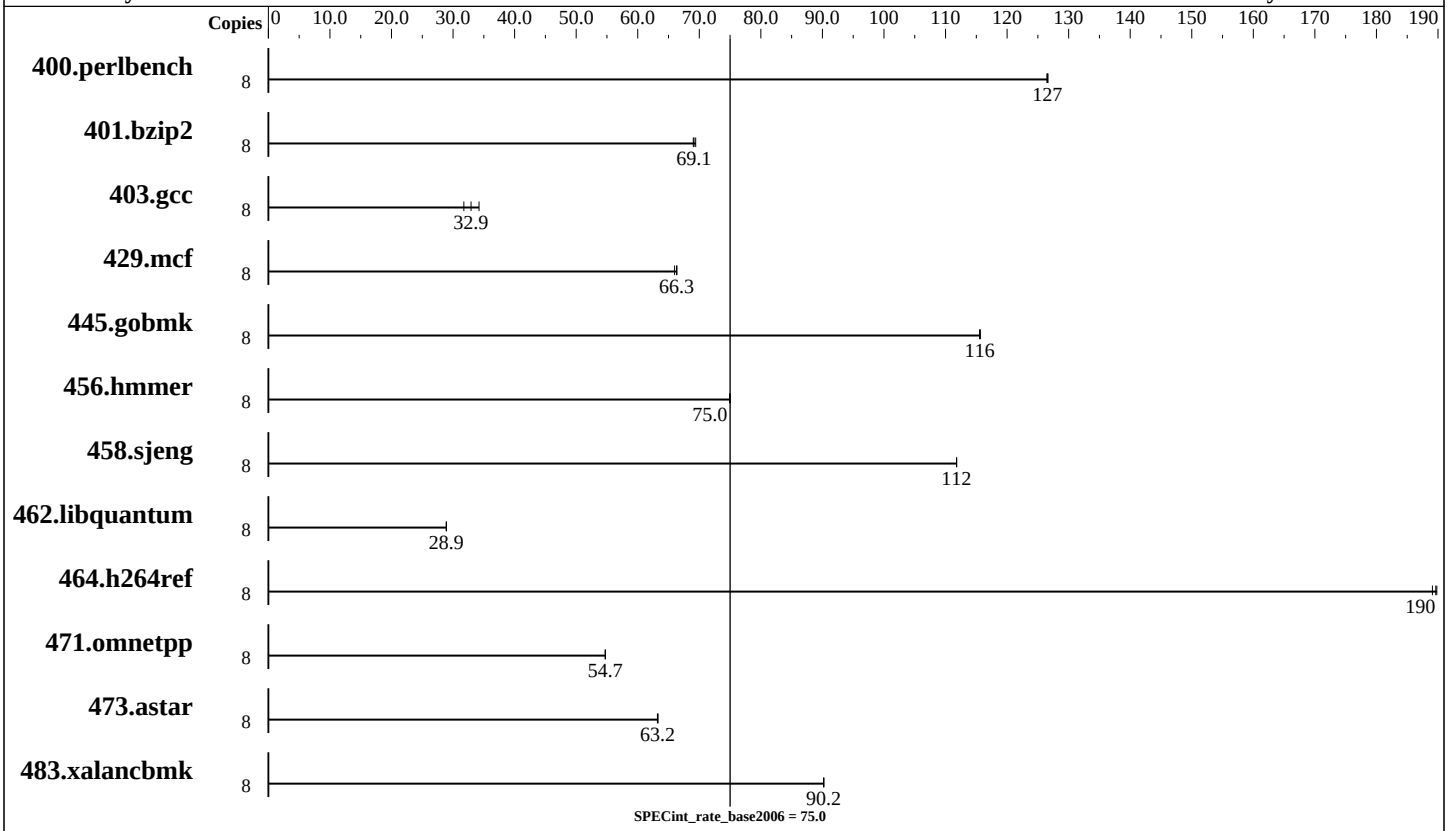
PowerEdge 1950 (Intel Xeon processor E5345, 2.33 GHz)

SPECint®_rate2006 = Not Run

SPECint_rate_base2006 = 75.0

CPU2006 license: 55
Test sponsor: Dell Inc.
Tested by: Dell Inc.

Test date: Mar-2007
Hardware Availability: Dec-2006
Software Availability: Nov-2006



Hardware

CPU Name:	Intel Xeon E5345
CPU Characteristics:	1333 MHz Bus Speed
CPU MHz:	2333
FPU:	Integrated
CPU(s) enabled:	8 cores, 2 chips, 4 cores/chip
CPU(s) orderable:	1,2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	8 MB I+D on chip per chip, 4 MB shared / 2 cores
L3 Cache:	None
Other Cache:	None
Memory:	16 GB (8 x 2 GB 667MHz CL5 DDR2 FB-DIMM SDRAM)
Disk Subsystem:	1 x 73 GB SAS 15k RPM
Other Hardware:	None

Software

Operating System:	Microsoft Windows Server 2003 Enterprise x64 Edition + SP1 (64-bit)
Compiler:	Intel C++ Compiler 9.1 for IA32 (20061103Z) Microsoft Visual Studio 2005
Auto Parallel:	No
File System:	NTFS
System State:	Default
Base Pointers:	32-bit
Peak Pointers:	Not Applicable
Other Software:	MicroQuill SmartHeap Library 8.0

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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	8	617	127	**618**	**127**	618	126							
401.bzip2	8	**1116**	**69.1**	1112	69.4	1118	69.0							
403.gcc	8	2028	31.8	1882	34.2	**1956**	**32.9**							
429.mcf	8	**1100**	**66.3**	1106	66.0	1099	66.4							
445.gobmk	8	726	116	726	116	**726**	**116**							
456.hmmer	8	995	75.0	**996**	**75.0**	996	74.9							
458.sjeng	8	**866**	**112**	866	112	866	112							
462.libquantum	8	**5732**	**28.9**	5729	28.9	5739	28.9							
464.h264ref	8	**934**	**190**	936	189	933	190							
471.omnetpp	8	**914**	**54.7**	913	54.7	914	54.7							
473.astar	8	887	63.3	889	63.2	**888**	**63.2**							
483.xalancbmk	8	612	90.2	612	90.2	**612**	**90.2**							

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Platform Notes

```
Hardware Prefetcher disabled in BIOS
Adjacent Cache Line Prefetch disabled in BIOS
```

Base Compiler Invocation

C benchmarks:

```
icl -Qc99
```

C++ benchmarks:

```
icl
```

Base Portability Flags

403.gcc: `-DSPEC_CPU_WIN32`
464.h264ref: `-DSPEC_CPU_NO_INTTYPES -DWIN32`
483.xalancbmk: `-Qoption,cpp,--no_wchar_t_keyword`

Base Optimization Flags

C benchmarks:

```
-fast /F512000000 shlW32M.lib           -link /FORCE:MULTIPLE
```

Continued on next page

Base Optimization Flags (Continued)

C++ benchmarks:
```
-fast -Qcxx_features /F512000000 shlW32M.lib
-link /FORCE:MULTIPLE
```

Base Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

The flags file that was used to format this result can be browsed at
`http://www.spec.org/cpu2006/flags/dell.cpu2006.ic91.flags.html`

You can also download the XML flags source by saving the following link:
`http://www.spec.org/cpu2006/flags/dell.cpu2006.ic91.flags.xml`

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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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