SPEC® CINT2006 Result

Dell Inc.

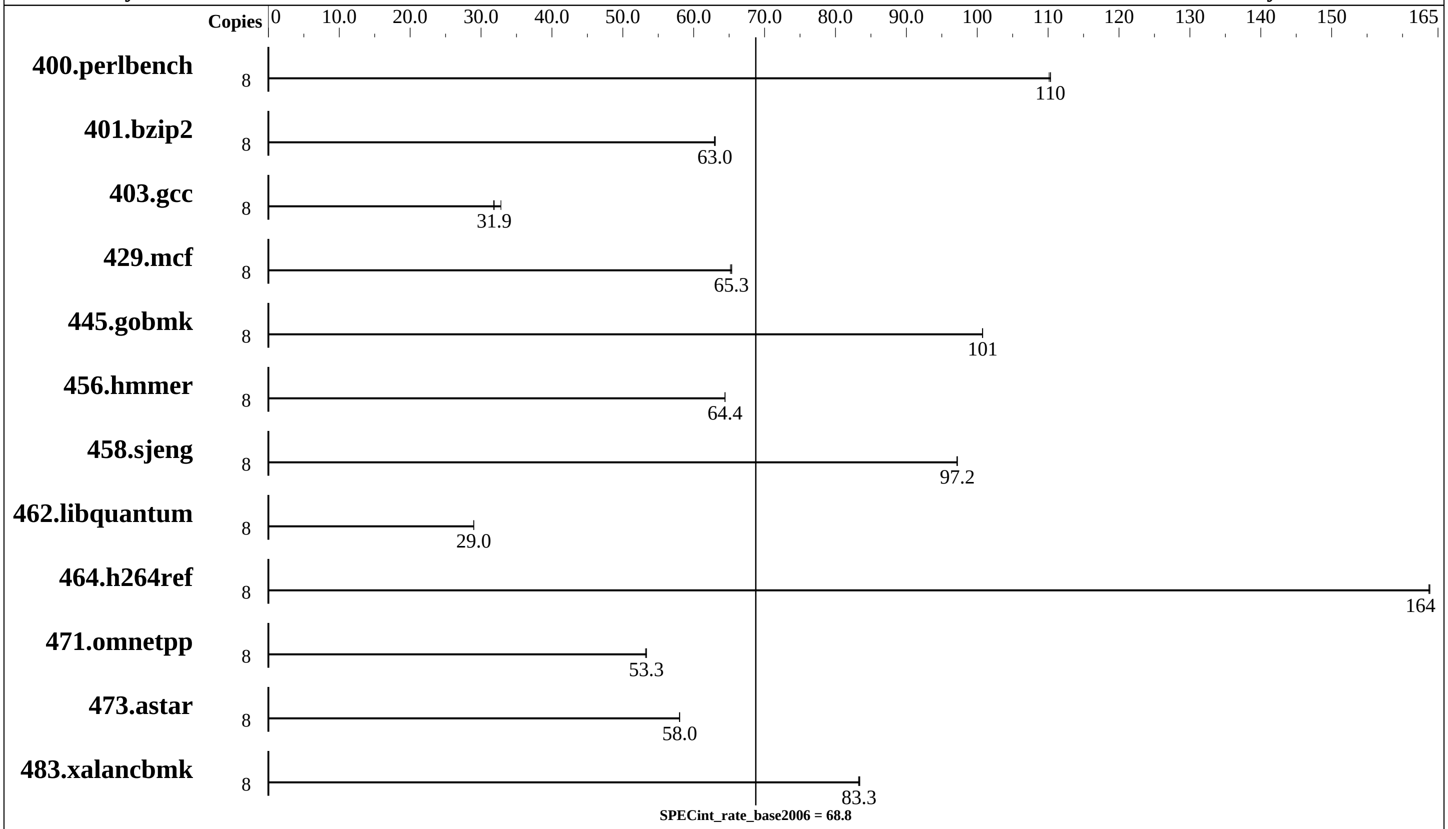
PowerEdge 2950 (Intel Xeon processor E5335, 2.00 GHz)

SPECint®_rate2006 = Not Run

SPECint_rate_base2006 = 68.8

CPU2006 license: 55
Test sponsor: Dell Inc.
Tested by: Dell Inc.

Test date: Mar-2007
Hardware Availability: Dec-2006
Software Availability: Nov-2006



Hardware

CPU Name:	Intel Xeon E5335
CPU Characteristics:	1333 MHz Bus Speed
CPU MHz:	2000
FPU:	Integrated
CPU(s) enabled:	8 cores, 2 chips, 4 cores/chip
CPU(s) orderable:	1,2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	8 MB I+D on chip per chip, 4 MB shared / 2 cores
L3 Cache:	None
Other Cache:	None
Memory:	16 GB (8 x 2 GB 667MHz CL5 DDR2 FB-DIMM SDRAM)
Disk Subsystem:	1 x 73 GB SAS 15k RPM
Other Hardware:	None

Software

Operating System:	Microsoft Windows Server 2003 Enterprise x64 Edition + SP1 (64-bit)
Compiler:	Intel C++ Compiler 9.1 for IA32 (20061103Z) Microsoft Visual Studio 2005
Auto Parallel:	No
File System:	NTFS
System State:	Default
Base Pointers:	32-bit
Peak Pointers:	Not Applicable
Other Software:	MicroQuill SmartHeap Library 8.0

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Results Table

	Base							Peak						
Benchmark	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	8	**708**	**110**	708	110	710	110							
401.bzip2	8	1227	62.9	1224	63.1	**1225**	**63.0**							
403.gcc	8	2027	31.8	1963	32.8	**2021**	**31.9**							
429.mcf	8	1119	65.2	**1117**	**65.3**	1116	65.4							
445.gobmk	8	833	101	833	101	**833**	**101**							
456.hmmer	8	1158	64.4	1159	64.4	**1159**	**64.4**							
458.sjeng	8	**996**	**97.2**	996	97.2	997	97.1							
462.libquantum	8	**5722**	**29.0**	5723	29.0	5719	29.0							
464.h264ref	8	**1081**	**164**	1080	164	1081	164							
471.omnetpp	8	937	53.3	**938**	**53.3**	940	53.2							
473.astar	8	968	58.0	**968**	**58.0**	969	58.0							
483.xalancbmk	8	**662**	**83.3**	662	83.4	663	83.3							

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Platform Notes

```
Hardware Prefetcher disabled in BIOS
Adjacent Cache Line Prefetch disabled in BIOS
```

Base Compiler Invocation

C benchmarks:
`icl -Qc99`

C++ benchmarks:
`icl`

Base Portability Flags

403.gcc: `-DSPEC_CPU_WIN32`
464.h264ref: `-DSPEC_CPU_NO_INTTYPES -DWIN32`
483.xalancbmk: `-Qoption,cpp,--no_wchar_t_keyword`

Base Optimization Flags

C benchmarks:
`-fast /F512000000 shlW32M.lib -link /FORCE:MULTIPLE`

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Base Optimization Flags (Continued)

C++ benchmarks:

```
-fast -Qcxx_features /F512000000 shlW32M.lib
      -link /FORCE:MULTIPLE
```

Base Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/dell.cpu2006.ic91.flags.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/dell.cpu2006.ic91.flags.xml