



SPEC® CINT2006 Result

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Dell Inc.

PowerEdge 2950 (Intel Xeon processor 5120,
1.86GHz)

SPECint_rate2006 = Not Run

SPECint_rate_base2006 = 37.9

CPU2006 license: 55

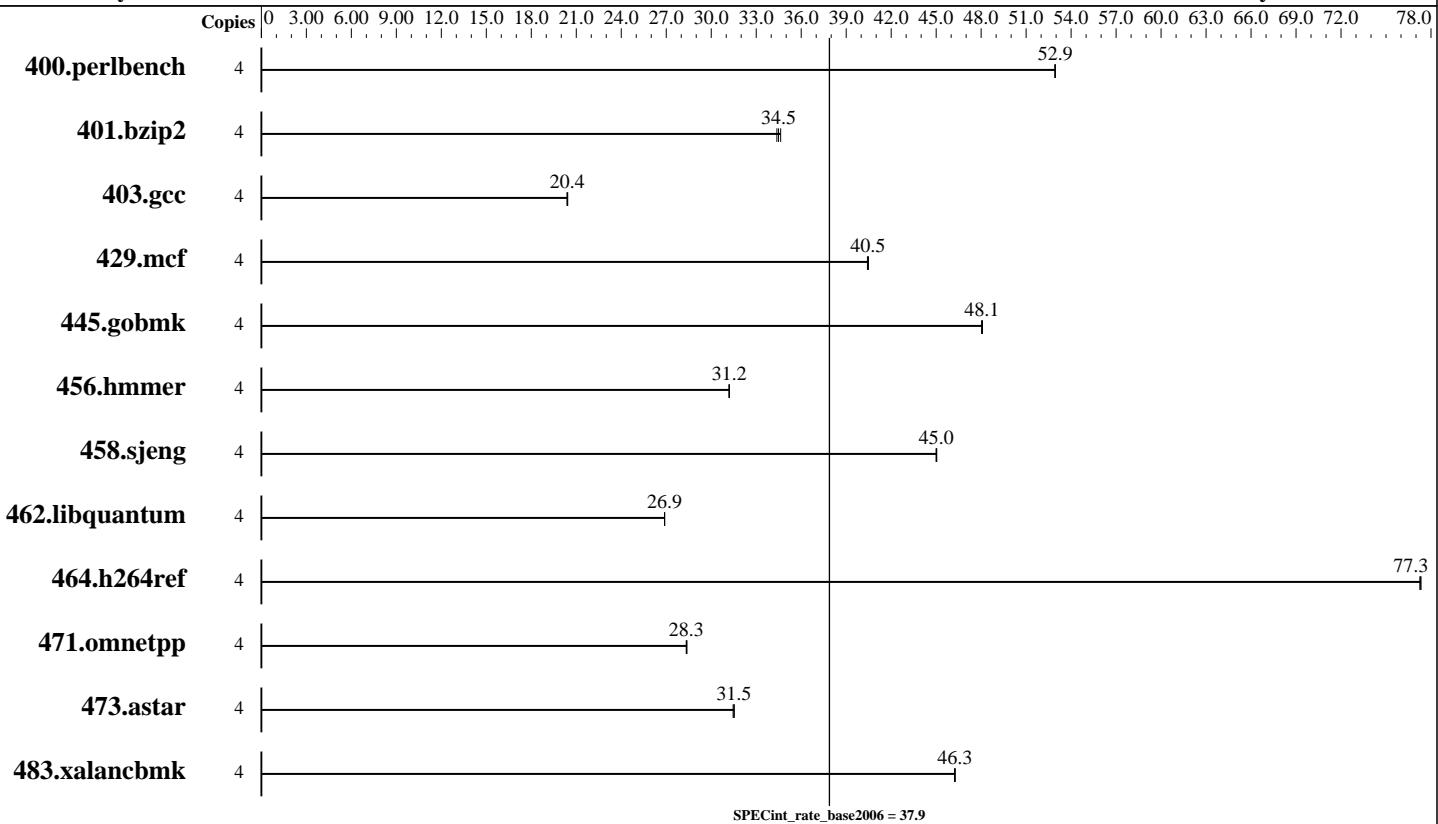
Test sponsor: Dell Inc.

Tested by: Dell Inc.

Test date: Mar-2007

Hardware Availability: Jul-2006

Software Availability: Nov-2006



Hardware

CPU Name: Intel Xeon 5120
 CPU Characteristics: 1066 MHz Bus Speed
 CPU MHz: 1866
 FPU: Integrated
 CPU(s) enabled: 4 cores, 2 chips, 2 cores/chip
 CPU(s) orderable: 1,2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 4 MB I+D on chip per chip
 L3 Cache: None
 Other Cache: None
 Memory: 16 GB (8 x 2 GB 667MHz CL5 DDR2 FB-DIMM SDRAM)
 Disk Subsystem: 1 x 73 GB SAS 15k RPM
 Other Hardware: None

Software

Operating System: Microsoft Windows Server 2003 Enterprise x64 Edition + SP1 (64-bit)
 Compiler: Intel C++ Compiler 9.1 for IA32 (20061103Z)
 Microsoft Visual Studio 2005
 Auto Parallel: No
 File System: NTFS
 System State: Default
 Base Pointers: 32-bit
 Peak Pointers: Not Applicable
 Other Software: MicroQuill SmartHeap Library 8.0



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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	4	739	52.9	738	52.9	738	52.9							
401.bzip2	4	1115	34.6	1123	34.4	1119	34.5							
403.gcc	4	1579	20.4	1576	20.4	1579	20.4							
429.mcf	4	901	40.5	902	40.5	903	40.4							
445.gobmk	4	873	48.1	873	48.1	874	48.0							
456.hmmer	4	1196	31.2	1196	31.2	1196	31.2							
458.sjeng	4	1076	45.0	1075	45.0	1075	45.0							
462.libquantum	4	3082	26.9	3082	26.9	3082	26.9							
464.h264ref	4	1146	77.3	1145	77.3	1145	77.3							
471.omnetpp	4	882	28.3	881	28.4	882	28.3							
473.astar	4	890	31.5	893	31.5	891	31.5							
483.xalancbmk	4	597	46.2	597	46.3	597	46.3							

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Platform Notes

Snoop Filter enabled in BIOS

General Notes

32-bit binaries were built on Windows XP Professional x64 Edition

Base Compiler Invocation

C benchmarks:

 icl -Qc99

C++ benchmarks:

 icl

Base Portability Flags

403.gcc: -DSPEC_CPU_WIN32

464.h264ref: -DSPEC_CPU_NO_INTTYPES -DWIN32

483.xalancbmk: -Qoption,cpp, --no_wchar_t_keyword



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Base Optimization Flags

C benchmarks:

-fast /F512000000 shlw32M.lib

-link /FORCE:MULTIPLE

C++ benchmarks:

-fast -Qcxx_features /F512000000 shlw32M.lib
-link /FORCE:MULTIPLE

Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/dell.cpu2006.ic91.flags.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/dell.cpu2006.ic91.flags.xml>

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For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.0.

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