### SPEC® CFP2006 Result

**Fujitsu Siemens Computers**

PRIMERGY TX600 S3, Intel Xeon processor 7140M, 3.40 GHz

<table>
<thead>
<tr>
<th>CPU2006 license: 22</th>
<th>Test date: Apr-2007</th>
</tr>
</thead>
<tbody>
<tr>
<td>Test sponsor: Fujitsu Siemens Computers</td>
<td>Hardware Availability: Dec-2006</td>
</tr>
<tr>
<td>Tested by: Fujitsu Siemens Computers</td>
<td>Software Availability: Feb-2007</td>
</tr>
</tbody>
</table>

**SPECfp®_rate2006 = 39.0**

| SPECfp_rate_base2006 = 37.9 |

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<table>
<thead>
<tr>
<th>Software</th>
<th>Hardware</th>
</tr>
</thead>
<tbody>
<tr>
<td>Operating System: 64-Bit SUSE LINUX Enterprise Server 10, Kernel 2.6.16.21-0.8-smp on an x86_64</td>
<td>CPU Name: Intel Xeon 7140M</td>
</tr>
<tr>
<td>Auto Parallel: No</td>
<td>FPU: Integrated</td>
</tr>
<tr>
<td>File System: ReiserFS</td>
<td>CPU(s) enabled: 4 cores, 2 chips, 2 cores/chip</td>
</tr>
<tr>
<td></td>
<td>CPU(s) orderable: 1,2,4 chips</td>
</tr>
<tr>
<td></td>
<td>Primary Cache: 12 K micro-ops I + 16 KB D on chip per core</td>
</tr>
<tr>
<td></td>
<td>Secondary Cache: 1 MB I+D on chip per core</td>
</tr>
</tbody>
</table>

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Copies

| SPECfp_rate2006 = 39.0 |

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| SPECfp_rate_base2006 = 37.9; |

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L3 Cache: 16 MB I+D on chip per chip
Other Cache: None
Memory: 32 GB (16x2 GB DDR2 PC2-3200R, 2 rank, CAS 3-3-3, with ECC)
Disk Subsystem: Fujitsu MAS3735NC (SCSI 73GB 15 krpm)
Other Hardware: None

System State: Multiuser, Runlevel 3
Base Pointers: 64-bit
Peak Pointers: 32/64-bit
Other Software: None

Results Table

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Copies</th>
<th>Base</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
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<th>Seconds</th>
<th>Ratio</th>
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<tbody>
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<td>73.9</td>
<td>1055</td>
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</tr>
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</table>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run '/usr/bin/taskset' used to bind processes to CPUs

General Notes

The system bus runs at 800 MHz

All binaries were built with 64-bit Intel compiler except:
433.milc, 434.zeusmp, 450.soplex, 470.lbm and 482.sphinx3 in peak were built with 32-bit Intel compiler by changing the path for include and library files.

BIOS configuration:

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Fujitsu Siemens Computers

3.40 GHz
PRIMERGY TX600 S3, Intel Xeon processor 7140M,

SPECfp_rate2006 = 39.0
SPECfp_rate_base2006 = 37.9

General Notes (Continued)

Hardware Prefetch = Enable, Adjacent Sector Prefetch = Enable

This result was measured on the PRIMERGY RX600 S3. The PRIMERGY RX600 S3 and the PRIMERGY TX600 S3 are electronically equivalent.

For information about Fujitsu Siemens Computers in your country please see: http://www.fujitsu-siemens.com/countries

Base Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc

Fortran benchmarks:
ifort

Benchmarks using both Fortran and C:
icc ifort

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
465.tonto: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:
-fast
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Base Optimization Flags (Continued)

C++ benchmarks:
- fast

Fortran benchmarks:
- fast

Benchmarks using both Fortran and C:
- fast

Peak Compiler Invocation

C benchmarks:
/opt/intel/cc/9.1.047/bin/icc -I/opt/intel/cc/9.1.047/include -L/opt/intel/cc/9.1.047/lib

C++ benchmarks (except as noted below):
icpc
450.soplex: /opt/intel/cc/9.1.047/bin/icpc
- I/opt/intel/cc/9.1.047/include -L/opt/intel/cc/9.1.047/lib

Fortran benchmarks (except as noted below):
ifort
434.zeusmp: /opt/intel/fc/9.1.043/bin/ifort
- I/opt/intel/fc/9.1.043/include -L/opt/intel/fc/9.1.043/lib

Benchmarks using both Fortran and C:
icc ifort

Peak Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.games: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
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454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
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Peak Optimization Flags

C benchmarks:
433.milc: -prof_gen(pass 1) -prof_use(pass 2) -fast
470.lbm: Same as 433.milc
482.sphinx3: -fast

C++ benchmarks:
444.namd: basepeak = yes
447.dealII: -prof_gen(pass 1) -prof_use(pass 2) -fast
450.soplex: Same as 447.dealII
453.povray: Same as 447.dealII

Fortran benchmarks:
410.bwaves: basepeak = yes
416.gamess: basepeak = yes
434.zeusmp: -fast
437.leslie3d: basepeak = yes
459.GemsFDTD: basepeak = yes
465.tonto: -prof_gen(pass 1) -prof_use(pass 2) -fast

Benchmarks using both Fortran and C:
435.gromacs: -prof_gen(pass 1) -prof_use(pass 2) -fast
436.cactusADM: basepeak = yes
454.calculix: Same as 435.gromacs
481.wrf: basepeak = yes

The flags file that was used to format this result can be browsed at

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/CPU2006_flags.20090714.09.xml
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