Bull SAS
NovaScale R440
(Intel Xeon processor E5310, 1.60GHz)

SPECint®2006 = 10.5
SPECint_base2006 = 10.1

CPU2006 license: 20
Test sponsor: Bull SAS
Tested by: Bull SAS

Hardware
CPU Name: Intel Xeon E5310
CPU Characteristics: 1.60 GHz, 8MB L2, 1066MHz bus
CPU MHz: 1600
FPU: Integrated
CPU(s) enabled: 1 core, 1 chip, 4 cores/chip
CPU(s) orderable: 1 to 2 chips
Primary Cache: 32 KB I + 32 KB D on chip per core
Secondary Cache: 8 MB L1+D on chip per core, 4 MB shared / 2 cores
L3 Cache: None
Other Cache: None
Memory: 12 GB (1GB DIMMs, FB-DIMM PC2-5300F ECC CL5)
Disk Subsystem: 73 GB SAS, 10000RPM
Other Hardware: None

Software
Compiler: Intel C++ Compiler for IA32 version 9.1
Package ID W_CC_C_9.1.033 Build no 20061103Z
Microsoft Visual Studio .NET 2003 (lib & linker)
Auto Parallel: No
File System: NTFS
System State: Default
Base Pointers: 32-bit
Peak Pointers: 32-bit
Other Software: MicroQuill SmartHeap Library 8.0 (shlW32M.lib)

Software Availability: Mar-2007
Hardware Availability: Dec-2006
Test date: Apr-2007
Tested by: Bull SAS
Hardware: Bull SAS
Software: Bull SAS

400.perlbm
401.bzip2
403.gcc
429.mcf
445.gobmk
456 hmm
458 sjeng
462.libquantum
464.h264ref
471.omnetpp
473.astar
483.xalancbmk

Specify CINT2006 Result
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(Intel Xeon processor E5310, 1.60GHz)

SPECint2006 = 10.5
SPECint_base2006 = 10.1

Results Table

<table>
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<tr>
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</tbody>
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Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes
/NUMPROC=1 flag was added to boot.ini to invoke uniprocessor environment

General Notes
The NovaScale R440 and the NovaScale R460 models are electronically equivalent.
The results have been measured on a NovaScale R460 model.

Base Compiler Invocation
C benchmarks: icl -Qvc7.1 -Qc99
C++ benchmarks: icl -Qvc7.1
SPEC CINT2006 Result

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Base Portability Flags
403.gcc: -DSPEC_CPU_WIN32
464.h264ref: -DSPEC_CPU_NO_INTTYPES -DWIN32

Base Optimization Flags
C benchmarks:
- fast /F512000000 shlw32m.lib
- link /FORCE:MULTIPLE

C++ benchmarks:
- fast -Qcxx_features /F512000000 shlw32m.lib
- link /FORCE:MULTIPLE

Base Other Flags
C benchmarks:
403.gcc: -Dalloca=_alloca

Peak Compiler Invocation
C benchmarks:
icl -Qvc7.1 -Qc99
C++ benchmarks:
icl -Qvc7.1

Peak Portability Flags
403.gcc: -DSPEC_CPU_WIN32
464.h264ref: -DSPEC_CPU_NO_INTTYPES -DWIN32

Peak Optimization Flags
C benchmarks:
- Qprof_gen(pass 1) -Qprof_use(pass 2) -fast /F512000000 shlw32m.lib
- link /FORCE:MULTIPLE

C++ benchmarks:
- Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qcxx_features
  /F512000000 shlw32m.lib
  - link /FORCE:MULTIPLE
# Bull SAS

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**Test date:** Apr-2007  
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**Software Availability:** Dec-2006

## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at  
http://www.spec.org/cpu2006/flags/flags.20090714.00.html

You can also download the XML flags source by saving the following link:  
http://www.spec.org/cpu2006/flags/flags.20090714.00.xml

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For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.0.  
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