Acer Incorporated

Acer Altos R520 (Intel Xeon X5355, 2.66GHz)

**SPECint_rate2006 = 85.0**

**SPECint_rate_base2006 = 79.4**

**CPU2006 license:** 97

**Test sponsor:** Acer Incorporated

**Tested by:** Acer Incorporated

**Test date:** Mar-2007

**Hardware Availability:** Nov-2006

**Software Availability:** Jul-2006

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### Hardware

- **CPU Name:** Intel Xeon X5355
- **CPU Characteristics:** 1333MHz system bus
- **CPU MHz:** 2666
- **FPU:** Integrated
- **CPU(s) enabled:** 8 cores, 2 chips, 4 cores/chip
- **CPU(s) orderable:** 1.2 chips
- **Primary Cache:** 32 KB I + 32 KB D on chip per core
- **Secondary Cache:** 8 MB I+D on chip per chip, 4 MB shared / 2 cores
- **L3 Cache:** None
- **Other Cache:** None
- **Memory:** 16 GB (8 x 2048MB ECC FR-DIMM DDR2-667 CL5-5-5-5)
- **Disk Subsystem:** 1 x 73GB 10000RPM SAS HDD
- **Other Hardware:** None

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### Software

- **Operating System:** Microsoft Windows Server 2003 Enterprise x64 Edition (Build 3790), Service Pack 1
- **Compiler:** Intel C++ Compiler for IA32 version 9.1
- **File System:** NTFS
- **System State:** Default
- **Base Pointers:** 32-bit
- **Peak Pointers:** 32-bit
- **Auto Parallel:** No
- **Other Software:** MicroQuill Smart Heap Library, Version 7.4
## SPEC CINT2006 Result

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### Results Table

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</table>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

### General Notes

Disable hardware prefetching and disable adjacent cache line prefetch BIOS setting

This result was measured on the Acer Altos R520.  
The Altos R720 and Altos R520 are electronically equivalent.

### Base Compiler Invocation

C benchmarks:  
```bash  
icl -Qvc7.1 -Qc99  
```

C++ benchmarks:  
```bash  
icl -Qvc7.1  
```

### Base Portability Flags

403.gcc: `-DSPEC_CPU_WIN32`  
464.h264ref: `-DSPEC_CPU_NO_INTTYPES -DWIN32`
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Base Optimization Flags

C benchmarks:
- fast /F512000000 shlw32m.lib  
  -link /FORCE:MULTIPLE

C++ benchmarks:
- fast -Qcxx_features /F512000000 shlw32m.lib  
  -link /FORCE:MULTIPLE

Base Other Flags

C benchmarks:
403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks:
icl -Qvc7.1 -Qc99

C++ benchmarks:
icl -Qvc7.1

Peak Portability Flags

403.gcc: -DSPEC_CPU_WIN32
464.h264ref: -DSPEC_CPU_NO_INTTYPES -DWIN32

Peak Optimization Flags

C benchmarks:
400.perlbench: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast /F512000000 
  shlw32m.lib  
  -link /FORCE:MULTIPLE

401.bzip2: Same as 400.perlbench

403.gcc: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast /F512000000 
  -link /FORCE:MULTIPLE

429.mcf: basepeak = yes

445.gobmk: Same as 400.perlbench

Continued on next page
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Peak Optimization Flags (Continued)

456.hmmer: Same as 400.perlbench
458.sjeng: Same as 400.perlbench
462.libquantum: Same as 400.perlbench
464.h264ref: Same as 400.perlbench

C++ benchmarks:

471.omnetpp: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qcxx_features
/F512000000 shlw32m.lib -link /FORCE:MULTIPLE

473.astar: basepeak = yes
483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Acer-CPU2006-ic91-flags.20090714.00.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/Acer-CPU2006-ic91-flags.20090714.00.xml

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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.0.