Supermicro
Motherboard X7DB8

**SPECint\_rate2006 = 81.5**

**SPECint\_rate_base2006 = 79.1**

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**CPU2006 license:** 001176  
**Test date:** Apr-2007

**Test sponsor:** Supermicro  
**Hardware Availability:** May-2007

**Tested by:** Supermicro  
**Software Availability:** Apr-2007

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### Hardware

- **CPU Name:** Intel Xeon X5355
- **CPU Characteristics:** 2.66GHz, 4x2 MB L2 shared, 1333 MHz bus
- **CPU MHz:** 2660
- **FPU:** Integrated
- **CPU(s) enabled:** 8 cores, 2 chips, 4 cores/chip
- **CPU(s) orderable:** 1, 2 chips
- **Primary Cache:** 32 KB I + 32 KB D on chip per core
- **Secondary Cache:** 8 MB I+D on chip per core, 4 MB shared / 2 cores
- **L3 Cache:** None
- **Other Cache:** None
- **Memory:** 16 GB (8 X 2GB ECC. CL5, FBDIMM)
- **Disk Subsystem:** 750GB IDE, 7200RPM
- **Other Hardware:** None

### Software

- **Operating System:** Windows Server 2003 Enterprise Edition W/ SP1 Build no 20070322Z
- **Compiler:** Intel C++ Compiler for IA32 version 9.1
- **Auto Parallel:** No
- **File System:** NTFS
- **System State:** Default
- **Base Pointers:** 32-bit
- **Peak Pointers:** 32-bit
- **Other Software:** SmartHeap Library Version 8.0 from http://www.microquill.com/
SPEC CINT2006 Result

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Results Table

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</table>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

Tested systems can be used with SC816S-R700 case,
To ensure system stability, a 500W (minimum) ATX power supply [4-pin (+12V), 8-pin (+12V) and 24-pin are required]
The system bus runs at 1333 MHz

Base Compiler Invocation

C benchmarks:
icl -Qvc7.1 -Qc99

C++ benchmarks:
icl -Qvc7.1

Base Portability Flags

403.gcc: -DSPEC_CPU_WIN32
464.h264ref: -DSPEC_CPU_NO_INTTYPES -DWIN32

Base Optimization Flags

C benchmarks:
-fast /F512000000 shlw32m.lib -link /FORCE:MULTIPLE
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Base Optimization Flags (Continued)

C++ benchmarks:
- fast -Qcxx_features /F512000000 shlw32m.lib
  -link /FORCE:MULTIPLE

Base Other Flags

C benchmarks:
403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks:
icl -Qvc7.1 -Qc99

C++ benchmarks:
icl -Qvc7.1

Peak Portability Flags

403.gcc: -DSPEC_CPU_WIN32
464.h264ref: -DSPEC_CPU_NO_INTTYPES -DWIN32

Peak Optimization Flags

C benchmarks:
400.perlbench: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast /F512000000
  shlw32m.lib -link /FORCE:MULTIPLE
  401.bzip2: Same as 400.perlbench
  403.gcc: Same as 400.perlbench
  429.mcf: basepeak = yes
  445.gobmk: Same as 400.perlbench
  456.hmmer: Same as 400.perlbench
  458.sjeng: Same as 400.perlbench

Continued on next page
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Peak Optimization Flags (Continued)

462.libquantum: Same as 400.perlbench
464.h264ref: Same as 400.perlbench

C++ benchmarks:

471.omnetpp: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qcxx_features
            /F512000000 shlw32m.lib -link /FORCE:MULTIPLE

473.astar: -Qprof_gen(pass 1) -Qprof_use(pass 2) -QxP -O2 -Qipo
           -Qprec-div -Qunroll4 -Ob2 -Qsfalign16 -Qcxx_features
           /F512000000 shlw32m.lib -link /FORCE:MULTIPLE

483.xalancbmk: Same as 471.omnetpp

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic91-ia32-flags.html
You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/Intel-ic91-ia32-flags.xml

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For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.0.
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