Supermicro
Motherboard PDSM4+

SPECint_rate2006 = 19.2
SPECint_rate_base2006 = 18.4

Software

Operating System: Windows XP Professional w/ SP2
Compiler: Intel C++ Compiler for IA32 version 9.1 Build no 20070322Z
Auto Parallel: No
File System: NTFS
System State: Default
Base Pointers: 32-bit
Peak Pointers: 32-bit
Other Software: SmartHeap Library Version 8.0 from http://www.microquill.com/

Hardware

CPU Name: Intel Core 2 Duo E4300
CPU Characteristics: 1.8GHz 800MHz bus
CPU MHz: 1800
FPU: Integrated
CPU(s) enabled: 2 cores, 1 chip, 2 cores/chip
CPU(s) orderable: 1 chip
Primary Cache: 32 KB I + 32 KB D on chip per core
Secondary Cache: 2 MB I+D on chip per chip
L3 Cache: None
Other Caches: None
Memory: 2 GB (4 X 512MB, DDR2 667MHz, CL5, ECC)
Disk Subsystem: WD2500YS-01S HB1 250GB SATA II, 7200RPM
Other Hardware: None

Tests:

- 400.perlbench
- 401.bzip2
- 403.gcc
- 429.mcf
- 445.gobmk
- 456.hmmer
- 458.sjeng
- 462.libquantum
- 464.h264ref
- 471.omnetpp
- 473.astar
- 483.xalancbmk

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Results Table

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</table>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

Tested systems can be used with CSE-823S-R500LP case, for a general system, a 420W (minimum) ATX12V power supply [8-pin +12V AND 24-pin is recommended to assure system stability].


The system bus runs at 800 MHz

Base Compiler Invocation

C benchmarks:
icl -Qvc7.1 -Qc99

C++ benchmarks:
icl -Qvc7.1

Base Portability Flags

403.gcc: -DSPEC_CPU_WIN32
464.h264ref: -DSPEC_CPU_NO_INTTYPES -DWIN32

Base Optimization Flags

C benchmarks:
-fast /F512000000 shlw32m.lib -link /FORCE:MULTIPLE

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**Supermicro Motherboard PDSM4+**

**SPECint_rate2006 = 19.2**  
**SPECint_rate_base2006 = 18.4**

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<td>Software Availability:</td>
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</table>

**Base Optimization Flags (Continued)**

C++ benchmarks:
- `-fast -Qcxx_features /F512000000 shlw32m.lib`
- `-link /FORCE:MULTIPLE`

**Base Other Flags**

C benchmarks:
- `403.gcc: -Dalloca=_alloca`

**Peak Compiler Invocation**

C benchmarks:
- `icl -Qvc7.1 -Qc99`

C++ benchmarks:
- `icl -Qvc7.1`

**Peak Portability Flags**

403.gcc: `-DSPEC_CPU_WIN32`
464.h264ref: `-DSPEC_CPU_NO_INTTYPES -DWIN32`

**Peak Optimization Flags**

C benchmarks:
- `400.perlbench: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast /F512000000 shlw32m.lib -link /FORCE:MULTIPLE`
  - `401.bzip2: Same as 400.perlbench`
  - `403.gcc: Same as 400.perlbench`
  - `429.mcf: basepeak = yes`
  - `445.gobmk: Same as 400.perlbench`
  - `456.hmmer: Same as 400.perlbench`
  - `458.sjeng: Same as 400.perlbench`

Continued on next page
Peak Optimization Flags (Continued)

462.libquantum: Same as 400.perlbench
464.h264ref: Same as 400.perlbench

C++ benchmarks:

471.omnetpp: -O prof-gen(pass 1) -O prof-use(pass 2) -fast -Qcx features
/F512000000 shlw32m.lib -link /FORCE:MULTIPLE

473.astar: -O prof-gen(pass 1) -O prof-use(pass 2) -QXP -O2 -Qipo
-Qprec-div -Qunroll4 -Ob2 -Qsfalign16 -Qcx features
/F512000000 shlw32m.lib -link /FORCE:MULTIPLE

483.xalancbmk: Same as 471.omnetpp

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at http://www.spec.org/cpu2006/flags/Intel-ic91-ia32-flags.html
You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/Intel-ic91-ia32-flags.xml

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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.0.