Fujitsu Siemens Computers
PRIMERGY TX300 S3, Intel Xeon processor 5120, 1.86 GHz

<table>
<thead>
<tr>
<th>Copied</th>
<th>SPECfp_rate2006</th>
<th>SPECfp_rate_base2006</th>
</tr>
</thead>
<tbody>
<tr>
<td>3.00</td>
<td>32.3</td>
<td>31.3</td>
</tr>
<tr>
<td>6.00</td>
<td></td>
<td></td>
</tr>
<tr>
<td>9.00</td>
<td></td>
<td></td>
</tr>
<tr>
<td>12.0</td>
<td></td>
<td></td>
</tr>
<tr>
<td>15.0</td>
<td></td>
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<tr>
<td>18.0</td>
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<tr>
<td>21.0</td>
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<tr>
<td>24.0</td>
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<tr>
<td>27.0</td>
<td></td>
<td></td>
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<tr>
<td>30.0</td>
<td></td>
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<tr>
<td>33.0</td>
<td></td>
<td></td>
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<tr>
<td>36.0</td>
<td></td>
<td></td>
</tr>
<tr>
<td>39.0</td>
<td></td>
<td></td>
</tr>
<tr>
<td>42.0</td>
<td></td>
<td></td>
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<tr>
<td>45.0</td>
<td></td>
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<tr>
<td>48.0</td>
<td></td>
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<tr>
<td>51.0</td>
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<tr>
<td>54.0</td>
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<tr>
<td>57.0</td>
<td></td>
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<tr>
<td>60.0</td>
<td></td>
<td></td>
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<tr>
<td>63.0</td>
<td></td>
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<tr>
<td>66.0</td>
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<tr>
<td>69.0</td>
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<td></td>
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<td>9.00</td>
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<tr>
<td>12.0</td>
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<td>36.0</td>
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<td>60.0</td>
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<tr>
<td>63.0</td>
<td></td>
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<tr>
<td>66.0</td>
<td></td>
<td></td>
</tr>
<tr>
<td>69.0</td>
<td></td>
<td></td>
</tr>
</tbody>
</table>

**Hardware**

- CPU Name: Intel Xeon 5120
- CPU Characteristics: 1067 MHz system bus
- CPU MHz: 1867
- FPU: Integrated
- CPU(s) enabled: 4 cores, 2 chips, 2 cores/chip
- CPU(s) orderable: 1,2 chips
- Primary Cache: 32 KB I + 32 KB D on chip per core
- Secondary Cache: 4 MB I+D on chip per chip

**Software**

- Operating System: 64-Bit SUSE LINUX Enterprise Server 10, Kernel 2.6.16.21-0.8-smp on an x86_64
- Auto Parallel: No
- File System: ext2
Fujitsu Siemens Computers

PRIMERGY TX300 S3, Intel Xeon processor 5120, 1.86 GHz

SPECfp_rate2006 = 32.3
SPECfp_rate_base2006 = 31.3

CPU2006 license: 22
Test sponsor: Fujitsu Siemens Computers
Tested by: Fujitsu Siemens Computers
L3 Cache: None
Other Cache: None
Memory: 8 GB (8x1 GB DDR2 PC2-5300F, 2 rank, CAS 5-5-5, with ECC)
Disk Subsystem: SAS (73GB 15400 rpm)
System State: Multiuser, Runlevel 3
Base Pointers: 64-bit
Peak Pointers: 32/64-bit
Other Software: None

Results Table

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Copies</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Base</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Copies</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
</tr>
</thead>
<tbody>
<tr>
<td>416.gamess</td>
<td>4</td>
<td>1535</td>
<td>51.0</td>
<td>1536</td>
<td>51.0</td>
<td>1537</td>
<td>51.0</td>
<td></td>
<td>4</td>
<td>1535</td>
<td>51.0</td>
<td>1536</td>
<td>51.0</td>
<td>1537</td>
<td>51.0</td>
</tr>
<tr>
<td>433.milc</td>
<td>4</td>
<td>2110</td>
<td>17.4</td>
<td>2110</td>
<td>17.4</td>
<td>2111</td>
<td>17.4</td>
<td></td>
<td>4</td>
<td>2221</td>
<td>16.5</td>
<td>2221</td>
<td>16.5</td>
<td>2221</td>
<td>16.5</td>
</tr>
<tr>
<td>434.zeusmp</td>
<td>4</td>
<td>1067</td>
<td>34.1</td>
<td>1063</td>
<td>34.2</td>
<td>1062</td>
<td>34.3</td>
<td></td>
<td>4</td>
<td>1057</td>
<td>34.4</td>
<td>1054</td>
<td>34.5</td>
<td>1054</td>
<td>34.5</td>
</tr>
<tr>
<td>435.gromacs</td>
<td>4</td>
<td>432</td>
<td>50.8</td>
<td>416</td>
<td>51.1</td>
<td>420</td>
<td>50.7</td>
<td></td>
<td>4</td>
<td>434</td>
<td>51.1</td>
<td>420</td>
<td>50.7</td>
<td>420</td>
<td>50.7</td>
</tr>
<tr>
<td>437.leslie3d</td>
<td>4</td>
<td>2022</td>
<td>18.6</td>
<td>2021</td>
<td>18.6</td>
<td>2018</td>
<td>18.6</td>
<td></td>
<td>4</td>
<td>2022</td>
<td>18.6</td>
<td>2021</td>
<td>18.6</td>
<td>2018</td>
<td>18.6</td>
</tr>
<tr>
<td>444.namd</td>
<td>4</td>
<td>820</td>
<td>39.1</td>
<td>820</td>
<td>39.1</td>
<td>822</td>
<td>39.0</td>
<td></td>
<td>4</td>
<td>820</td>
<td>39.1</td>
<td>820</td>
<td>39.1</td>
<td>822</td>
<td>39.0</td>
</tr>
<tr>
<td>447.dealII</td>
<td>4</td>
<td>783</td>
<td>58.5</td>
<td>783</td>
<td>58.5</td>
<td>776</td>
<td>59.0</td>
<td></td>
<td>4</td>
<td>763</td>
<td>60.0</td>
<td>761</td>
<td>60.1</td>
<td>765</td>
<td>59.8</td>
</tr>
<tr>
<td>450.soplex</td>
<td>4</td>
<td>1520</td>
<td>21.9</td>
<td>1526</td>
<td>21.9</td>
<td>1526</td>
<td>21.9</td>
<td></td>
<td>4</td>
<td>1403</td>
<td>23.8</td>
<td>1403</td>
<td>23.8</td>
<td>1394</td>
<td>23.9</td>
</tr>
<tr>
<td>453.povray</td>
<td>4</td>
<td>419</td>
<td>50.8</td>
<td>416</td>
<td>51.1</td>
<td>420</td>
<td>50.7</td>
<td></td>
<td>4</td>
<td>315</td>
<td>67.6</td>
<td>318</td>
<td>66.9</td>
<td>315</td>
<td>67.5</td>
</tr>
<tr>
<td>454.calculix</td>
<td>4</td>
<td>853</td>
<td>38.7</td>
<td>857</td>
<td>38.5</td>
<td>855</td>
<td>38.6</td>
<td></td>
<td>4</td>
<td>835</td>
<td>39.5</td>
<td>840</td>
<td>39.3</td>
<td>841</td>
<td>39.2</td>
</tr>
<tr>
<td>459.GemsFDTD</td>
<td>4</td>
<td>2489</td>
<td>17.0</td>
<td>2507</td>
<td>16.9</td>
<td>2508</td>
<td>16.9</td>
<td></td>
<td>4</td>
<td>2489</td>
<td>17.0</td>
<td>2507</td>
<td>16.9</td>
<td>2508</td>
<td>16.9</td>
</tr>
<tr>
<td>465.tonto</td>
<td>4</td>
<td>1101</td>
<td>35.8</td>
<td>1098</td>
<td>35.9</td>
<td>1096</td>
<td>35.9</td>
<td></td>
<td>4</td>
<td>1067</td>
<td>36.9</td>
<td>1070</td>
<td>36.8</td>
<td>1069</td>
<td>36.8</td>
</tr>
<tr>
<td>470.lbm</td>
<td>4</td>
<td>3456</td>
<td>15.9</td>
<td>3523</td>
<td>15.6</td>
<td>3460</td>
<td>15.9</td>
<td></td>
<td>4</td>
<td>3236</td>
<td>17.0</td>
<td>3235</td>
<td>17.0</td>
<td>3234</td>
<td>17.0</td>
</tr>
<tr>
<td>481.wrf</td>
<td>4</td>
<td>1359</td>
<td>32.9</td>
<td>1362</td>
<td>32.8</td>
<td>1359</td>
<td>32.9</td>
<td></td>
<td>4</td>
<td>1359</td>
<td>32.9</td>
<td>1362</td>
<td>32.8</td>
<td>1359</td>
<td>32.9</td>
</tr>
<tr>
<td>482.sphinx3</td>
<td>4</td>
<td>2491</td>
<td>31.3</td>
<td>2496</td>
<td>31.2</td>
<td>2486</td>
<td>31.4</td>
<td></td>
<td>4</td>
<td>2320</td>
<td>33.6</td>
<td>2317</td>
<td>33.6</td>
<td>2320</td>
<td>33.6</td>
</tr>
</tbody>
</table>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run '/usr/bin/taskset' used to bind processes to CPUs

General Notes

The system bus runs at 1067 MHz

All binaries were built with 64-bit Intel compiler except: 433.milc, 434.zeusmp, 450.soplex, 470.lbm and 482.sphinx3 in peak were built with 32-bit Intel compiler by changing the path for include and library files.

This result was measured on the PRIMERGY RX300 S3. The PRIMERGY RX300 S3 and

Continued on next page
SPEC CFP2006 Result

Fujitsu Siemens Computers
PRIMERGY TX300 S3, Intel Xeon processor 5120, 1.86 GHz

SPECfp_rate2006 = 32.3
SPECfp_rate_base2006 = 31.3

CPU2006 license: 22
Test sponsor: Fujitsu Siemens Computers
Tested by: Fujitsu Siemens Computers

Test date: May-2007
Hardware Availability: Jul-2006
Software Availability: Feb-2007

General Notes (Continued)

the PRIMERGY TX300 S3 are electronically equivalent.

For information about Fujitsu Siemens Computers in your country please see:
http://www.fujitsu-siemens.com/countries

Base Compiler Invocation

C benchmarks:
  icc
C++ benchmarks:
  icpc
Fortran benchmarks:
  ifort
Benchmarks using both Fortran and C:
  icc ifort

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.eusump: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:
  -fast
C++ benchmarks:
  -fast

Continued on next page
SPEC CFP2006 Result

Fujitsu Siemens Computers
PRIMERGY TX300 S3, Intel Xeon processor 5120, 1.86 GHz

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Base Optimization Flags (Continued)

Fortran benchmarks:
- `fast`

Benchmarks using both Fortran and C:
- `fast`

Peak Compiler Invocation

C benchmarks:
```
/opt/intel/cc/9.1.047/bin/icc -I/opt/intel/cc/9.1.047/include
-L/opt/intel/cc/9.1.047/lib
```

C++ benchmarks (except as noted below):
```
icpc
```
```
450.soplex: /opt/intel/cc/9.1.047/bin/icpc
-I/opt/intel/cc/9.1.047/include -L/opt/intel/cc/9.1.047/lib
```

Fortran benchmarks (except as noted below):
```
ifort
```
```
434.zeusmp: /opt/intel/fc/9.1.043/bin/ifort
-I/opt/intel/fc/9.1.043/include -L/opt/intel/fc/9.1.043/lib
```

Benchmarks using both Fortran and C:
```
icc ifort
```

Peak Portability Flags

```
410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
```

Standard Performance Evaluation Corporation
info@spec.org
http://www.spec.org/
Fujitsu Siemens Computers
PRIMERGY TX300 S3, Intel Xeon processor 5120, 1.86 GHz

| SPECfp_rate2006 | 32.3 |
| SPECfp_rate_base2006 | 31.3 |

Peak Optimization Flags

C benchmarks:
- 433.milc: -prof_gen(pass 1) -prof_use(pass 2) -fast
- 470.lbm: Same as 433.milc
- 482.sphinx3: -fast

C++ benchmarks:
- 444.namd: basepeak = yes
- 447.dealII: -prof_gen(pass 1) -prof_use(pass 2) -fast
- 450.soplex: Same as 447.dealII
- 453.povray: Same as 447.dealII

Fortran benchmarks:
- 410.bwaves: basepeak = yes
- 416.gamess: basepeak = yes
- 434.zeusmp: -fast
- 437.leslie3d: basepeak = yes
- 459.GemsFDTD: basepeak = yes
- 465.tonto: -prof_gen(pass 1) -prof_use(pass 2) -fast

Benchmarks using both Fortran and C:
- 435.gromacs: -prof_gen(pass 1) -prof_use(pass 2) -fast
- 436.cactusADM: basepeak = yes
- 454.calculix: Same as 435.gromacs
- 481.wrf: basepeak = yes

The flags file that was used to format this result can be browsed at

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/CPU2006_flags.20090714.09.xml
Fujitsu Siemens Computers

PRIMERGY TX300 S3, Intel Xeon processor 5120, 1.86 GHz

SPECfp_rate2006 = 32.3
SPECfp_rate_base2006 = 31.3

CPU2006 license: 22
Test sponsor: Fujitsu Siemens Computers
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Test date: May-2007
Hardware Availability: Jul-2006
Software Availability: Feb-2007

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For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.0.