



# SPEC® CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Bull SAS

**SPECint®2006 = 8.19**

NovaScale R480 (2.60 GHz, Intel Xeon 7110M)

**SPECint\_base2006 = 7.75**

CPU2006 license: 20

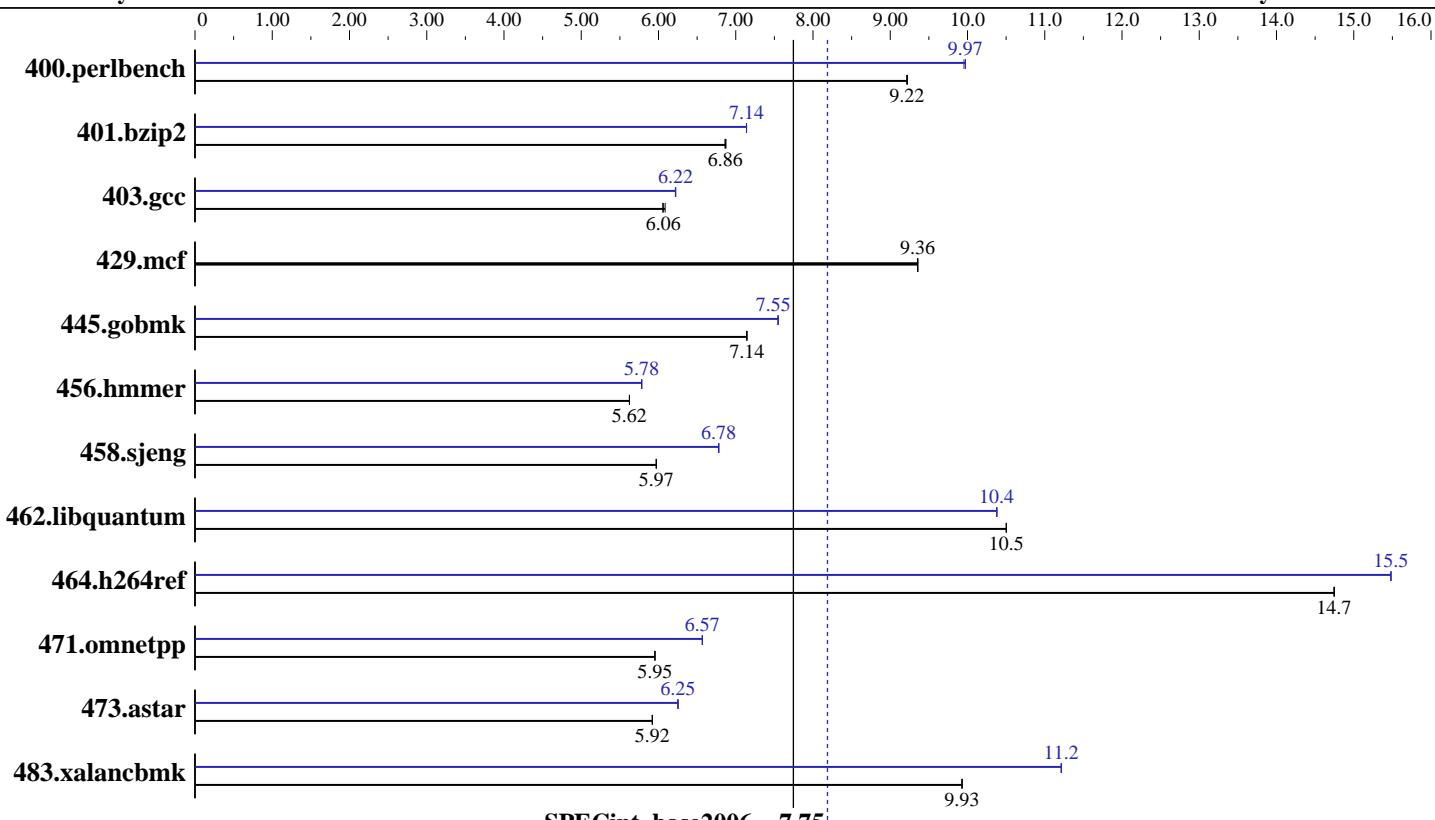
Test sponsor: Bull SAS

Tested by: Bull SAS

Test date: Apr-2007

Hardware Availability: Sep-2006

Software Availability: Nov-2006



## Hardware

CPU Name: Intel Xeon 7110M  
CPU Characteristics: 2.6 GHz, 800 MHz bus  
CPU MHz: 2600  
FPU: Integrated  
CPU(s) enabled: 1 core, 1 chip, 2 cores/chip  
CPU(s) orderable: 1,2,4 chips  
Primary Cache: 12 K micro-ops I + 16 KB D on chip per core  
Secondary Cache: 1 MB I+D on chip per core  
L3 Cache: 4 MB I+D on chip per chip  
Other Cache: None  
Memory: 32 GB (16x2 GB) DDR2 400 PC2-3200R-333  
Disk Subsystem: 2x36 GB SAS 15000 RPM  
Other Hardware: None

## Software

Operating System: Windows Server 2003 Enterprise X64 Edition  
Compiler: Intel C++ Compiler for IA32 version 9.1  
Package ID W\_CC\_C\_9.1.033 Build no 20061103Z  
Microsoft Visual Studio .NET 2003 (lib & linker)  
Auto Parallel: No  
File System: NTFS  
System State: Default  
Base Pointers: 32-bit  
Peak Pointers: 32-bit  
Other Software: MicroQuill SmartHeap Library 8.0 (shlw32M.lib)



# SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Bull SAS

NovaScale R480 (2.60 GHz, Intel Xeon 7110M)

**SPECint2006 = 8.19**

**SPECint\_base2006 = 7.75**

CPU2006 license: 20

Test date: Apr-2007

Test sponsor: Bull SAS

Hardware Availability: Sep-2006

Tested by: Bull SAS

Software Availability: Nov-2006

## Results Table

Benchmark	Base						Peak					
	Seconds	Ratio										
400.perlbench	1059	9.22	1060	9.21	<b>1060</b>	<b>9.22</b>	980	9.97	<b>980</b>	<b>9.97</b>	981	9.95
401.bzip2	1404	6.87	<b>1406</b>	<b>6.86</b>	1406	6.86	1352	7.14	1352	7.14	<b>1352</b>	<b>7.14</b>
403.gcc	1323	6.09	<b>1328</b>	<b>6.06</b>	1329	6.06	<b>1294</b>	<b>6.22</b>	1294	6.22	1295	6.22
429.mcf	<b>975</b>	<b>9.36</b>	975	9.36	975	9.35	<b>975</b>	<b>9.36</b>	975	9.36	975	9.35
445.gobmk	1469	7.14	1468	7.15	<b>1468</b>	<b>7.14</b>	<b>1390</b>	<b>7.55</b>	1390	7.55	1390	7.55
456.hmmer	<b>1659</b>	<b>5.62</b>	1659	5.62	1659	5.62	1613	5.78	1613	5.78	<b>1613</b>	<b>5.78</b>
458.sjeng	2026	5.97	2026	5.97	<b>2026</b>	<b>5.97</b>	<b>1785</b>	<b>6.78</b>	1785	6.78	1785	6.78
462.libquantum	1973	10.5	<b>1973</b>	<b>10.5</b>	1973	10.5	<b>1996</b>	<b>10.4</b>	1996	10.4	1996	10.4
464.h264ref	1500	14.7	1501	14.7	<b>1501</b>	<b>14.7</b>	1429	15.5	<b>1429</b>	<b>15.5</b>	1430	15.5
471.omnetpp	<b>1050</b>	<b>5.95</b>	1050	5.95	1050	5.95	<b>952</b>	<b>6.57</b>	952	6.57	952	6.57
473.astar	1186	5.92	<b>1186</b>	<b>5.92</b>	1186	5.92	1123	6.25	1123	6.25	<b>1123</b>	<b>6.25</b>
483.xalancbmk	695	9.93	695	9.93	<b>695</b>	<b>9.93</b>	615	11.2	<b>615</b>	<b>11.2</b>	615	11.2

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## General Notes

### Other Configuration Notes

/NUMPROC=1 flag was added to boot.ini to invoke uniprocessor environment

Hyper-Threading technology was disabled in the Bios.

The NovaScale T880 and the NovaScale R480 models are electronically equivalent.

The results have been measured on a NovaScale R480 model.

## Base Compiler Invocation

C benchmarks:

icl -Qvc7.1 -Qc99

C++ benchmarks:

icl -Qvc7.1



# SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Bull SAS

NovaScale R480 (2.60 GHz, Intel Xeon 7110M)

**SPECint2006 = 8.19**

**SPECint\_base2006 = 7.75**

**CPU2006 license:** 20

**Test sponsor:** Bull SAS

**Tested by:** Bull SAS

**Test date:** Apr-2007

**Hardware Availability:** Sep-2006

**Software Availability:** Nov-2006

## Base Portability Flags

403.gcc: -DSPEC\_CPU\_WIN32  
464.h264ref: -DSPEC\_CPU\_NO\_INTTYPES -DWIN32

## Base Optimization Flags

C benchmarks:

-fast /F512000000 shlw32m.lib -link /FORCE:MULTIPLE

C++ benchmarks:

-fast -Qcxx\_features /F512000000 shlw32m.lib  
-link /FORCE:MULTIPLE

## Base Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

## Peak Compiler Invocation

C benchmarks:

icl -Qvc7.1 -Qc99

C++ benchmarks:

icl -Qvc7.1

## Peak Portability Flags

403.gcc: -DSPEC\_CPU\_WIN32  
464.h264ref: -DSPEC\_CPU\_NO\_INTTYPES -DWIN32

## Peak Optimization Flags

C benchmarks:

400.perlbench: -Qprof\_gen(pass 1) -Qprof\_use(pass 2) -fast /F512000000  
shlw32m.lib -link /FORCE:MULTIPLE

401.bzip2: Same as 400.perlbench

Continued on next page



# SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Bull SAS

SPECint2006 = 8.19

NovaScale R480 (2.60 GHz, Intel Xeon 7110M)

SPECint\_base2006 = 7.75

CPU2006 license: 20

Test date: Apr-2007

Test sponsor: Bull SAS

Hardware Availability: Sep-2006

Tested by: Bull SAS

Software Availability: Nov-2006

## Peak Optimization Flags (Continued)

403.gcc: Same as 400.perlbench

429.mcf: basepeak = yes

445.gobmk: Same as 400.perlbench

456.hmmr: Same as 400.perlbench

458.sjeng: Same as 400.perlbench

462.libquantum: Same as 400.perlbench

464.h264ref: Same as 400.perlbench

C++ benchmarks:

-Qprof\_gen(pass 1) -Qprof\_use(pass 2) -fast -Qcxx\_features  
/F512000000 shlw32m.lib -link /FORCE:MULTIPLE

## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=\_\_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/flags.20090714.00.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/flags.20090714.00.xml>

SPEC and SPECint are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.  
For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

Tested with SPEC CPU2006 v1.0.

Report generated on Tue Jul 22 11:47:35 2014 by SPEC CPU2006 PS/PDF formatter v6932.

Originally published on 29 May 2007.