Bull SAS

NovaScale R480 (3.0 GHz, Intel Xeon 7120M)

CPU2006 license: 20
Test sponsor: Bull SAS
Tested by: Bull SAS

SPECint\_rate2006 = 53.9
SPECint\_rate\_base2006 = 50.7

CPU Name: Intel Xeon 7120M
CPU Characteristics: 3.0 GHz, 800 MHz bus
CPU MHz: 3000
FPU: Integrated
CPU(s) enabled: 8 cores, 4 chips, 2 cores/chip, 2 threads/core
CPU(s) orderable: 1,2,4 chips
Primary Cache: 12 K micro-ops I + 16 KB D on chip per core
Secondary Cache: 1 MB I+D on chip per core
L3 Cache: 4 MB I+D on chip per chip
Other Cache: None
Memory: 32 GB (16x2 GB) DDR2 400 PC2-3200R-333
Disk Subsystem: 2x36 GB SAS 15000 RPM
Other Hardware: None

Compiler: Intel C++ Compiler for IA32 version 9.1
Package ID W_CC_C_9.1.033 Build no 20061103Z
Microsoft Visual Studio .NET 2003 (lib & linker)
Auto Parallel: No
File System: NTFS
System State: Default
Base Pointers: 32-bit
Peak Pointers: 32-bit
Other Software: MicroQuill SmartHeap Library 8.0 (shlW32M.lib)
### Results Table

<table>
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<th>Ratio</th>
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<td><strong>64.6</strong></td>
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</tbody>
</table>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

### General Notes

**Other Configuration Notes**

The NovaScale T880 and the NovaScale R480 models are electronically equivalent.

The results have been measured on a NovaScale R480 model.

### Base Compiler Invocation

**C benchmarks:**

icl -Qvc7.1 -Qc99

**C++ benchmarks:**

icl -Qvc7.1

### Base Portability Flags

403.gcc: -DSPEC_CPU_WIN32

Continued on next page
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| SPECint_rate2006 = | 53.9 |
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CPU2006 license: 20
Test sponsor: Bull SAS
Test date: May-2007
Tested by: Bull SAS
Hardware Availability: Sep-2006
Software Availability: Nov-2006

### Base Portability Flags (Continued)

464.h264ref: -DSPEC_CPU_NO_INTTYPES -DWIN32

### Base Optimization Flags

C benchmarks:
-fast /F512000000 shlw32m.lib
- link /FORCE:MULTIPLE

C++ benchmarks:
- fast -Qcxx_features /F512000000 shlw32m.lib
- link /FORCE:MULTIPLE

### Base Other Flags

C benchmarks:
403.gcc: -Dalloca=_alloca

### Peak Compiler Invocation

C benchmarks:
icl -Qvc7.1 -Qc99

C++ benchmarks:
icl -Qvc7.1

### Peak Portability Flags

403.gcc: -DSPEC_CPU_WIN32
464.h264ref: -DSPEC_CPU_NO_INTTYPES -DWIN32

### Peak Optimization Flags

C benchmarks:
400.perlbench: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast /F512000000
shlw32m.lib
- link /FORCE:MULTIPLE

401.bzip2: Same as 400.perlbench
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Peak Optimization Flags (Continued)

403.gcc: Same as 400.perlbench
429.mcf: basepeak = yes
445.gobmk: Same as 400.perlbench
456.hmmer: Same as 400.perlbench
458.sjeng: Same as 400.perlbench
462.libquantum: Same as 400.perlbench
464.h264ref: Same as 400.perlbench

C++ benchmarks:
- -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qcxx_features
/F512000000 shlw32m.lib
- -link /FORCE:MULTIPLE

Peak Other Flags

C benchmarks:
403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/flags.20090714.00.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/flags.20090714.00.xml

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For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.0.