Bull SAS
NovaScale T880 (2.60 GHz, Intel Xeon 7110M)

**SPECint®2006 =** 8.19
**SPECint_base2006 =** 7.75

<table>
<thead>
<tr>
<th>Test Date</th>
<th>Apr-2007</th>
</tr>
</thead>
<tbody>
<tr>
<td>Hardware Availability</td>
<td>Sep-2006</td>
</tr>
<tr>
<td>Software Availability</td>
<td>Nov-2006</td>
</tr>
</tbody>
</table>

### CPU2006 license
20

### Test sponsor
Bull SAS

### Tested by
Bull SAS

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<thead>
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### Test date
Apr-2007

### Hardware

<table>
<thead>
<tr>
<th>Test</th>
<th>Scores</th>
</tr>
</thead>
<tbody>
<tr>
<td>400.perlbench</td>
<td>9.97</td>
</tr>
<tr>
<td>401.bzip2</td>
<td>9.22</td>
</tr>
<tr>
<td>403.gcc</td>
<td>6.22</td>
</tr>
<tr>
<td>429.mcf</td>
<td>6.06</td>
</tr>
<tr>
<td>445.gobmk</td>
<td>7.14</td>
</tr>
<tr>
<td>456.hmmer</td>
<td>5.78</td>
</tr>
<tr>
<td>458.sjeng</td>
<td>5.97</td>
</tr>
<tr>
<td>462.libquantum</td>
<td>10.4</td>
</tr>
<tr>
<td>464.h264ref</td>
<td>15.5</td>
</tr>
<tr>
<td>471.omnetpp</td>
<td>6.57</td>
</tr>
<tr>
<td>473.astar</td>
<td>5.95</td>
</tr>
<tr>
<td>483.xalancbmk</td>
<td>9.93</td>
</tr>
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</table>

### Software

<table>
<thead>
<tr>
<th></th>
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</thead>
<tbody>
<tr>
<td>Compiler:</td>
<td>Intel C++ Compiler for IA32 version 9.1</td>
</tr>
<tr>
<td>Package ID:</td>
<td>W_CC_C_9.1.033 Build no 20061103Z</td>
</tr>
<tr>
<td>Microsoft Visual Studio .NET 2003 (lib &amp; linker)</td>
<td></td>
</tr>
<tr>
<td>Auto Parallel:</td>
<td>No</td>
</tr>
<tr>
<td>File System:</td>
<td>NTFS</td>
</tr>
<tr>
<td>System State:</td>
<td>Default</td>
</tr>
<tr>
<td>Base Pointers:</td>
<td>32-bit</td>
</tr>
<tr>
<td>Peak Pointers:</td>
<td>32-bit</td>
</tr>
<tr>
<td>Other Software:</td>
<td>MicroQuill SmartHeap Library 8.0 (shlW32M.lib)</td>
</tr>
</tbody>
</table>

### CPU Name
Intel Xeon 7110M

### CPU Characteristics
2.6 GHz, 800 MHz bus

### CPU MHz
2600

### FPU
Integrated

### CPU(s) enabled
1 core, 1 chip, 2 cores/chip

### CPU(s) orderable
1,2,4 chips

### Primary Cache
12 K micro-ops I + 16 KB D on chip per core

### Secondary Cache
1 MB I+D on chip per core

### L3 Cache:
4 MB I+D on chip per chip

### Other Cache:
None

### Memory:
32 GB (16x2 GB) DDR2 400 PC2-3200R-333

### Disk Subsystem:
2x36 GB SAS 15000 RPM

### Other Hardware:
None
SPEC CINT2006 Result

Bull SAS

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CPU2006 license: 20
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Results Table

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
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<th>Ratio</th>
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<tr>
<td>403.gcc</td>
<td>1323</td>
<td>6.09</td>
<td>1328</td>
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<td>429.mcf</td>
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<td>456.hmmer</td>
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<td><strong>615</strong></td>
<td><strong>11.2</strong></td>
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</table>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

Other Configuration Notes
/NUMPROC=1 flag was added to boot.ini to invoke uniprocessor environment
Hyper-Threading technology was disabled in the Bios.

The NovaScale T880 and the NovaScale R480 models are electronically equivalent.
The results have been measured on a NovaScale R480 model.

Base Compiler Invocation

C benchmarks:
  icl -Qvc7.1 -Qc99

C++ benchmarks:
  icl -Qvc7.1
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Base Portability Flags

403.gcc: -DSPEC_CPU_WIN32
464.h264ref: -DSPEC_CPU_NO_INTTYPES -DWIN32

Base Optimization Flags

C benchmarks:
- fast /F512000000 shlw32m.lib -link /FORCE:MULTIPLE

C++ benchmarks:
- fast -Qcxx_features /F512000000 shlw32m.lib
- link /FORCE:MULTIPLE

Base Other Flags

C benchmarks:
403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks:
icl -Qvc7.1 -Qc99

C++ benchmarks:
icl -Qvc7.1

Peak Portability Flags

403.gcc: -DSPEC_CPU_WIN32
464.h264ref: -DSPEC_CPU_NO_INTTYPES -DWIN32

Peak Optimization Flags

C benchmarks:
400.perlbench: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast /F512000000 shlw32m.lib -link /FORCE:MULTIPLE
401.bzip2: Same as 400.perlbench

Continued on next page
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Peak Optimization Flags (Continued)

403.gcc: Same as 400.perlbench
429.mcf: basepeak = yes
445.gobmk: Same as 400.perlbench
456.hmmer: Same as 400.perlbench
458.sjeng: Same as 400.perlbench
462.libquantum: Same as 400.perlbench
464.h264ref: Same as 400.perlbench

C++ benchmarks:
-Oproft_gen(pass 1) -Oproft_use(pass 2) -fast -Qcxx_features
/F512000000 shlw32m.lib -link /FORCE:MULTIPLE

Peak Other Flags

C benchmarks:
403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/flags.20090714.00.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/flags.20090714.00.xml

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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.0.