

**Bull SAS**

NovaScale T880 (3.0 GHz, Intel Xeon 7120M)

**SPECint_rate2006 = 53.9**

**SPECint_rate_base2006 = 50.7**

**CPU2006 license:** 20  
**Test sponsor:** Bull SAS  
**Tested by:** Bull SAS

**Hardware**

**CPU Name:** Intel Xeon 7120M  
**CPU Characteristics:** 3.0 GHz, 800 MHz bus  
**CPU MHz:** 3000  
**FPU:** Integrated  
**CPU(s) enabled:** 8 cores, 4 chips, 2 cores/chip, 2 threads/core  
**CPU(s) orderable:** 1,2,4 chips  
**Primary Cache:** 12 K micro-ops I + 16 KB D on chip per core  
**Secondary Cache:** 1 MB I+D on chip per core  
**L3 Cache:** None  
**Other Cache:** None  
**Memory:** 32 GB (16x2 GB) DDR2 400 PC2-3200R-333  
**Disk Subsystem:** 2x36 GB SAS 15000 RPM  
**Other Hardware:** None

**Software**

**Operating System:** Windows Server 2003 Enterprise X64 Edition  
**Compiler:** Intel C++ Compiler for IA32 version 9.1  
**Package ID** W_CC_C_9.1.033 Build no 20061103Z  
**Microsoft Visual Studio .NET 2003** (lib & linker)  
**Auto Parallel:** No  
**File System:** NTFS  
**System State:** Default  
**Base Pointers:** 32-bit  
**Peak Pointers:** 32-bit  
**Other Software:** MicroQuill SmartHeap Library 8.0 (shlW32M.lib)
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Results Table

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Copies</th>
<th>Seconds</th>
<th>Ratio</th>
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<th>Ratio</th>
<th>Copies</th>
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<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
</tr>
</thead>
<tbody>
<tr>
<td>400.perlbench</td>
<td>16</td>
<td>1892</td>
<td>82.6</td>
<td>1893</td>
<td>82.6</td>
<td>1890</td>
<td>82.7</td>
<td>16</td>
<td>1691</td>
<td>92.4</td>
<td>1692</td>
<td>92.4</td>
<td>1692</td>
<td>92.4</td>
</tr>
<tr>
<td>401.bzip2</td>
<td>16</td>
<td>3488</td>
<td>44.3</td>
<td>3495</td>
<td>44.2</td>
<td>3488</td>
<td>44.3</td>
<td>16</td>
<td>3472</td>
<td>44.5</td>
<td>3474</td>
<td>44.4</td>
<td>3464</td>
<td>44.6</td>
</tr>
<tr>
<td>403.gcc</td>
<td>16</td>
<td>5693</td>
<td>22.6</td>
<td>5658</td>
<td>22.8</td>
<td>5679</td>
<td>22.7</td>
<td>16</td>
<td>5670</td>
<td>22.7</td>
<td>5693</td>
<td>22.6</td>
<td>5702</td>
<td>22.6</td>
</tr>
<tr>
<td>429.mcf</td>
<td>16</td>
<td>3509</td>
<td>41.6</td>
<td>3501</td>
<td>41.7</td>
<td>3514</td>
<td>41.5</td>
<td>16</td>
<td>3509</td>
<td>41.6</td>
<td>3501</td>
<td>41.7</td>
<td>3514</td>
<td>41.5</td>
</tr>
<tr>
<td>445.gobmk</td>
<td>16</td>
<td>2220</td>
<td>75.6</td>
<td>2215</td>
<td>75.8</td>
<td>2216</td>
<td>75.7</td>
<td>16</td>
<td>2028</td>
<td>82.8</td>
<td>2022</td>
<td>83.0</td>
<td>2020</td>
<td>83.1</td>
</tr>
<tr>
<td>456.hmmer</td>
<td>16</td>
<td>2669</td>
<td>55.9</td>
<td>2624</td>
<td>56.9</td>
<td>2622</td>
<td>56.9</td>
<td>16</td>
<td>2095</td>
<td>71.3</td>
<td>2097</td>
<td>71.2</td>
<td>2102</td>
<td>71.0</td>
</tr>
<tr>
<td>458.sjeng</td>
<td>16</td>
<td>3358</td>
<td>57.6</td>
<td>3352</td>
<td>57.8</td>
<td>3247</td>
<td>59.6</td>
<td>16</td>
<td>2875</td>
<td>67.3</td>
<td>2948</td>
<td>65.7</td>
<td>2951</td>
<td>65.6</td>
</tr>
<tr>
<td>462.libquantum</td>
<td>16</td>
<td>10882</td>
<td>30.5</td>
<td>10882</td>
<td>30.5</td>
<td>10894</td>
<td>30.4</td>
<td>16</td>
<td>10883</td>
<td>30.5</td>
<td>10882</td>
<td>30.5</td>
<td>10884</td>
<td>30.5</td>
</tr>
<tr>
<td>464.h264ref</td>
<td>16</td>
<td>2543</td>
<td>139</td>
<td>2539</td>
<td>139</td>
<td>2537</td>
<td>140</td>
<td>16</td>
<td>2457</td>
<td>144</td>
<td>2453</td>
<td>144</td>
<td>2437</td>
<td>145</td>
</tr>
<tr>
<td>471.omnetpp</td>
<td>16</td>
<td>3671</td>
<td>27.2</td>
<td>3671</td>
<td>27.2</td>
<td>3672</td>
<td>27.2</td>
<td>16</td>
<td>3608</td>
<td>27.7</td>
<td>3608</td>
<td>27.7</td>
<td>3609</td>
<td>27.7</td>
</tr>
<tr>
<td>473.astar</td>
<td>16</td>
<td>2258</td>
<td>49.8</td>
<td>2252</td>
<td>49.9</td>
<td>2249</td>
<td>49.9</td>
<td>16</td>
<td>2210</td>
<td>50.8</td>
<td>2213</td>
<td>50.8</td>
<td>2210</td>
<td>50.8</td>
</tr>
<tr>
<td>483.xalancbmk</td>
<td>16</td>
<td>1880</td>
<td>58.7</td>
<td>1879</td>
<td>58.8</td>
<td>1882</td>
<td>58.7</td>
<td>16</td>
<td>1714</td>
<td>64.4</td>
<td>1708</td>
<td>64.6</td>
<td>1710</td>
<td>64.6</td>
</tr>
</tbody>
</table>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

Other Configuration Notes
The NovaScale T880 and the NovaScale R480 models are electronically equivalent.
The results have been measured on a NovaScale R480 model.

Base Compiler Invocation
C benchmarks:
icl -Qvc7.1 -Qc99
C++ benchmarks:
icl -Qvc7.1

Base Portability Flags
403.gcc: -DSPEC_CPU_WIN32

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Tested by: Bull SAS

Test date: May-2007
Hardware Availability: Sep-2006
Software Availability: Nov-2006

Base Portability Flags (Continued)
464.h264ref: -DSPEC_CPU_NO_INTTYPES -DWIN32

Base Optimization Flags
C benchmarks:
-fast /F512000000 shlw32m.lib
-link /FORCE:MULTIPLE

C++ benchmarks:
-fast -Qcxx_features /F512000000 shlw32m.lib
-link /FORCE:MULTIPLE

Base Other Flags
C benchmarks:
403.gcc: -Dalloca=_alloca

Peak Compiler Invocation
C benchmarks:
icl -Qvc7.1 -Qc99

C++ benchmarks:
icl -Qvc7.1

Peak Portability Flags
403.gcc: -DSPEC_CPU_WIN32
464.h264ref: -DSPEC_CPU_NO_INTTYPES -DWIN32

Peak Optimization Flags
C benchmarks:
400.perlbench: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast /F512000000
shlw32m.lib
-link /FORCE:MULTIPLE

401.bzip2: Same as 400.perlbench
### Bull SAS

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#### Peak Optimization Flags (Continued)

- 403.gcc: Same as 400.perlbench
- 429.mcf: basepeak = yes
- 445.gobmk: Same as 400.perlbench
- 456.hmmer: Same as 400.perlbench
- 458.sjeng: Same as 400.perlbench
- 462.libquantum: Same as 400.perlbench
- 464.h264ref: Same as 400.perlbench

**C++ benchmarks:**

- `-Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qcxx_features`  
  `/F512000000 shlw32m.lib`  
- `-link /FORCE:MULTIPLE`

#### Peak Other Flags

- **C benchmarks:**

  - 403.gcc: `-Dalloca=_alloca`

The flags file that was used to format this result can be browsed at http://www.spec.org/cpu2006/flags/flags.20090714.00.html

You can also download the XML flags source by saving the following link:

http://www.spec.org/cpu2006/flags/flags.20090714.00.xml

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For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.0.  