## SPEC® CINT2006 Result

**Bull SAS**  
NovaScale R460  
(Intel Xeon processor X5355, 2.66GHz)

### SPECint\_rate2006 = 78.4  
SPECint\_rate\_base2006 = 76.1

<table>
<thead>
<tr>
<th>CPU2006 license:</th>
<th>20</th>
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<tr>
<td>Test sponsor:</td>
<td>Bull SAS</td>
</tr>
<tr>
<td>Tested by:</td>
<td>Bull SAS</td>
</tr>
<tr>
<td>Hardware Availability:</td>
<td>May-2007</td>
</tr>
<tr>
<td>Software Availability:</td>
<td>Mar-2007</td>
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</tbody>
</table>

### Hardware

<table>
<thead>
<tr>
<th>Parameter</th>
<th>Details</th>
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</thead>
<tbody>
<tr>
<td>CPU Name:</td>
<td>Intel Xeon X5355</td>
</tr>
<tr>
<td>CPU Characteristics:</td>
<td>2.66 GHz, 8 MB L2, 1333 MHz bus</td>
</tr>
<tr>
<td>CPU MHz:</td>
<td>2666</td>
</tr>
<tr>
<td>FPU:</td>
<td>Integrated</td>
</tr>
<tr>
<td>CPU(s) enabled:</td>
<td>8 cores, 2 chips, 4 cores/chip</td>
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<tr>
<td>CPU(s) orderable:</td>
<td>1 to 2 chips</td>
</tr>
<tr>
<td>Primary Cache:</td>
<td>32 KB I + 32 KB D on chip per core</td>
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<tr>
<td>Secondary Cache:</td>
<td>8 MB I+D on chip per chip, 4 MB shared / 2 cores</td>
</tr>
<tr>
<td>L3 Cache:</td>
<td>None</td>
</tr>
<tr>
<td>Other Cache:</td>
<td>None</td>
</tr>
<tr>
<td>Memory:</td>
<td>24 GB (12x2 GB) FB-DIMM PC2-5300F ECC CL5</td>
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<td>Disk Subsystem:</td>
<td>1x73 GB SAS, 10000 RPM</td>
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<td>Other Hardware:</td>
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### Software

<table>
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<tr>
<td>Operating System:</td>
<td>Windows Server 2003 R2 Enterprise X64 Edition Service Pack1</td>
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<tr>
<td>Compiler:</td>
<td>Intel C++ Compiler for IA32 version 9.1</td>
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<tr>
<td></td>
<td>Package ID W_CC_C_9.1.033 Build no 20061103Z</td>
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<td></td>
<td>Microsoft Visual Studio .NET 2003 (lib &amp; linker)</td>
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<tr>
<td>Auto Parallel:</td>
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<tr>
<td>File System:</td>
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<td>System State:</td>
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<tr>
<td>Base Pointers:</td>
<td>32-bit</td>
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<td>Peak Pointers:</td>
<td>32-bit</td>
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<tr>
<td>Other Software:</td>
<td>MicroQuill SmartHeap Library 8.0 (shlW32M.lib)</td>
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</tbody>
</table>
Bull SAS
NovaScale R460
(Intel Xeon processor X5355,2.66GHz)

CPU2006 license: 20
Test sponsor: Bull SAS
Tested by: Bull SAS

SPECint_rate2006 = 78.4
SPECint_rate_base2006 = 76.1

Results Table

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</table>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes
The NovaScale R440 and the NovaScale R460 models are electronically equivalent.
The results have been measured on a NovaScale R440 model.

Base Compiler Invocation
C benchmarks:
  icl -Qvc7.1 -Qc99
C++ benchmarks:
  icl -Qvc7.1

Base Portability Flags
403.gcc: -DSPEC_CPU_WIN32
464.h264ref: -DSPEC_CPU_NO_INTTYPES -DWIN32
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SPEClnt_rate_base2006 = 76.1

CPU2006 license: 20
Test sponsor: Bull SAS
Test date: May-2007
Test by: Bull SAS
Hardware Availability: Mar-2007
Tested by: Bull SAS
Software Availability: Dec-2006

Base Optimization Flags
C benchmarks:
- fast /F512000000 shelw32m.lib
  -link /FORCE:MULTIPLE
C++ benchmarks:
- fast -Qcxx_features /F512000000 shelw32m.lib
  -link /FORCE:MULTIPLE

Base Other Flags
C benchmarks:
  403.gcc: -Dalloca=_alloca

Peak Compiler Invocation
C benchmarks:
  icl -Qvc7.1 -Qc99
C++ benchmarks:
  icl -Qvc7.1

Peak Portability Flags
403.gcc: -DSPEC_CPU_WIN32
464.h264ref: -DSPEC_CPU_NO_INTTYPES -DWIN32

Peak Optimization Flags
C benchmarks:
  -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast /F512000000 shelw32m.lib
  -link /FORCE:MULTIPLE
C++ benchmarks:
  -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qcxx_features
  /F512000000 shelw32m.lib
  -link /FORCE:MULTIPLE

Peak Other Flags
C benchmarks:

Continued on next page
## Peak Other Flags (Continued)

403.gcc: -Dalloca=_alloca

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The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/flags.20090714.00.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/flags.20090714.00.xml

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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.0.