## Bull SAS

NovaScale B260 (Intel Xeon processor E5310, 1.60GHz)

<table>
<thead>
<tr>
<th>SPECint2006</th>
<th>SPECint_base2006</th>
</tr>
</thead>
<tbody>
<tr>
<td>10.3</td>
<td>9.85</td>
</tr>
</tbody>
</table>

| CPU2006 license: | 20      |
| Test sponsor:    | Bull SAS |
| Tested by:       | Bull SAS |
| CPU Name:        | Intel Xeon E5310 |
| CPU Characteristics: | 1.60 GHz, 8 MB L2, 1066 MHz bus |
| CPU MHz:         | 1600     |
| FPU:             | Integrated |
| CPU(s) enabled:  | 1 core, 1 chip, 4 cores/chip |
| CPU(s) orderable:| 1 to 2 chips |
| Primary Cache:   | 32 KB I + 32 KB D on chip per core |
| Secondary Cache: | 8 MB I+D on chip per core, 4 MB shared / 2 cores |
| L3 Cache:        | None     |
| Other Cache:     | None     |
| Memory:          | 8 GB (4x2 GB) FB-DIMM PC2-5300F ECC CL5 |
| Disk Subsystem:  | 1x73 GB SAS, 10000 RPM |
| Other Hardware:  | None     |

### SPECint2006 Results

| Test date:         | Apr-2007   |
| Hardware Availability: | Jan-2007 |
| Software Availability: | Dec-2006 |

### Software

- **Operating System:** Windows Server 2003 Enterprise Edition (32 bits) Service Pack1
- **Compiler:** Intel C++ Compiler for IA32 version 9.1 Package ID W_CC_C_9.1.033 Build no 20061103Z Microsoft Visual Studio.NET 2003 (lib & linker)
- **Auto Parallel:** No
- **File System:** NTFS
- **System State:** Default
- **Base Pointers:** 32-bit
- **Peak Pointers:** 32-bit
- **Other Software:** MicroQuill SmartHeap Library 8.0 (shlW32M.lib)
## SPEC CINT2006 Result

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**SPECint2006 =** 10.3  
**SPECint_base2006 =** 9.85

### Results Table

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
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<tbody>
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<td>542</td>
<td>12.7</td>
</tr>
</tbody>
</table>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

### Operating System Notes

/NUMPROC=1 flag was added to boot.ini to invoke uniprocessor environment

### Base Compiler Invocation

C benchmarks:  
icl -Qvc7.1 -Qc99

C++ benchmarks:  
icl -Qvc7.1

### Base Portability Flags

403.gcc: -DSPEC_CPU_WIN32  
464.h264ref: -DSPEC_CPU_NO_INTTYPES -DWIN32

### Base Optimization Flags

C benchmarks:  
-fast /F512000000 shlw32m.lib -link /FORCE:MULTIPLE

Continued on next page
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Base Optimization Flags (Continued)

C++ benchmarks:
- -fast -Qcxx_features /F512000000 shlw32m.lib
- -link /FORCE:MULTIPLE

Base Other Flags

C benchmarks:
403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks:
icl -Qvc7.1 -Qc99

C++ benchmarks:
icl -Qvc7.1

Peak Portability Flags

403.gcc: -DSPEC_CPU_WIN32
464.h264ref: -DSPEC_CPU_NO_IN TTYPES -DWIN32

Peak Optimization Flags

C benchmarks:
- -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast /F512000000 shlw32m.lib
- -link /FORCE:MULTIPLE

C++ benchmarks:
- -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qcxx_features
/F512000000 shlw32m.lib
- -link /FORCE:MULTIPLE

Peak Other Flags

C benchmarks:
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The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/flags.20090714.00.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/flags.20090714.00.xml

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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Tested with SPEC CPU2006 v1.0.