Fujitsu Siemens Computers

PRIMERGY RX200 S3, Intel Xeon processor 5140, 2.33 GHz

SPECint®2006 = 15.9
SPECint_base2006 = 15.0

Test date: May-2007
Hardware Availability: Jul-2006
Software Availability: Mar-2007

CPU2006 license: 22
Test sponsor: Fujitsu Siemens Computers
Tested by: Fujitsu Siemens Computers
Hardware

CPU Name: Intel Xeon 5140
CPU Characteristics: 1333 MHz system bus
CPU MHz: 2333
FPU: Integrated
CPU(s) enabled: 2 cores, 1 chip, 2 cores/chip
CPU(s) orderable: 1.2 chips
Primary Cache: 32 KB I + 32 KB D on chip per core
Secondary Cache: 4 MB I+D on chip per chip
L3 Cache: None
Other Cache: None
Memory: 16 GB (8x2 GB DDR2 PC2-5300F, 2 rank, CAS 5-5-5, with ECC)
Disk Subsystem: SAS (73GB 15400 rpm)
Other Hardware: None

Operating System: 64-Bit SUSE LINUX Enterprise Server 10, Kernel 2.6.16.21-0.8-smp on an x86_64
Auto Parallel: No
File System: ext2
System State: Multiuser, Runlevel 3
Base Pointers: 32-bit
Peak Pointers: 32/64-bit
Other Software: Smart Heap Library, Version 8.1

Software
SPEC CINT2006 Result

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Results Table

<table>
<thead>
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<th>Benchmark</th>
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<th>Ratio</th>
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<th>Ratio</th>
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</tbody>
</table>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run

General Notes

The system bus runs at 1333 MHz

All binaries were built with 32-bit Intel compiler except:
401.bzip2, 456.hmmer and 462.libquantum in peak were built with
64-bit Intel compiler by changing the path for include and library files.

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http://www.fujitsu-siemens.com/countries

Base Compiler Invocation

C benchmarks:
   icc

C++ benchmarks:
icpc

Standard Performance Evaluation Corporation
info@spec.org
http://www.spec.org/
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**Base Portability Flags**

- 400.perlbench: -DSPEC_CPU_LINUX_X64
- 462.libquantum: -DSPEC_CPU_LINUX
- 483.xalancbmk: -DSPEC_CPU_LINUX

**Base Optimization Flags**

- C benchmarks: -fast
- C++ benchmarks: -xp -G3 -ipo -no-prec-div -L/opt/SmartHeap_8_1/lib -lsmartheap

**Peak Compiler Invocation**

- C benchmarks (except as noted below):
  - icc
    - 401.bzip2: /opt/intel/cce/9.1.047/bin/icc
      -I/opt/intel/cce/9.1.047/include
      -L/opt/intel/cce/9.1.047/lib
    - 456.hmmer: /opt/intel/cce/9.1.047/bin/icc
      -I/opt/intel/cce/9.1.047/include
      -L/opt/intel/cce/9.1.047/lib
    - 462.libquantum: /opt/intel/cce/9.1.047/bin/icc
      -I/opt/intel/cce/9.1.047/include
      -L/opt/intel/cce/9.1.047/lib
  - C++ benchmarks: icpc

**Peak Portability Flags**

- 400.perlbench: -DSPEC_CPU_LINUX_X64
- 401.bzip2: -DSPEC_CPU_LP64
- 456.hmmer: -DSPEC_CPU_LP64
- 462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
- 483.xalancbmk: -DSPEC_CPU_LINUX
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Peak Optimization Flags

C benchmarks:

400.perlbench: -prof_gen(pass 1) -prof_use(pass 2) -fast
401.bzip2: -fast
403.gcc: basepeak = yes
429.mcf: -prof_gen(pass 1) -prof_use(pass 2) -fast
-L/opt/SmartHeap_8_1/lib -lsmartheap
445.gobmk: Same as 429.mcf
456.hmmer: Same as 400.perlbench
458.sjeng: Same as 429.mcf
462.libquantum: Same as 400.perlbench
464.h264ref: Same as 429.mcf

C++ benchmarks:

471.omnetpp: -prof_gen(pass 1) -prof_use(pass 2) -xP -O3 -ipo
-no-prec-div -L/opt/SmartHeap_8_1/lib -lsmartheap
473.astar: -prof_gen(pass 1) -prof_use(pass 2) -fast
-L/opt/SmartHeap_8_1/lib -lsmartheap
483.xalancbmk: basepeak = yes

The flags file that was used to format this result can be browsed at
You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/CPUS2006_flags.20090714.09.xml

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For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.0.