SPEC® CFP2006 Result

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Fujitsu Siemens Computers

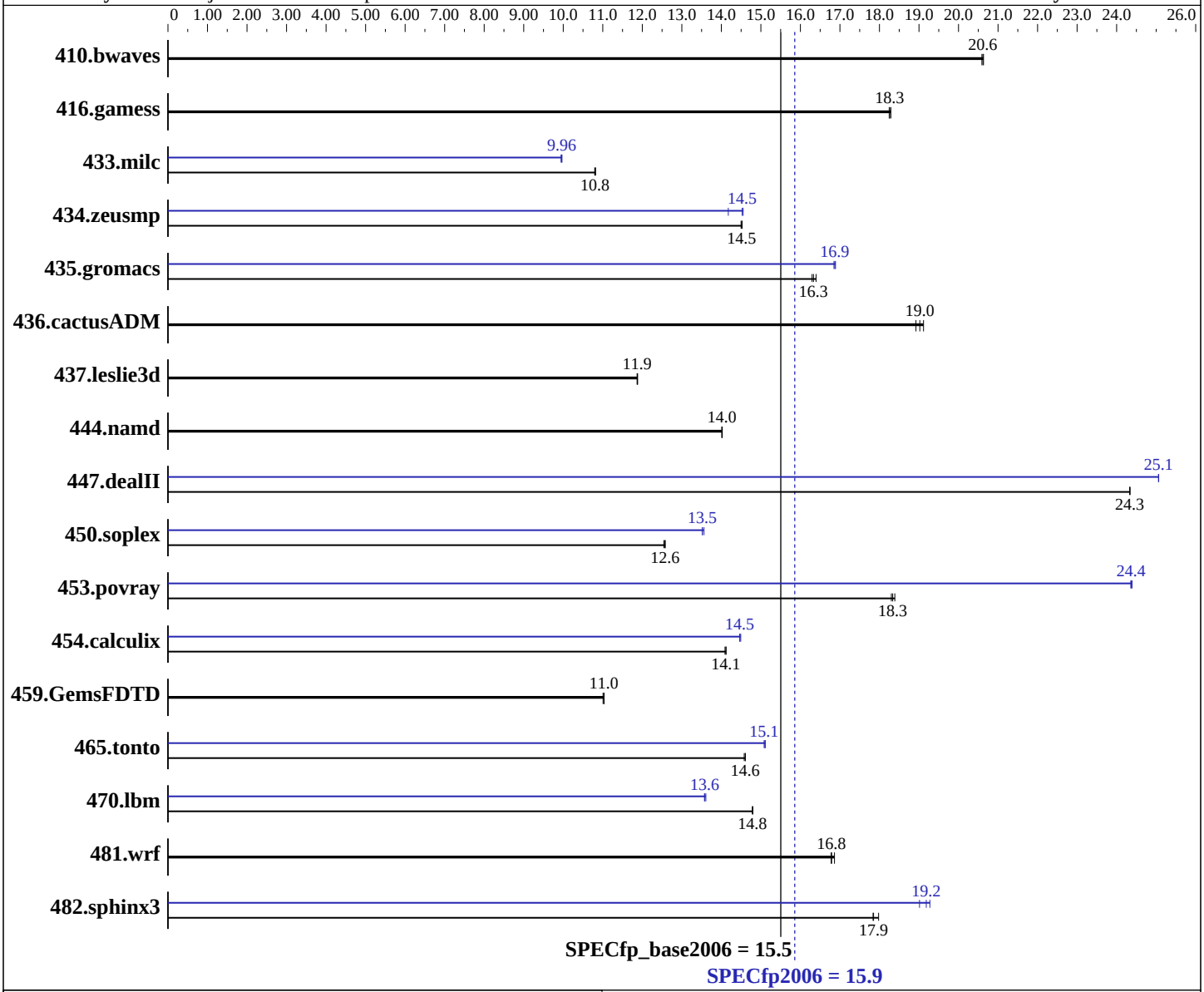
PRIMERGY RX200 S3, Intel Xeon processor 5150, 2.66 GHz

SPECfp®2006 = 15.9

SPECfp_base2006 = 15.5

CPU2006 license: 22
Test sponsor: Fujitsu Siemens Computers
Tested by: Fujitsu Siemens Computers

Test date: May-2007
Hardware Availability: Jul-2006
Software Availability: Feb-2007



Hardware

CPU Name:	Intel Xeon 5150
CPU Characteristics:	1333 MHz system bus
CPU MHz:	2667
FPU:	Integrated
CPU(s) enabled:	2 cores, 1 chip, 2 cores/chip
CPU(s) orderable:	1,2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	4 MB I+D on chip per chip

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Software

Operating System:	64-Bit SUSE LINUX Enterprise Server 10, Kernel 2.6.16.21-0.8-smp on an x86_64
Compiler:	Intel C++ Compiler for IA32/EM64T application, Version 9.1 - Build 20070215, Package-ID: l_cc_p_9.1.047 Intel Fortran Compiler for IA32/EM64T application, Version 9.1 - Build 20070215, Package ID: l_fc_p_9.1.043
Auto Parallel:	No
File System:	ext2

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L3 Cache: None
Other Cache: None
Memory: 16 GB (8x2 GB DDR2 PC2-5300F, 2 rank, CAS 5-5-5, with ECC)
Disk Subsystem: SAS (73GB 15400 rpm)
Other Hardware: None

System State: Multiuser, Runlevel 3
Base Pointers: 64-bit
Peak Pointers: 32/64-bit
Other Software: None

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	660	20.6	**659**	**20.6**	659	20.6	660	20.6	**659**	**20.6**	659	20.6
416.gamess	**1072**	**18.3**	1073	18.2	1071	18.3	**1072**	**18.3**	1073	18.2	1071	18.3
433.milc	850	10.8	**849**	**10.8**	849	10.8	921	9.97	923	9.94	**921**	**9.96**
434.zeusmp	628	14.5	**627**	**14.5**	626	14.5	642	14.2	**626**	**14.5**	625	14.6
435.gromacs	438	16.3	**437**	**16.3**	435	16.4	423	16.9	424	16.8	**423**	**16.9**
436.cactusADM	632	18.9	625	19.1	**628**	**19.0**	632	18.9	625	19.1	**628**	**19.0**
437.leslie3d	792	11.9	791	11.9	**792**	**11.9**	792	11.9	791	11.9	**792**	**11.9**
444.namd	572	14.0	572	14.0	**572**	**14.0**	572	14.0	572	14.0	**572**	**14.0**
447.dealII	470	24.3	**470**	**24.3**	470	24.3	**456**	**25.1**	457	25.1	456	25.1
450.soplex	665	12.5	**663**	**12.6**	663	12.6	617	13.5	**617**	**13.5**	615	13.6
453.povray	291	18.3	**290**	**18.3**	289	18.4	**218**	**24.4**	218	24.4	218	24.4
454.calculix	585	14.1	**584**	**14.1**	584	14.1	**570**	**14.5**	570	14.5	569	14.5
459.GemsFDTD	964	11.0	**962**	**11.0**	962	11.0	964	11.0	**962**	**11.0**	962	11.0
465.tonto	**674**	**14.6**	674	14.6	675	14.6	652	15.1	651	15.1	**652**	**15.1**
470.lbm	929	14.8	928	14.8	**929**	**14.8**	1010	13.6	**1012**	**13.6**	1012	13.6
481.wrf	666	16.8	662	16.9	**665**	**16.8**	666	16.8	662	16.9	**665**	**16.8**
482.sphinx3	1084	18.0	**1092**	**17.9**	1093	17.8	1011	19.3	**1016**	**19.2**	1025	19.0

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run

General Notes

The system bus runs at 1333 MHz

All binaries were built with 64-bit Intel compiler except:
433.milc, 434.zeusmp, 450.soplex, 470.lbm and 482.sphinx3 in peak were built with 32-bit Intel compiler by changing the path for include and library files.

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http://www.fujitsu-siemens.com/countries

Base Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc

Fortran benchmarks:
ifort

Benchmarks using both Fortran and C:
icc ifort

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:
-fast

C++ benchmarks:
-fast

Fortran benchmarks:
-fast

Benchmarks using both Fortran and C:
-fast

Peak Compiler Invocation

C benchmarks:
```
/opt/intel/cc/9.1.047/bin/icc -I/opt/intel/cc/9.1.047/include
-L/opt/intel/cc/9.1.047/lib
```

C++ benchmarks (except as noted below):
```
icpc
```

450.soplex: `/opt/intel/cc/9.1.047/bin/icpc -I/opt/intel/cc/9.1.047/include -L/opt/intel/cc/9.1.047/lib`

Fortran benchmarks (except as noted below):
```
ifort
```

434.zeusmp: `/opt/intel/fc/9.1.043/bin/ifort -I/opt/intel/fc/9.1.043/include -L/opt/intel/fc/9.1.043/lib`

Benchmarks using both Fortran and C:
```
icc ifort
```

Peak Portability Flags

410.bwaves: `-DSPEC_CPU_LP64`
416.gamess: `-DSPEC_CPU_LP64`
435.gromacs: `-DSPEC_CPU_LP64 -nofor_main`
436.cactusADM: `-DSPEC_CPU_LP64 -nofor_main`
437.leslie3d: `-DSPEC_CPU_LP64`
444.namd: `-DSPEC_CPU_LP64`
447.dealII: `-DSPEC_CPU_LP64`
453.povray: `-DSPEC_CPU_LP64`
454.calculix: `-DSPEC_CPU_LP64 -nofor_main`
459.GemsFDTD: `-DSPEC_CPU_LP64`
465.tonto: `-DSPEC_CPU_LP64`
481.wrf: `-DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX`

Peak Optimization Flags

C benchmarks:

433.milc: `-prof_gen`(pass 1) `-prof_use`(pass 2) `-fast`

470.lbm: `Same as 433.milc`

482.sphinx3: `-fast`

C++ benchmarks:

Continued on next page

Peak Optimization Flags (Continued)

444.namd: `basepeak = yes`

447.dealII: `-prof_gen`(pass 1) `-prof_use`(pass 2) `-fast`

450.soplex: `Same as 447.dealII`

453.povray: `Same as 447.dealII`

Fortran benchmarks:

410.bwaves: `basepeak = yes`

416.gamess: `basepeak = yes`

434.zeusmp: `-fast`

437.leslie3d: `basepeak = yes`

459.GemsFDTD: `basepeak = yes`

465.tonto: `-prof_gen`(pass 1) `-prof_use`(pass 2) `-fast`

Benchmarks using both Fortran and C:

435.gromacs: `-prof_gen`(pass 1) `-prof_use`(pass 2) `-fast`

436.cactusADM: `basepeak = yes`

454.calculix: `Same as 435.gromacs`

481.wrf: `basepeak = yes`

The flags file that was used to format this result can be browsed at
`http://www.spec.org/cpu2006/flags/CPU2006_flags.20090714.09.html`

You can also download the XML flags source by saving the following link:
`http://www.spec.org/cpu2006/flags/CPU2006_flags.20090714.09.xml`

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