SPEC® CINT2006 Result

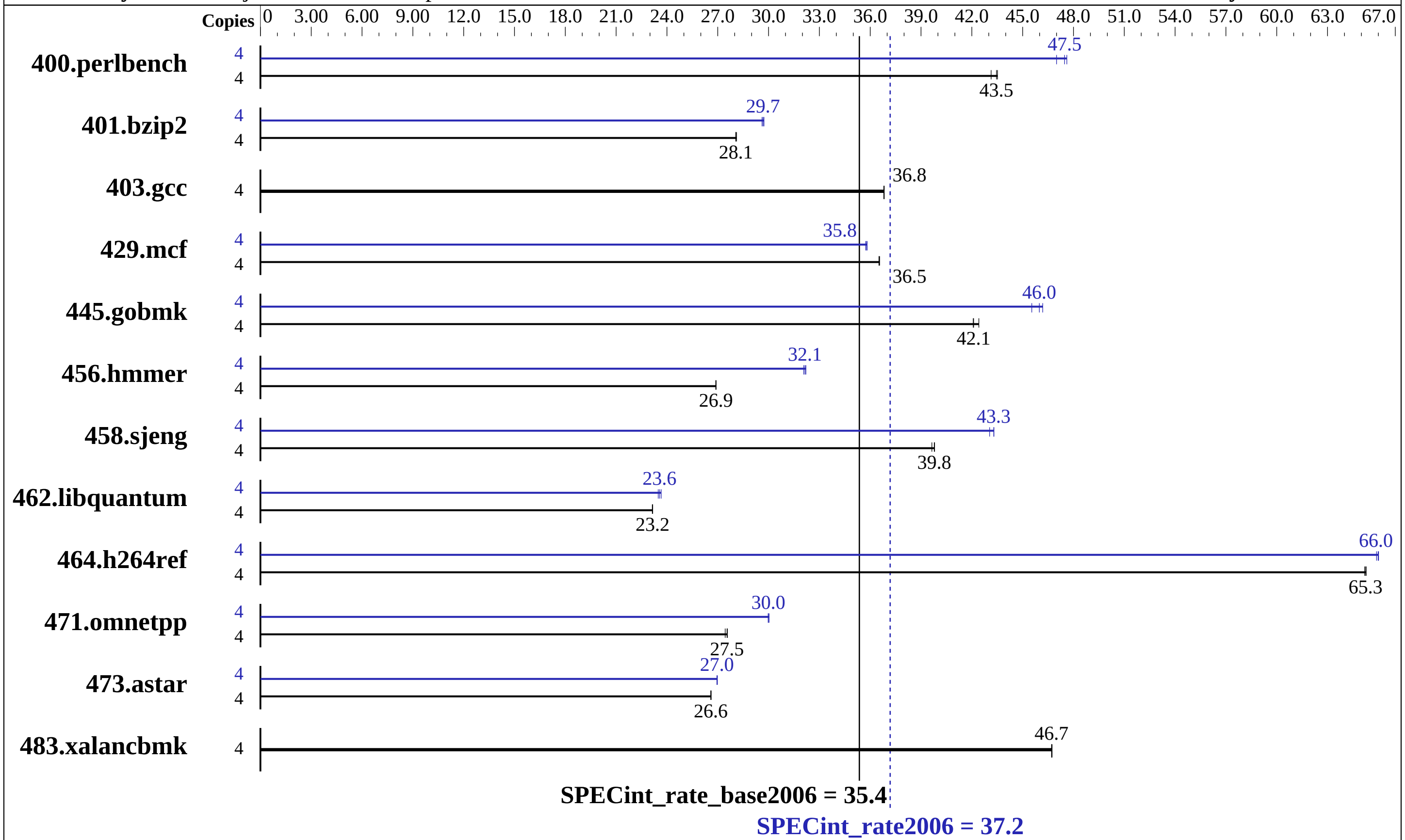
Fujitsu Siemens Computers

PRIMERGY RX200 S3, Intel Xeon processor 5110, 1.60 GHz

SPECint®_rate2006 = 37.2

SPECint_rate_base2006 = 35.4

CPU2006 license: 22		**Test date:**	May-2007
Test sponsor:	Fujitsu Siemens Computers	**Hardware Availability:**	Jul-2006
Tested by:	Fujitsu Siemens Computers	**Software Availability:**	Mar-2007



Hardware

CPU Name:	Intel Xeon 5110
CPU Characteristics:	1067 MHz system bus
CPU MHz:	1600
FPU:	Integrated
CPU(s) enabled:	4 cores, 2 chips, 2 cores/chip
CPU(s) orderable:	1,2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	4 MB I+D on chip per chip
L3 Cache:	None
Other Cache:	None
Memory:	8 GB (8x1 GB DDR2 PC2-5300F, 2 rank, CAS 5-5-5, with ECC)
Disk Subsystem:	SAS (73GB 15400 rpm)
Other Hardware:	None

Software

Operating System:	64-Bit SUSE LINUX Enterprise Server 10, Kernel 2.6.16.21-0.8-smp on an x86_64
Compiler:	Intel C++ Compiler for IA32/EM64T application, Version 9.1 - Build 20070215, Package-ID: l_cc_p_9.1.047
Auto Parallel:	No
File System:	ext2
System State:	Multiuser, Runlevel 3
Base Pointers:	32-bit
Peak Pointers:	32/64-bit
Other Software:	Smart Heap Library, Version 8.1

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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	4	**899**	**43.5**	898	43.5	906	43.1	4	821	47.6	**823**	**47.5**	831	47.0
401.bzip2	4	1376	28.1	**1375**	**28.1**	1373	28.1	4	**1301**	**29.7**	1298	29.7	1303	29.6
403.gcc	4	**875**	**36.8**	875	36.8	874	36.8	4	**875**	**36.8**	875	36.8	874	36.8
429.mcf	4	**999**	**36.5**	998	36.6	999	36.5	4	1018	35.8	**1019**	**35.8**	1021	35.7
445.gobmk	4	989	42.4	997	42.1	**997**	**42.1**	4	908	46.2	**912**	**46.0**	921	45.5
456.hmmer	4	1387	26.9	1388	26.9	**1388**	**26.9**	4	1164	32.1	**1161**	**32.1**	1159	32.2
458.sjeng	4	**1217**	**39.8**	1216	39.8	1221	39.6	4	**1118**	**43.3**	1118	43.3	1124	43.0
462.libquantum	4	3582	23.1	**3579**	**23.2**	3578	23.2	4	**3517**	**23.6**	3503	23.7	3529	23.5
464.h264ref	4	1356	65.3	1358	65.2	**1357**	**65.3**	4	1341	66.0	1343	65.9	**1341**	**66.0**
471.omnetpp	4	907	27.6	**908**	**27.5**	911	27.4	4	834	30.0	832	30.0	**834**	**30.0**
473.astar	4	1056	26.6	**1056**	**26.6**	1055	26.6	4	1041	27.0	1042	27.0	**1042**	**27.0**
483.xalancbmk	4	**591**	**46.7**	591	46.7	591	46.7	4	**591**	**46.7**	591	46.7	591	46.7

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

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'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run
'/usr/bin/taskset' used to bind processes to CPUs
```

General Notes

```
The system bus runs at 1067 MHz

All binaries were built with 32-bit Intel compiler except:
401.bzip2, 456.hmmer and 462.libquantum in peak were built with
64-bit Intel compiler by changing the path for include and library files.

BIOS configuration:
Adjacent Sector Prefetch = Disable

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http://www.fujitsu-siemens.com/countries
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Base Compiler Invocation

C benchmarks:
`icc`

C++ benchmarks:
`icpc`

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Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_X64
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
-fast

C++ benchmarks:
-xP -O3 -ipo -no-prec-div -L/opt/SmartHeap_8_1/lib -lsmartheap

Peak Compiler Invocation

C benchmarks (except as noted below):
icc

401.bzip2: /opt/intel/cce/9.1.047/bin/icc -I/opt/intel/cce/9.1.047/include -L/opt/intel/cce/9.1.047/lib

456.hmmer: /opt/intel/cce/9.1.047/bin/icc -I/opt/intel/cce/9.1.047/include -L/opt/intel/cce/9.1.047/lib

462.libquantum: /opt/intel/cce/9.1.047/bin/icc -I/opt/intel/cce/9.1.047/include -L/opt/intel/cce/9.1.047/lib

C++ benchmarks:
icpc

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_X64
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: `-prof_gen`(pass 1) `-prof_use`(pass 2) `-fast`

401.bzip2: `-fast`

403.gcc: `basepeak = yes`

429.mcf: `-prof_gen`(pass 1) `-prof_use`(pass 2) `-fast` `-L/opt/SmartHeap_8_1/lib -lsmartheap`

445.gobmk: `Same as 429.mcf`

456.hmmer: `Same as 400.perlbench`

458.sjeng: `Same as 429.mcf`

462.libquantum: `Same as 400.perlbench`

464.h264ref: `Same as 429.mcf`

C++ benchmarks:

471.omnetpp: `-prof_gen`(pass 1) `-prof_use`(pass 2) `-xP -O3 -ipo` `-no-prec-div -L/opt/SmartHeap_8_1/lib -lsmartheap`

473.astar: `-prof_gen`(pass 1) `-prof_use`(pass 2) `-fast` `-L/opt/SmartHeap_8_1/lib -lsmartheap`

483.xalancbmk: `basepeak = yes`

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/CPU2006_flags.20090714.09.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/CPU2006_flags.20090714.09.xml

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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