



SPEC® CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Fujitsu Siemens Computers

PRIMERGY RX200 S3, Intel Xeon processor E5335,
2.0 GHz

SPECint_rate2006 = 76.9

SPECint_rate_base2006 = 72.6

CPU2006 license: 22

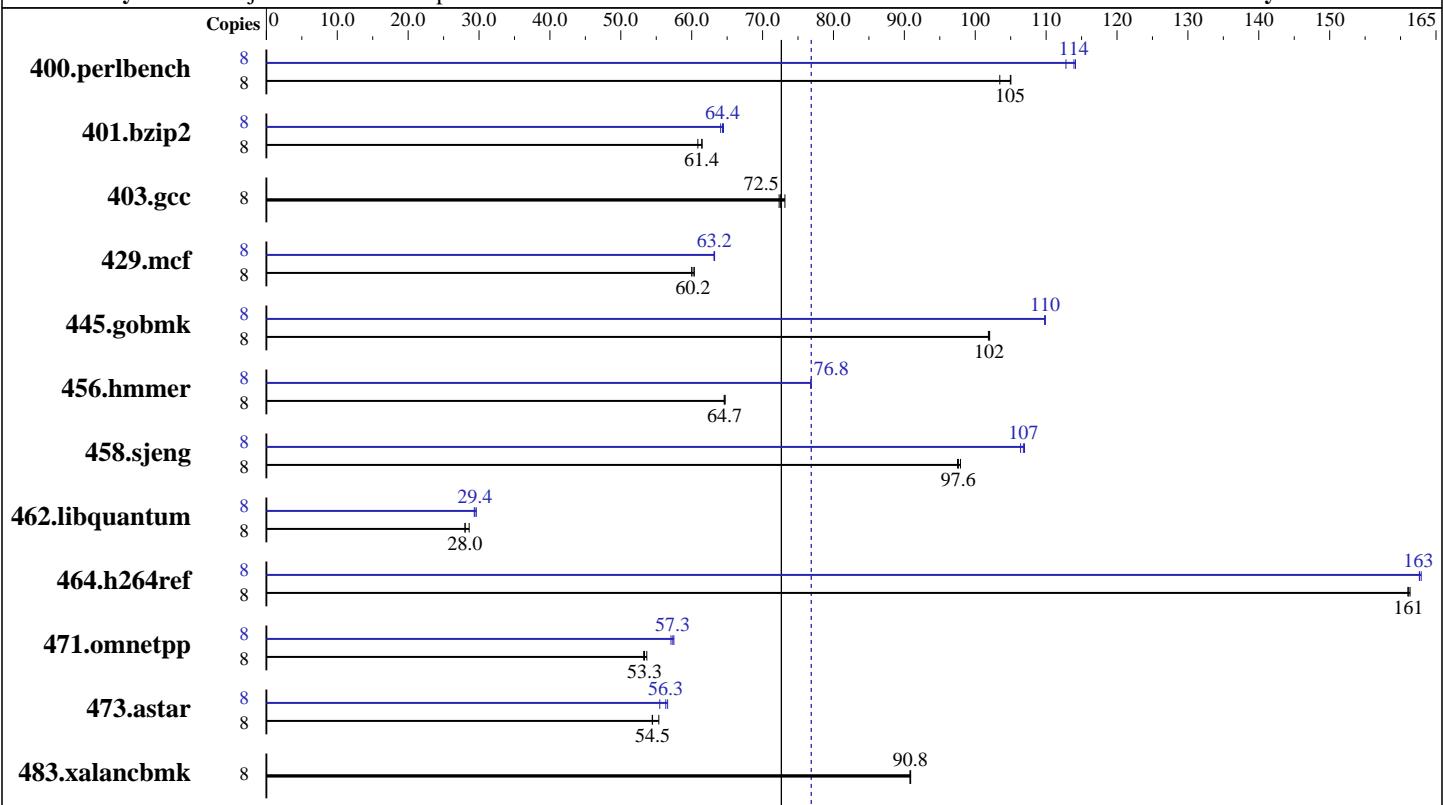
Test sponsor: Fujitsu Siemens Computers

Tested by: Fujitsu Siemens Computers

Test date: May-2007

Hardware Availability: Feb-2007

Software Availability: Mar-2007



SPECint_rate_base2006 = 72.6

SPECint_rate2006 = 76.9

Hardware

CPU Name: Intel Xeon E5335
CPU Characteristics: 1333 MHz system bus
CPU MHz: 2000
FPU: Integrated
CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip
CPU(s) orderable: 1,2 chips
Primary Cache: 32 KB I + 32 KB D on chip per core
Secondary Cache: 8 MB I+D on chip per chip, 4 MB shared / 2 cores
L3 Cache: None
Other Cache: None
Memory: 16 GB (8x2 GB DDR2 PC2-5300F, 2 rank, CAS 5-5-5, with ECC)
Disk Subsystem: SAS (73GB 15400 rpm)
Other Hardware: None

Software

Operating System: 64-Bit SUSE LINUX Enterprise Server 10, Kernel 2.6.16.21-0.8-smp on an x86_64
Compiler: Intel C++ Compiler for IA32/EM64T application, Version 9.1 - Build 20070215, Package-ID: l_cc_p_9.1.047
Auto Parallel: No
File System: ext2
System State: Multiuser, Runlevel 3
Base Pointers: 32-bit
Peak Pointers: 32/64-bit
Other Software: Smart Heap Library, Version 8.1



SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Fujitsu Siemens Computers

PRIMERGY RX200 S3, Intel Xeon processor E5335,
2.0 GHz

SPECint_rate2006 = 76.9

SPECint_rate_base2006 = 72.6

CPU2006 license: 22

Test date: May-2007

Test sponsor: Fujitsu Siemens Computers

Hardware Availability: Feb-2007

Tested by: Fujitsu Siemens Computers

Software Availability: Mar-2007

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	8	745	105	755	103	744	105	8	693	113	686	114	685	114
401.bzip2	8	1268	60.9	1257	61.4	1256	61.5	8	1205	64.1	1200	64.4	1197	64.5
403.gcc	8	880	73.1	888	72.5	890	72.3	8	880	73.1	888	72.5	890	72.3
429.mcf	8	1212	60.2	1216	60.0	1208	60.4	8	1155	63.1	1154	63.2	1154	63.2
445.gobmk	8	824	102	823	102	822	102	8	764	110	764	110	764	110
456.hammer	8	1153	64.7	1156	64.6	1154	64.7	8	971	76.8	971	76.8	972	76.8
458.sjeng	8	993	97.5	992	97.6	989	97.9	8	910	106	906	107	905	107
462.libquantum	8	5911	28.0	5916	28.0	5794	28.6	8	5597	29.6	5635	29.4	5652	29.3
464.h264ref	8	1099	161	1099	161	1097	161	8	1088	163	1088	163	1087	163
471.omnetpp	8	937	53.3	939	53.2	932	53.7	8	870	57.5	872	57.3	876	57.1
473.astar	8	1031	54.5	1032	54.4	1014	55.4	8	1012	55.5	992	56.6	997	56.3
483.xalancbmk	8	608	90.8	608	90.8	607	90.9	8	608	90.8	608	90.8	607	90.9

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run
'/usr/bin/taskset' used to bind processes to CPUs

General Notes

The system bus runs at 1333 MHz

All binaries were built with 32-bit Intel compiler except:
401.bzip2, 456.hammer and 462.libquantum in peak were built with
64-bit Intel compiler by changing the path for include and library files.

BIOS configuration:
Hardware Prefetch = Disable, Adjacent Sector Prefetch = Disable

For information about Fujitsu Siemens Computers in your country please see:
<http://www.fujitsu-siemens.com/countries>

Base Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc



SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Fujitsu Siemens Computers

PRIMERGY RX200 S3, Intel Xeon processor E5335,
2.0 GHz

SPECint_rate2006 = 76.9

SPECint_rate_base2006 = 72.6

CPU2006 license: 22

Test date: May-2007

Test sponsor: Fujitsu Siemens Computers

Hardware Availability: Feb-2007

Tested by: Fujitsu Siemens Computers

Software Availability: Mar-2007

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_X64
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
-fast

C++ benchmarks:
-xP -O3 -ipo -no-prec-div -L/opt/SmartHeap_8_1/lib -lsmartheap

Peak Compiler Invocation

C benchmarks (except as noted below):
icc

401.bzip2: /opt/intel/cce/9.1.047/bin/icc
-I/opt/intel/cce/9.1.047/include
-L/opt/intel/cce/9.1.047/lib

456.hmmr: /opt/intel/cce/9.1.047/bin/icc
-I/opt/intel/cce/9.1.047/include
-L/opt/intel/cce/9.1.047/lib

462.libquantum: /opt/intel/cce/9.1.047/bin/icc
-I/opt/intel/cce/9.1.047/include
-L/opt/intel/cce/9.1.047/lib

C++ benchmarks:
icpc

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_X64
401.bzip2: -DSPEC_CPU_LP64
456.hmmr: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX



SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Fujitsu Siemens Computers

PRIMERGY RX200 S3, Intel Xeon processor E5335,
2.0 GHz

SPECint_rate2006 = 76.9

SPECint_rate_base2006 = 72.6

CPU2006 license: 22

Test date: May-2007

Test sponsor: Fujitsu Siemens Computers

Hardware Availability: Feb-2007

Tested by: Fujitsu Siemens Computers

Software Availability: Mar-2007

Peak Optimization Flags

C benchmarks:

400.perlbench: -prof_gen(pass 1) -prof_use(pass 2) -fast

401.bzip2: -fast

403.gcc: basepeak = yes

429.mcf: -prof_gen(pass 1) -prof_use(pass 2) -fast
-L/opt/SmartHeap_8_1/lib -lsmartheap

445.gobmk: Same as 429.mcf

456.hmmer: Same as 400.perlbench

458.sjeng: Same as 429.mcf

462.libquantum: Same as 400.perlbench

464.h264ref: Same as 429.mcf

C++ benchmarks:

471.omnetpp: -prof_gen(pass 1) -prof_use(pass 2) -xP -O3 -ipo
-no-prec-div -L/opt/SmartHeap_8_1/lib -lsmartheap

473.astar: -prof_gen(pass 1) -prof_use(pass 2) -fast
-L/opt/SmartHeap_8_1/lib -lsmartheap

483.xalancbmk: basepeak = yes

The flags file that was used to format this result can be browsed at

http://www.spec.org/cpu2006/flags/CPU2006_flags.20090714.09.html

You can also download the XML flags source by saving the following link:

http://www.spec.org/cpu2006/flags/CPU2006_flags.20090714.09.xml

SPEC and SPECint are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.0.

Report generated on Tue Jul 22 10:59:39 2014 by SPEC CPU2006 PS/PDF formatter v6932.

Originally published on 12 June 2007.