## Fujitsu Siemens Computers

**PRIMERGY TX150 S5, Intel Xeon processor 3060, 2.40 GHz**

<table>
<thead>
<tr>
<th>SPECint_rate2006</th>
<th>SPECint_rate_base2006</th>
</tr>
</thead>
<tbody>
<tr>
<td><strong>28.5</strong></td>
<td><strong>27.2</strong></td>
</tr>
</tbody>
</table>

### CPU2006 license: 22

**Test date:** May-2007  
**Hardware Availability:** Sep-2006  
**Software Availability:** Mar-2007

### Hardware

<table>
<thead>
<tr>
<th>CPU Name:</th>
<th>Intel Xeon 3060</th>
</tr>
</thead>
<tbody>
<tr>
<td>CPU MHz:</td>
<td>2400</td>
</tr>
<tr>
<td>FPU:</td>
<td>Integrated</td>
</tr>
<tr>
<td>CPU(s) enabled:</td>
<td>2 cores, 1 chip, 2 cores/chip</td>
</tr>
<tr>
<td>CPU(s) orderable:</td>
<td>1 chip</td>
</tr>
<tr>
<td>Primary Cache:</td>
<td>32 KB I + 32 KB D on chip per core</td>
</tr>
<tr>
<td>Secondary Cache:</td>
<td>4 MB I+D on chip per chip</td>
</tr>
<tr>
<td>L3 Cache:</td>
<td>None</td>
</tr>
<tr>
<td>Other Cache:</td>
<td>None</td>
</tr>
<tr>
<td>Memory:</td>
<td>8 GB (4x2 GB DDR2 PC2-4200E, 2 rank, CAS 4-4-4, with ECC)</td>
</tr>
<tr>
<td>Disk Subsystem:</td>
<td>SATA (160 GB 7200 rpm)</td>
</tr>
<tr>
<td>Other Hardware:</td>
<td>None</td>
</tr>
</tbody>
</table>

### Software

- **Operating System:** 64-Bit SUSE LINUX Enterprise Server 10, Kernel 2.6.16.21-0.8-smp on an x86_64
- **Compiler:** Intel C++ Compiler for IA32/EM64T application, Version 9.1 - Build 20070215, Package-ID: l_cc_p_9.1.047
- **Auto Parallel:** No
- **File System:** ReiserFS
- **System State:** Multiusr, Runlevel 3
- **Base Pointers:** 32-bit
- **Peak Pointers:** 32/64-bit
- **Other Software:** Smart Heap Library, Version 8.1
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SPECint_rate_base2006 = 27.2

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Results Table

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Copies</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
</tr>
</thead>
<tbody>
<tr>
<td></td>
<td>Base</td>
<td></td>
<td></td>
<td>Peak</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>400.perlbench</td>
<td>2</td>
<td>595</td>
<td>32.9</td>
<td>600</td>
<td>32.6</td>
<td>598</td>
<td>32.7</td>
</tr>
<tr>
<td>401.bzip2</td>
<td>2</td>
<td>859</td>
<td>22.5</td>
<td>855</td>
<td>22.6</td>
<td>849</td>
<td>22.7</td>
</tr>
<tr>
<td>403.gcc</td>
<td>2</td>
<td>576</td>
<td>30.4</td>
<td>579</td>
<td>30.5</td>
<td>598</td>
<td>30.5</td>
</tr>
<tr>
<td>429.mcf</td>
<td>2</td>
<td>666</td>
<td>31.5</td>
<td>666</td>
<td>31.5</td>
<td>666</td>
<td>31.5</td>
</tr>
<tr>
<td>445.gobmk</td>
<td>2</td>
<td>926</td>
<td>20.2</td>
<td>926</td>
<td>20.2</td>
<td>925</td>
<td>20.2</td>
</tr>
<tr>
<td>456.hmmer</td>
<td>2</td>
<td>2257</td>
<td>18.4</td>
<td>2255</td>
<td>18.4</td>
<td>2256</td>
<td>18.4</td>
</tr>
<tr>
<td>464.h264ref</td>
<td>2</td>
<td>894</td>
<td>49.5</td>
<td>894</td>
<td>49.5</td>
<td>892</td>
<td>49.6</td>
</tr>
<tr>
<td>471.omnetpp</td>
<td>2</td>
<td>611</td>
<td>20.5</td>
<td>611</td>
<td>20.5</td>
<td>612</td>
<td>20.4</td>
</tr>
<tr>
<td>473.astar</td>
<td>2</td>
<td>701</td>
<td>20.0</td>
<td>699</td>
<td>20.1</td>
<td>702</td>
<td>20.0</td>
</tr>
<tr>
<td>483.xalancbmk</td>
<td>2</td>
<td>380</td>
<td>36.3</td>
<td>382</td>
<td>36.1</td>
<td>382</td>
<td>36.1</td>
</tr>
</tbody>
</table>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run '/usr/bin/taskset' used to bind processes to CPUs

General Notes

The system bus runs at 1067 MHz

All binaries were built with 32-bit Intel compiler except:
401.bzip2, 456.hmmer and 462.libquantum in peak were built with 64-bit Intel compiler by changing the path for include and library files.

BIOS configuration:
Adjacent Sector Prefetch = Disable

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Base Compiler Invocation

C benchmarks:
  icc

C++ benchmarks:
  icpc
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Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_X64
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
- fast

C++ benchmarks:
- -xP -O3 -ipo -no-prec-div -L/opt/SmartHeap_8_1/lib -lsmartheap

Peak Compiler Invocation

C benchmarks (except as noted below):
  icc

  401.bzip2: /opt/intel/cce/9.1.047/bin/icc
  -I/opt/intel/cce/9.1.047/include
  -L/opt/intel/cce/9.1.047/lib

  456.hmmer: /opt/intel/cce/9.1.047/bin/icc
  -I/opt/intel/cce/9.1.047/include
  -L/opt/intel/cce/9.1.047/lib

  462.libquantum: /opt/intel/cce/9.1.047/bin/icc
  -I/opt/intel/cce/9.1.047/include
  -L/opt/intel/cce/9.1.047/lib

C++ benchmarks:
  icpc

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_X64
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX
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Peak Optimization Flags

C benchmarks:

400.perlbench: -prof_gen(pass 1) -prof_use(pass 2) -fast
401.bzip2: -fast
403.gcc: basepeak = yes
429.mcf: -prof_gen(pass 1) -prof_use(pass 2) -fast
-L/opt/SmartHeap_8_1/lib -lsmartheap
445.gobmk: Same as 429.mcf
456.hmmer: Same as 400.perlbench
458.sjeng: Same as 429.mcf
462.libquantum: Same as 400.perlbench
464.h264ref: Same as 429.mcf

C++ benchmarks:

471.omnetpp: -prof_gen(pass 1) -prof_use(pass 2) -xP -O3 -ipo
-no-prec-div -L/opt/SmartHeap_8_1/lib -lsmartheap
473.astar: -prof_gen(pass 1) -prof_use(pass 2) -fast
-L/opt/SmartHeap_8_1/lib -lsmartheap
483.xalancbmk: basepeak = yes

The flags file that was used to format this result can be browsed at

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/CPUCPU2006_flags.20090714.09.xml

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For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.0.