SPEC® CFP2006 Result

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Bull SAS

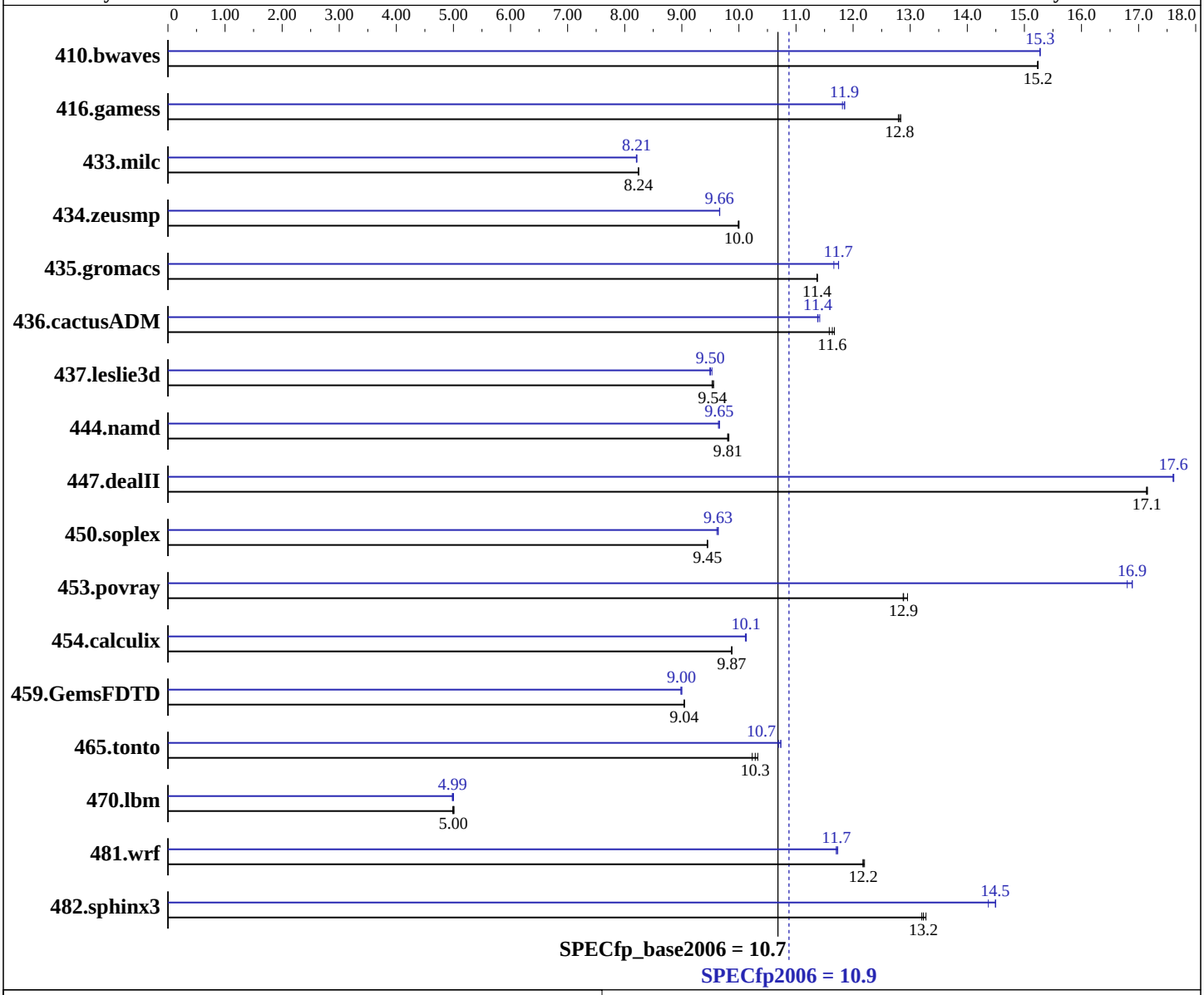
NovaScale R460
(Intel Xeon processor 5120,1.86GHz)

SPECfp®2006 = 10.9

SPECfp_base2006 = 10.7

CPU2006 license: 20
Test sponsor: Bull SAS
Tested by: Bull SAS

Test date: May-2007
Hardware Availability: Mar-2007
Software Availability: Dec-2006



Hardware

CPU Name:	Intel Xeon 5120
CPU Characteristics:	1.86 GHz, 4 MB L2, 1066 MHz system bus
CPU MHz:	1866
FPU:	Integrated
CPU(s) enabled:	1 core, 1 chip, 2 cores/chip
CPU(s) orderable:	1 to 2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	4 MB I+D on chip per chip

Continued on next page

Software

Operating System:	SuSE Linux Enterprise Server 10 (EM64T) kernel 2.6.16.21-0.8-smp
Compiler:	Intel C++ Compiler for Intel EM64T-based applications, Version 9.1 Package ID l_cc_c_9.1.045 Build no 20061101 Intel Fortran Compiler for Intel EM64T-based applications, Version 9.1 Package ID l_fc_c_9.1.040 Build no 20061101
Auto Parallel:	No

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L3 Cache: None
Other Cache: None
Memory: 24 GB (12x2 GB) FB-DIMM PC2-5300F ECC CL5
Disk Subsystem: 1x73 GB SAS, 15000 RPM
Other Hardware: None

File System: ext2
System State: Multi-user run level 3
Base Pointers: 64-bit
Peak Pointers: 32/64-bit
Other Software: None

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	**892**	**15.2**	892	15.2	892	15.2	890	15.3	890	15.3	**890**	**15.3**
416.gamess	1526	12.8	1531	12.8	**1528**	**12.8**	**1652**	**11.9**	1652	11.9	1657	11.8
433.milc	1114	8.24	1114	8.24	**1114**	**8.24**	**1118**	**8.21**	1119	8.20	1118	8.21
434.zeusmp	910	10.0	911	9.99	**910**	**10.0**	**942**	**9.66**	942	9.66	942	9.66
435.gromacs	**628**	**11.4**	628	11.4	628	11.4	612	11.7	**608**	**11.7**	608	11.7
436.cactusADM	**1027**	**11.6**	1032	11.6	1024	11.7	1050	11.4	**1050**	**11.4**	1047	11.4
437.leslie3d	**985**	**9.54**	986	9.53	983	9.56	987	9.53	**989**	**9.50**	990	9.49
444.namd	818	9.80	816	9.83	**818**	**9.81**	830	9.66	832	9.64	**831**	**9.65**
447.dealII	668	17.1	**667**	**17.1**	667	17.2	**650**	**17.6**	649	17.6	650	17.6
450.soplex	**883**	**9.45**	882	9.45	883	9.45	**866**	**9.63**	865	9.64	867	9.61
453.povray	413	12.9	411	13.0	**413**	**12.9**	**315**	**16.9**	317	16.8	315	16.9
454.calculix	835	9.88	**836**	**9.87**	836	9.87	816	10.1	814	10.1	**815**	**10.1**
459.GemsFDTD	1173	9.04	1173	9.04	**1173**	**9.04**	1179	9.00	1181	8.98	**1179**	**9.00**
465.tonto	952	10.3	962	10.2	**957**	**10.3**	917	10.7	920	10.7	**917**	**10.7**
470.lbm	2742	5.01	**2747**	**5.00**	2754	4.99	2747	5.00	**2755**	**4.99**	2758	4.98
481.wrf	**917**	**12.2**	918	12.2	916	12.2	952	11.7	954	11.7	**954**	**11.7**
482.sphinx3	1468	13.3	**1473**	**13.2**	1476	13.2	1344	14.5	1356	14.4	**1345**	**14.5**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

```
Environment stack size set to 'unlimited'

system was booted uniprocessor by setting "maxcpus=0"
kernel parameter in menu.lst

/usr/bin/taskset utility used to bind CPU(s) to processes
```

General Notes

```
The NovaScale R440 and the NovaScale R460 models are
electronically equivalent.
The results have been measured on a NovaScale R460 model.
```

Base Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc

Fortran benchmarks:
ifort

Benchmarks using both Fortran and C:
icc ifort

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:
-fast

C++ benchmarks:
-fast

Fortran benchmarks:
-fast

Benchmarks using both Fortran and C:
-fast

Peak Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc

Fortran benchmarks:
ifort

Benchmarks using both Fortran and C:
icc ifort

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:
-prof_gen(pass 1) -prof_use(pass 2) -fast -auto_ilp32

C++ benchmarks:
-prof_gen(pass 1) -prof_use(pass 2) -fast -auto_ilp32

Fortran benchmarks:
-prof_gen(pass 1) -prof_use(pass 2) -fast

Benchmarks using both Fortran and C:
-prof_gen(pass 1) -prof_use(pass 2) -fast -auto_ilp32

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/EM64T_Intel91_flags.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/EM64T_Intel91_flags.xml

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For other inquiries, please contact webmaster@spec.org.

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