Bull SAS
NovaScale R440
(Intel Xeon processor 5130, 2.00GHz)

<table>
<thead>
<tr>
<th>SPECint®2006</th>
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<td>May-2007</td>
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**Hardware**

- **CPU Name:** Intel Xeon 5130
- **CPU Characteristics:** 2.00 GHz, 4 MB L2, 1333 MHz system bus
- **CPU MHz:** 2000
- **FPU:** Integrated
- **CPU(s) enabled:** 1 core, 1 chip, 2 cores/chip
- **CPU(s) orderable:** 1 to 2 chips
- **Primary Cache:** 32 KB I + 32 KB D on chip per core
- **Secondary Cache:** 4 MB I+D on chip per chip
- **L3 Cache:** None
- **Other Cache:** None
- **Memory:** 24 GB (12x2 GB) FB-DIMM PC2-5300F ECC CL5
- **Disk Subsystem:** 1x73 GB SAS, 15000 RPM
- **Other Hardware:** None

**Software**

- **Operating System:** Windows Server 2003 Enterprise Edition X64 Edition Service Pack1
- **Compiler:** Intel C++ Compiler for IA32 version 9.1
- **Auto Parallel:** No
- **File System:** NTFS
- **System State:** Default
- **Base Pointers:** 32-bit
- **Peak Pointers:** 32-bit
- **Other Software:** MicroQuill SmartHeap Library 8.0 (shlW32M.lib)
### SPEC CINT2006 Result

**Bull SAS**

NovaScale R440  
(Intel Xeon processor 5130,2.00GHz)

**SPEC2006 =** 12.9

**SPECint_base2006 =** 12.4

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#### Results Table

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Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

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#### Operating System Notes

/NUMPROC=1 flags was added to boot.ini invoke uniprocessor environment

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#### General Notes

The NovaScale R440 and the NovaScale R460 models are electronically equivalent.

The results have been measured on a NovaScale R460 model.

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#### Base Compiler Invocation

C benchmarks:

icl -Qvc7.1 -Qc99

C++ benchmarks:

icl -Qvc7.1

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#### Base Portability Flags

403.gcc: -DSPEC_CPU_WIN32  
464.h264ref: -DSPEC_CPU_NO_INTTYPES -DWIN32
SPEC CINT2006 Result

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SPECint_base2006 = 12.4

CPU2006 license: 20
Test sponsor: Bull SAS
Tested by: Bull SAS

Test date: May-2007
Hardware Availability: Feb-2007
Software Availability: Dec-2006

Base Optimization Flags

C benchmarks:
- fast /F512000000 shlw32m.lib -link /FORCE:MULTIPLE

C++ benchmarks:
- fast -Qcxx_features /F512000000 shlw32m.lib
  -link /FORCE:MULTIPLE

Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks:

icl -Qvc7.1 -Qc99

C++ benchmarks:

icl -Qvc7.1

Peak Portability Flags

403.gcc: -DSPEC_CPU_WIN32
464.h264ref: -DSPEC_CPU_NO_INTTYPES -DWIN32

Peak Optimization Flags

C benchmarks:

-Qprof_gen(pass 1) -Qprof_use(pass 2) -fast /F512000000 shlw32m.lib
  -link /FORCE:MULTIPLE

C++ benchmarks:

-Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qcxx_features
  /F512000000 shlw32m.lib -link /FORCE:MULTIPLE

Peak Other Flags

C benchmarks:

Continued on next page
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Peak Other Flags (Continued)
403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/flags.20090714.00.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/flags.20090714.00.xml

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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.0.