SPEC® CFP2006 Result

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Bull SAS

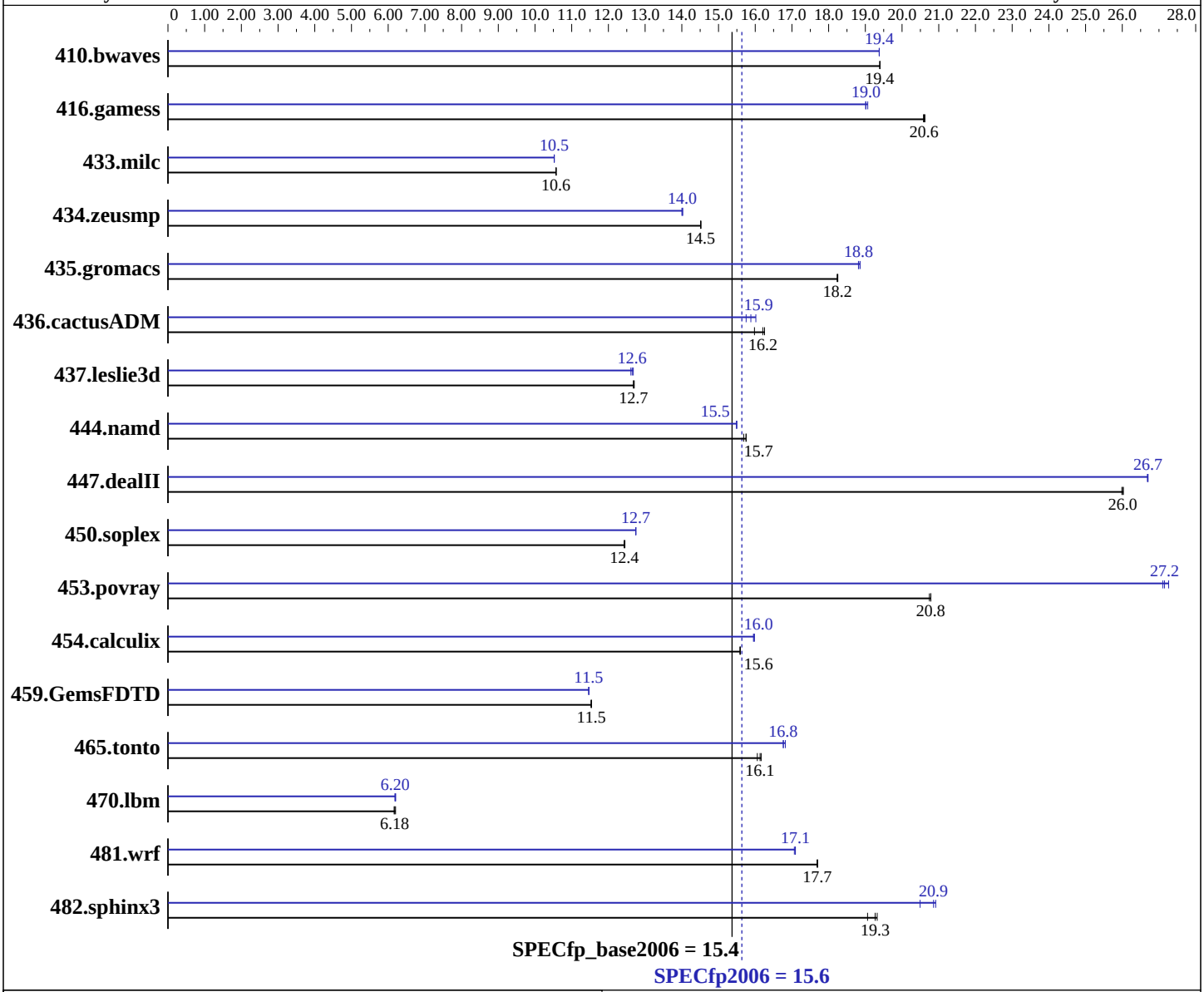
NovaScale R440
(Intel Xeon processor 5160,3.00GHz)

SPECfp®2006 = 15.6

SPECfp_base2006 = 15.4

CPU2006 license: 20
Test sponsor: Bull SAS
Tested by: Bull SAS

Test date: May-2007
Hardware Availability: Mar-2007
Software Availability: Dec-2006



Hardware

CPU Name:	Intel Xeon 5160
CPU Characteristics:	3.00 GHz, 4 MB L2, 1333 MHz system bus
CPU MHz:	3000
FPU:	Integrated
CPU(s) enabled:	1 core, 1 chip, 2 cores/chip
CPU(s) orderable:	1 to 2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	4 MB I+D on chip per chip

Continued on next page

Software

Operating System:	SuSE Linux Enterprise Server 10 (EM64T) kernel 2.6.16.21-0.8-smp
Compiler:	Intel C++ Compiler for Intel EM64T-based applications, Version 9.1 Package ID l_cc_c_9.1.045 Build no 20061101 Intel Fortran Compiler for Intel EM64T-based applications, Version 9.1 Package ID l_fc_c_9.1.040 Build no 20061101
Auto Parallel:	No

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L3 Cache: None
Other Cache: None
Memory: 24 GB (12x2 GB) FB-DIMM PC2-5300F ECC CL5
Disk Subsystem: 1x73 GB SAS, 15000 RPM
Other Hardware: None

File System: ext2
System State: Multi-user run level 3
Base Pointers: 64-bit
Peak Pointers: 32/64-bit
Other Software: None

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	**701**	**19.4**	701	19.4	701	19.4	**701**	**19.4**	701	19.4	701	19.4
416.gamess	949	20.6	**950**	**20.6**	951	20.6	1030	19.0	**1030**	**19.0**	1027	19.1
433.milc	869	10.6	868	10.6	**868**	**10.6**	**872**	**10.5**	872	10.5	872	10.5
434.zeusmp	**627**	**14.5**	627	14.5	627	14.5	**649**	**14.0**	649	14.0	650	14.0
435.gromacs	391	18.3	**391**	**18.2**	392	18.2	380	18.8	**379**	**18.8**	379	18.9
436.cactusADM	735	16.3	**737**	**16.2**	748	16.0	759	15.8	**752**	**15.9**	746	16.0
437.leslie3d	741	12.7	**741**	**12.7**	740	12.7	741	12.7	**743**	**12.6**	745	12.6
444.namd	509	15.8	**509**	**15.7**	511	15.7	**518**	**15.5**	518	15.5	518	15.5
447.dealII	439	26.0	440	26.0	**440**	**26.0**	429	26.7	428	26.7	**428**	**26.7**
450.soplex	**671**	**12.4**	671	12.4	670	12.4	654	12.7	**654**	**12.7**	654	12.7
453.povray	**256**	**20.8**	256	20.7	256	20.8	**196**	**27.2**	196	27.1	195	27.3
454.calculix	529	15.6	**529**	**15.6**	529	15.6	516	16.0	**517**	**16.0**	517	16.0
459.GemsFDTD	921	11.5	**920**	**11.5**	919	11.5	926	11.5	**925**	**11.5**	925	11.5
465.tonto	609	16.2	**610**	**16.1**	613	16.1	585	16.8	**587**	**16.8**	587	16.8
470.lbm	2216	6.20	2230	6.16	**2224**	**6.18**	2214	6.21	**2218**	**6.20**	2222	6.18
481.wrf	**631**	**17.7**	631	17.7	632	17.7	653	17.1	655	17.1	**653**	**17.1**
482.sphinx3	1009	19.3	1023	19.1	**1012**	**19.3**	932	20.9	**934**	**20.9**	951	20.5

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

```
Environment stack size set to 'unlimited'

system was booted uniprocessor by setting "maxcpus=0"
kernel parameter in menu.lst

/usr/bin/taskset utility used to bind CPU(s) to processes
```

General Notes

```
The NovaScale R440 and the NovaScale R460 models are
electronically equivalent.
The results have been measured on a NovaScale R460 model.
```

Base Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc

Fortran benchmarks:
ifort

Benchmarks using both Fortran and C:
icc ifort

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:
-fast

C++ benchmarks:
-fast

Fortran benchmarks:
-fast

Benchmarks using both Fortran and C:
-fast

Peak Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc

Fortran benchmarks:
ifort

Benchmarks using both Fortran and C:
icc ifort

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:
-prof_gen(pass 1) -prof_use(pass 2) -fast -auto_ilp32

C++ benchmarks:
-prof_gen(pass 1) -prof_use(pass 2) -fast -auto_ilp32

Fortran benchmarks:
-prof_gen(pass 1) -prof_use(pass 2) -fast

Benchmarks using both Fortran and C:
-prof_gen(pass 1) -prof_use(pass 2) -fast -auto_ilp32

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/EM64T_Intel91_flags.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/EM64T_Intel91_flags.xml

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For other inquiries, please contact webmaster@spec.org.

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