



SPEC® CINT2006 Result

Copyright ©2007 Standard Performance Evaluation Corporation

Intel Corporation

Supermicro X7DB+ (Intel Xeon processor 5160, 3.00 GHz)

SPECint®2006 = 20.8

SPECint_base2006 = 18.9

CPU2006 license: 13

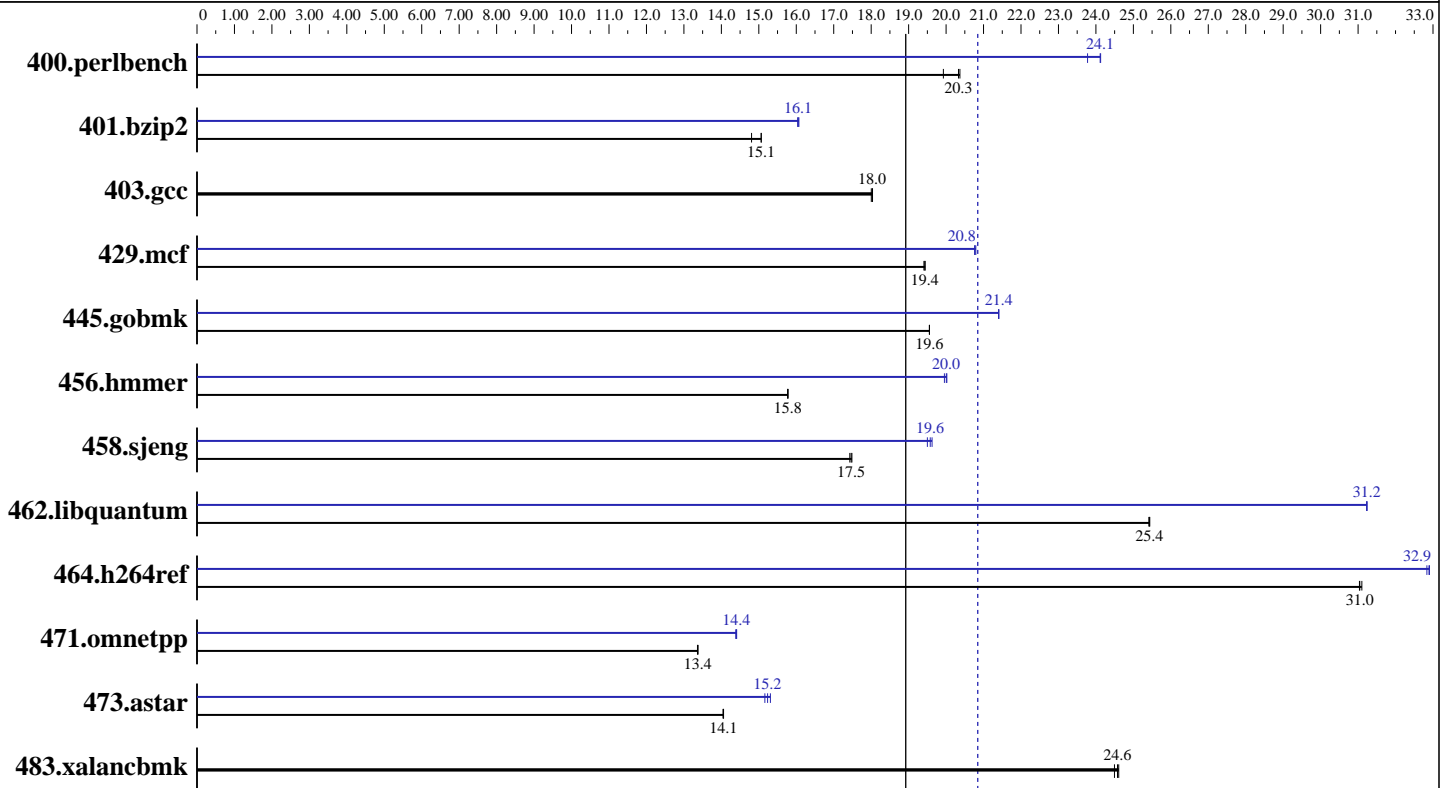
Test sponsor: Intel Corporation

Tested by: Intel Corporation

Test date: May-2007

Hardware Availability: Nov-2006

Software Availability: Jun-2007



SPECint_base2006 = 18.9

SPECint2006 = 20.8

Hardware

CPU Name: Intel Xeon 5160
 CPU Characteristics: Dual Core, 3.0 GHz
 CPU MHz: 3000
 FPU: Integrated
 CPU(s) enabled: 4 cores, 2 chips, 2 cores/chip
 CPU(s) orderable: 1,2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 4 MB I+D on chip per chip
 L3 Cache: None
 Other Cache: None
 Memory: 16 GB (8 * 2GB Samsung DDR2 5300F, 2 rank, CL5-5-5, ECC)
 Disk Subsystem: Seagate, SCSI, 73GB, 10Krpm, 1 disk only
 Other Hardware: None

Software

Operating System: 64-Bit SUSE LINUX Enterprise Server 10, Kernel 2.6.16.21-0.8-smp for x86_64
 Compiler: Intel C++ Compiler for Linux32 version 10.0 Build 20070426 Package ID: l_cc_p_10.0.023
 Auto Parallel: No
 File System: ReiserFS
 System State: Multi-user, run level 3
 Base Pointers: 32-bit
 Peak Pointers: 32/64-bit
 Other Software: SmartHeap library V8.1 Binutils 2.17.50.0.15



SPEC CINT2006 Result

Copyright ©2007 Standard Performance Evaluation Corporation

Intel Corporation

Supermicro X7DB8+ (Intel Xeon processor 5160, 3.00 GHz)

SPECint2006 = **20.8**

SPECint_base2006 = **18.9**

CPU2006 license: 13

Test sponsor: Intel Corporation

Tested by: Intel Corporation

Test date: May-2007

Hardware Availability: Nov-2006

Software Availability: Jun-2007

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	490	19.9	480	20.4	481	20.3	411	23.8	405	24.1	405	24.1
401.bzip2	641	15.1	641	15.1	652	14.8	602	16.0	600	16.1	601	16.1
403.gcc	446	18.0	446	18.0	447	18.0	446	18.0	446	18.0	447	18.0
429.mcf	469	19.4	470	19.4	469	19.4	439	20.8	439	20.8	439	20.8
445.gobmk	536	19.6	536	19.6	536	19.6	490	21.4	490	21.4	490	21.4
456.hmmer	592	15.8	592	15.8	591	15.8	468	20.0	466	20.0	466	20.0
458.sjeng	692	17.5	694	17.4	693	17.5	620	19.5	618	19.6	617	19.6
462.libquantum	815	25.4	815	25.4	815	25.4	663	31.2	663	31.2	663	31.2
464.h264ref	713	31.0	713	31.0	712	31.1	673	32.9	673	32.9	674	32.8
471.omnetpp	467	13.4	467	13.4	468	13.4	434	14.4	434	14.4	434	14.4
473.astar	499	14.1	499	14.1	500	14.0	461	15.2	459	15.3	463	15.2
483.xalancbmk	281	24.6	282	24.5	280	24.6	281	24.6	282	24.5	280	24.6

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

Bios settings:

Hardware Prefetcher: Enabled

Adjacent Sector Prefetch: Disabled

All benchmarks compiled in 32-bit mode except 401.bzip2 and 456.hmmer, for peak, are compiled in 64-bit mode

Base Compiler Invocation

C benchmarks:

icc

C++ benchmarks:

icpc

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
473.astar: -DSPEC_CPU_LITTLE_ENDIAN
483.xalancbmk: -DSPEC_CPU_LINUX



SPEC CINT2006 Result

Copyright ©2007 Standard Performance Evaluation Corporation

Intel Corporation

SPECint2006 = 20.8

Supermicro X7DB8+ (Intel Xeon processor 5160, 3.00 GHz)

SPECint_base2006 = 18.9

CPU2006 license: 13

Test date: May-2007

Test sponsor: Intel Corporation

Hardware Availability: Nov-2006

Tested by: Intel Corporation

Software Availability: Jun-2007

Base Optimization Flags

C benchmarks:

-fast

C++ benchmarks:

-xT -ipo -O3 -no-prec-div -Wl,-z,muldefs
-L/spec/cpu2006.1.0/lib -lsmartheap

Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):

icc

401.bzip2: /opt/intel/cce/10.0.023/bin/icc

456.hmmer: /opt/intel/cce/10.0.023/bin/icc

C++ benchmarks:

icpc

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32

401.bzip2: -DSPEC_CPU_LP64

456.hmmer: -DSPEC_CPU_LP64

462.libquantum: -DSPEC_CPU_LINUX

473.astar: -DSPEC_CPU_LITTLE_ENDIAN

483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -prof-gen(pass 1) -prof-use(pass 2) -fast -ansi-alias
-prefetch

Continued on next page



SPEC CINT2006 Result

Copyright ©2007 Standard Performance Evaluation Corporation

Intel Corporation

SPECint2006 = 20.8

Supermicro X7DB8+ (Intel Xeon processor 5160, 3.00 GHz)

SPECint_base2006 = 18.9

CPU2006 license: 13

Test date: May-2007

Test sponsor: Intel Corporation

Hardware Availability: Nov-2006

Tested by: Intel Corporation

Software Availability: Jun-2007

Peak Optimization Flags (Continued)

401.bzip2: -L/opt/intel/cce/10.0.023/lib -I/opt/intel/cce/10.0.023/include
-prof-gen(pass 1) -prof-use(pass 2) -fast

403.gcc: basepeak = yes

429.mcf: -fast -prefetch

445.gobmk: -prof-gen(pass 1) -prof-use(pass 2) -xT -O2 -ipo
-no-prec_div -ansi-alias

456.hmmcr: -L/opt/intel/cce/10.0.023/lib -I/opt/intel/cce/10.0.023/include
-prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2
-ansi-alias

458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll4

462.libquantum: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll4 -Ob0
-prefetch -opt-streaming-stores always

464.h264ref: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2
-ansi-alias

C++ benchmarks:

471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo
-no-prec_div -ansi-alias -Wl,-z,muldefs
-L/spec/cpu2006.1.0/lib -lsmartheap

473.astar: Same as 471.omnetpp

483.xalancbmk: basepeak = yes

Peak Other Flags

Same as Base Other Flags

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags-file-20070525.xml.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags-file-20070525.xml.xml>



SPEC CINT2006 Result

Copyright ©2007 Standard Performance Evaluation Corporation

Intel Corporation

Supermicro X7DB8+ (Intel Xeon processor 5160,
3.00 GHz)

SPECint2006 = 20.8

SPECint_base2006 = 18.9

CPU2006 license: 13

Test sponsor: Intel Corporation

Tested by: Intel Corporation

Test date: May-2007

Hardware Availability: Nov-2006

Software Availability: Jun-2007

SPEC and SPECint are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.0.
Report generated on Sun Dec 16 03:34:44 2007 by SPEC CPU2006 PS/PDF formatter v5614.