SPEC® CINT2006 Result

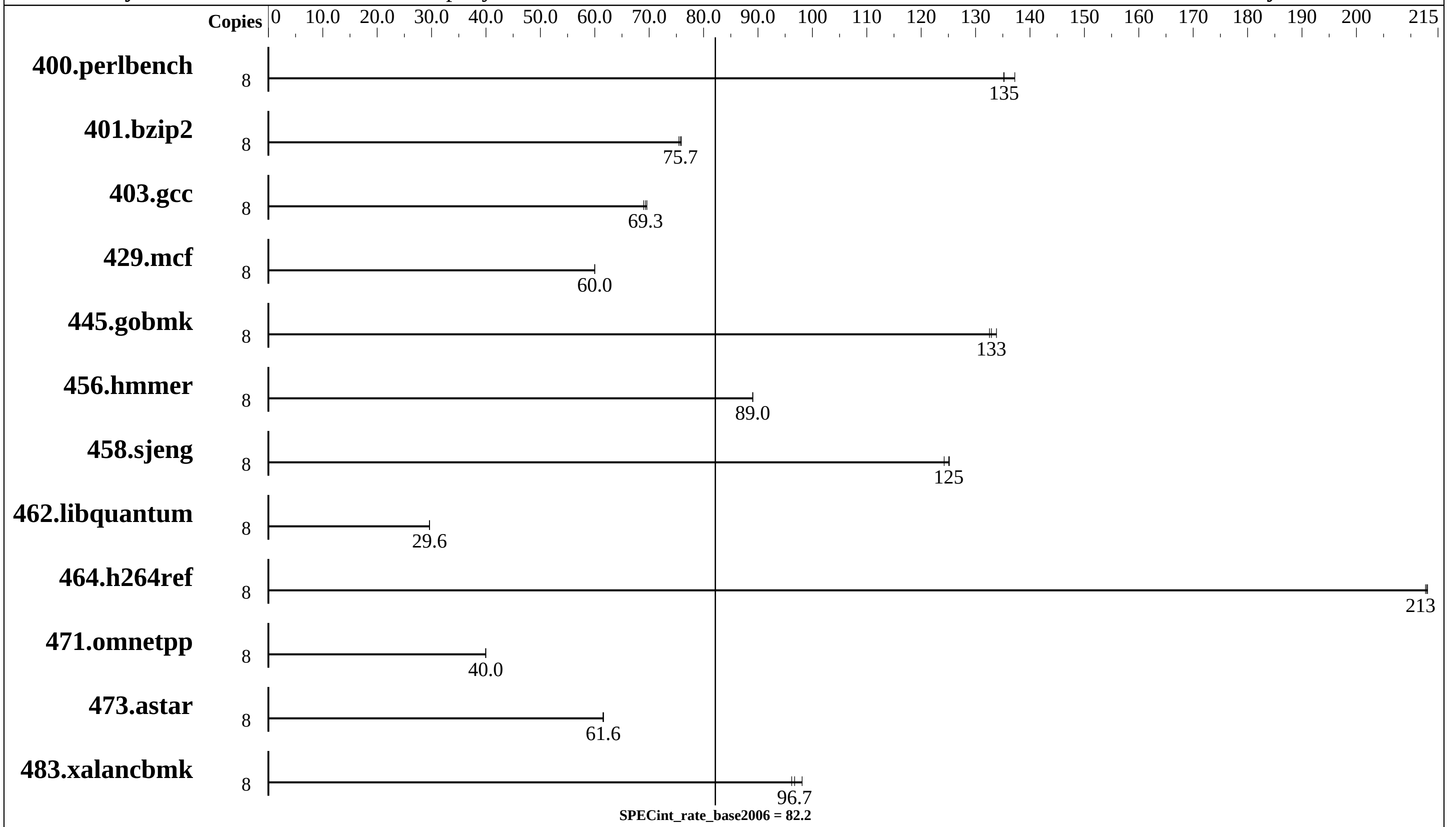
Hewlett-Packard Company

ProLiant DL140 G3
(2.66 GHz, Intel Xeon processor X5355)

SPECint®_rate2006 = Not Run

SPECint_rate_base2006 = 82.2

CPU2006 license:	3	**Test date:**	May-2007
Test sponsor:	Hewlett-Packard Company	**Hardware Availability:**	Jan-2007
Tested by:	Hewlett-Packard Company	**Software Availability:**	Mar-2007



Hardware

CPU Name:	Intel Xeon X5355
CPU Characteristics:	2.66 GHz, 2x4 MB L2 shared, 1333 MHz system bus
CPU MHz:	2660
FPU:	Integrated
CPU(s) enabled:	8 cores, 2 chips, 4 cores/chip
CPU(s) orderable:	1,2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	8 MB I+D on chip per chip, 4 MB shared / 2 cores
L3 Cache:	None
Other Cache:	None
Memory:	16 GB (8x2 GB PC2-5300F CL5)
Disk Subsystem:	1x36 GB 10 K SAS
Other Hardware:	None

Software

Operating System:	SuSE Linux Enterprise Server 10 (x86_64) kernel 2.6.16.21-0.8-smp
Compiler:	Intel C++ Compiler for Intel 32-bit applications, Version 9.1 Build 20070320Z, Package ID: l_cc_c_9.1.049
Auto Parallel:	No
File System:	ext2
System State:	Multi-user run level 3
Base Pointers:	32-bit
Peak Pointers:	Not Applicable
Other Software:	None

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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	8	570	137	578	135	**578**	**135**							
401.bzip2	8	**1019**	**75.7**	1023	75.5	1017	75.9							
403.gcc	8	933	69.0	926	69.6	**929**	**69.3**							
429.mcf	8	1216	60.0	**1216**	**60.0**	1216	60.0							
445.gobmk	8	627	134	**631**	**133**	633	133							
456.hmmer	8	838	89.0	**838**	**89.0**	838	89.1							
458.sjeng	8	773	125	779	124	**774**	**125**							
462.libquantum	8	**5596**	**29.6**	5595	29.6	5597	29.6							
464.h264ref	8	832	213	**831**	**213**	831	213							
471.omnetpp	8	**1251**	**40.0**	1252	39.9	1250	40.0							
473.astar	8	911	61.6	**912**	**61.6**	913	61.5							
483.xalancbmk	8	**571**	**96.7**	563	98.1	574	96.2							

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Platform Notes

```
Adjacent Sector Prefetch Disabled in BIOS.
taskset utility used to bind CPU(s) to processes
ulimit -s unlimited set
```

Base Compiler Invocation

C benchmarks:
`icc`

C++ benchmarks:
`icpc`

Base Portability Flags

400.perlbench: `-DSPEC_CPU_LINUX_X64`
401.bzip2: `-DSPEC_CPU_LP64`
462.libquantum: `-DSPEC_CPU_LINUX`
483.xalancbmk: `-DSPEC_CPU_LINUX`

Base Optimization Flags

C benchmarks:
    -fast

C++ benchmarks:
    -xP -O3 -ipo -no-prec-div -L/compiler/SmartHeap_8_1/lib -lsmartheap

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/hp-ic91-flags.20090715.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/hp-ic91-flags.20090715.xml

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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