## SPEC® CINT2006 Result

**Fujitsu Siemens Computers**  
**PRIMERGY TX150 S5, Intel Pentium D processor 925, 3.0 GHz**  

**SPECint\_rate2006 = 19.5**  
**SPECint\_rate\_base2006 = 18.7**

### Hardware

<table>
<thead>
<tr>
<th>Test</th>
<th>Total</th>
<th>CPU Name</th>
<th>CPU Characteristics</th>
<th>CPU MHZ</th>
<th>FPU</th>
<th>CPU(s) enabled</th>
<th>CPU(s) orderable</th>
<th>Primary Cache</th>
<th>Secondary Cache</th>
<th>L3 Cache</th>
<th>Other Cache</th>
<th>Memory</th>
<th>Disk Subsystem</th>
<th>Other Hardware</th>
</tr>
</thead>
<tbody>
<tr>
<td></td>
<td>2</td>
<td>Intel Pentium D 925</td>
<td>800 MHz system bus</td>
<td>3000</td>
<td>Integrated</td>
<td>2 cores, 1 chip, 2 cores/chip</td>
<td>1 chip</td>
<td>12 K micro-ops I + 16 KB D on chip per core</td>
<td>2 MB I+D on chip per core</td>
<td>None</td>
<td>None</td>
<td>8 GB (4x2 GB DDR2 PC2-4200E, 2 rank, CAS 4-4-4, with ECC)</td>
<td>SATA (160GB 7200 rpm)</td>
<td></td>
</tr>
</tbody>
</table>

### Software

<table>
<thead>
<tr>
<th>Operating System</th>
<th>Compiler</th>
<th>Auto Parallel</th>
<th>File System</th>
<th>System State</th>
<th>Base Pointers</th>
<th>Peak Pointers</th>
<th>Other Software</th>
</tr>
</thead>
<tbody>
<tr>
<td>64-Bit SUSE LINUX Enterprise Server 10, Kernel 2.6.16.21-0.8-smp on an x86_64</td>
<td>Intel C++ Compiler for IA32/EM64T application, Version 9.1 - Build 20070215, Package-ID: l_cc_p_9.1.047</td>
<td>No</td>
<td>ReiserFS</td>
<td>Multiuser, Runlevel 3</td>
<td>32-bit</td>
<td>32/64-bit</td>
<td>Smart Heap Library, Version 8.1</td>
</tr>
</tbody>
</table>

### CPU2006 license: 22  
**Test date:** Jun-2007  
**Hardware Availability:** Oct-2006  
**Software Availability:** Mar-2007

### Results

<table>
<thead>
<tr>
<th>Test</th>
<th>Total</th>
<th>SPECint_rate2006</th>
<th>SPECint_rate_base2006</th>
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<tbody>
<tr>
<td>400.perlbench</td>
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<td>15.9</td>
<td>21.4</td>
</tr>
<tr>
<td>401.bzip2</td>
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<td>15.3</td>
<td>20.5</td>
</tr>
<tr>
<td>403.gcc</td>
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<td>17.5</td>
<td>21.0</td>
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<td>429.mcf</td>
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<td>16.6</td>
<td>21.6</td>
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<tr>
<td>445.gobmk</td>
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<td>16.4</td>
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<td>456.hmmer</td>
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<td>17.2</td>
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<td>458.sjeng</td>
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<td>19.3</td>
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<td>462.libquantum</td>
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<td>14.9</td>
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<tr>
<td>464.h264ref</td>
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<td>33.9</td>
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<tr>
<td>471.omnetpp</td>
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<td>473.astar</td>
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<td>13.5</td>
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<tr>
<td>483.xalancbmk</td>
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**SPECint\_rate\_base2006 = 18.7**  
**SPECint\_rate2006 = 19.5**
Fujitsu Siemens Computers
PRIMERGY TX150 S5, Intel Pentium D processor 925, 3.0 GHz

SPECint_rate2006 = 19.5
SPECint_rate_base2006 = 18.7

Results Table

<table>
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<tr>
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<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
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<th>Seconds</th>
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<th>Ratio</th>
</tr>
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<td>541</td>
<td>25.5</td>
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</tbody>
</table>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run
'/usr/bin/taskset' used to bind processes to CPUs

General Notes

The system bus runs at 800 MHz

All binaries were built with 32-bit Intel compiler except:
401.bzip2, 456.hmmer and 462.libquantum in peak were built with
64-bit Intel compiler by changing the path for include and library files.

For information about Fujitsu Siemens Computers in your country please see:
http://www.fujitsu-siemens.com/countries

Base Compiler Invocation

C benchmarks: icc
C++ benchmarks: icpc
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CPU2006 license: 22
Test sponsor: Fujitsu Siemens Computers
Tested by: Fujitsu Siemens Computers
Test date: Jun-2007
Hardware Availability: Oct-2006
Software Availability: Mar-2007

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_X64
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
- fast
C++ benchmarks:
- -xP -O3 -ipo -no-prec-div -L/opt/SmartHeap_8_1/lib -lsmartheap

Peak Compiler Invocation

C benchmarks (except as noted below):
icc

401.bzip2: /opt/intel/cce/9.1.047/bin/icc
-I/opt/intel/cce/9.1.047/include
-L/opt/intel/cce/9.1.047/lib

456.hmmer: /opt/intel/cce/9.1.047/bin/icc
-I/opt/intel/cce/9.1.047/include
-L/opt/intel/cce/9.1.047/lib

462.libquantum: /opt/intel/cce/9.1.047/bin/icc
-I/opt/intel/cce/9.1.047/include
-L/opt/intel/cce/9.1.047/lib

C++ benchmarks:
icpc

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_X64
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX
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Peak Optimization Flags

C benchmarks:

400.perlbench: -prof_gen(pass 1) -prof_use(pass 2) -fast
401.bzip2: -fast
403.gcc: basepeak = yes
429.mcf: -prof_gen(pass 1) -prof_use(pass 2) -fast
- /opt/SmartHeap_8_1/lib -lsmartheap
445.gobmk: Same as 429.mcf
456.hmmer: Same as 400.perlbench
458.sjeng: Same as 429.mcf
462.libquantum: Same as 400.perlbench
464.h264ref: Same as 429.mcf

C++ benchmarks:

471.omnetpp: -prof_gen(pass 1) -prof_use(pass 2) -xP -O3 -ipo
- no-prec-div - /opt/SmartHeap_8_1/lib -lsmartheap
473.astar: -prof_gen(pass 1) -prof_use(pass 2) -fast
- /opt/SmartHeap_8_1/lib -lsmartheap
483.xalancbmk: basepeak = yes

The flags file that was used to format this result can be browsed at

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/CPU2006_flags.20090714.09.xml

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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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