



SPEC® CINT2006 Result

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Intel Corporation

Supermicro X7DB8+ (Intel Xeon processor E5335, 2.00 GHz)

SPECint®2006 = 15.6

SPECint_base2006 = 14.2

CPU2006 license: 13

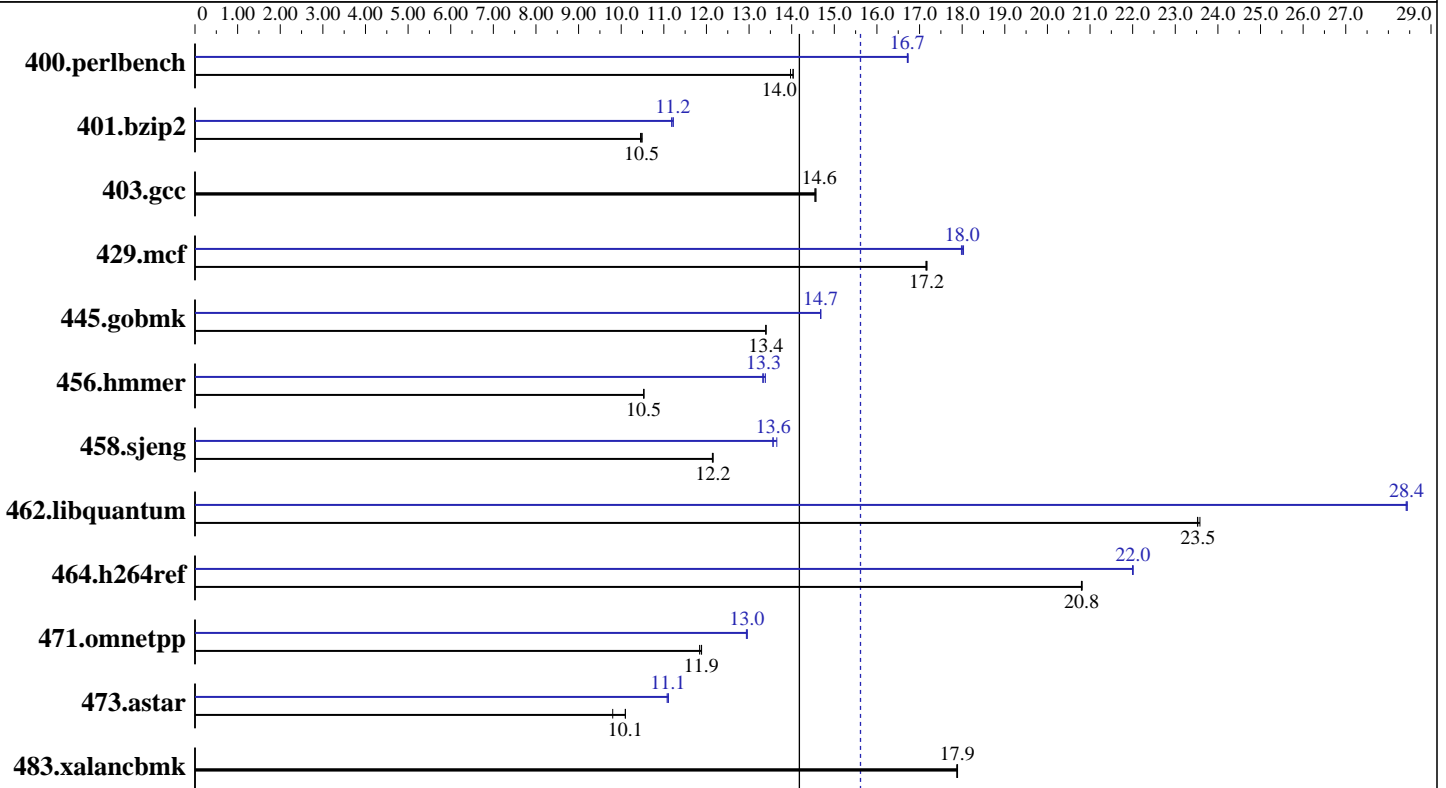
Test sponsor: Intel Corporation

Tested by: Intel Corporation

Test date: May-2007

Hardware Availability: Sep-2007

Software Availability: Jun-2007



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Hardware

CPU Name: Intel Xeon E5335
 CPU Characteristics: Quad Core, 2.00 GHz
 CPU MHz: 2000
 FPU: Integrated
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip
 CPU(s) orderable: 1,2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 8 MB I+D on chip per chip, 4 MB shared / 2 cores
 L3 Cache: None
 Other Cache: None
 Memory: 16 GB (8 * 2GB Samsung DDR2 5300F, 2 rank, CL5-5-5, ECC)
 Disk Subsystem: Seagate, SCSI, 73GB, 10Krpm, 1 disk only
 Other Hardware: None

Software

Operating System: 64-Bit SUSE LINUX Enterprise Server 10, Kernel 2.6.16.21-0.8-smp for x86_64
 Compiler: Intel C++ Compiler for Linux32 version 10.0 Build 20070426 Package ID: l_cc_p_10.0.023
 Auto Parallel: No
 File System: ReiserFS
 System State: Multi-user, run level 3
 Base Pointers: 32-bit
 Peak Pointers: 32/64-bit
 Other Software: SmartHeap library V8.1 Binutils 2.17.50.0.15



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Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	<u>696</u>	<u>14.0</u>	696	14.0	699	14.0	584	16.7	585	16.7	<u>584</u>	<u>16.7</u>
401.bzip2	920	10.5	<u>921</u>	<u>10.5</u>	923	10.5	863	11.2	<u>861</u>	<u>11.2</u>	860	11.2
403.gcc	<u>553</u>	<u>14.6</u>	553	14.5	552	14.6	<u>553</u>	<u>14.6</u>	553	14.5	552	14.6
429.mcf	<u>531</u>	<u>17.2</u>	532	17.1	531	17.2	<u>507</u>	<u>18.0</u>	506	18.0	507	18.0
445.gobmk	783	13.4	<u>783</u>	<u>13.4</u>	783	13.4	<u>715</u>	<u>14.7</u>	714	14.7	715	14.7
456.hmmer	886	10.5	<u>886</u>	<u>10.5</u>	886	10.5	697	13.4	<u>699</u>	<u>13.3</u>	700	13.3
458.sjeng	996	12.2	<u>996</u>	<u>12.2</u>	997	12.1	892	13.6	886	13.7	<u>892</u>	<u>13.6</u>
462.libquantum	<u>881</u>	<u>23.5</u>	879	23.6	881	23.5	728	28.4	729	28.4	<u>729</u>	<u>28.4</u>
464.h264ref	1064	20.8	1063	20.8	<u>1064</u>	<u>20.8</u>	1005	22.0	1006	22.0	<u>1006</u>	<u>22.0</u>
471.omnetpp	<u>526</u>	<u>11.9</u>	526	11.9	528	11.8	<u>482</u>	<u>13.0</u>	482	13.0	483	12.9
473.astar	716	9.80	695	10.1	<u>695</u>	<u>10.1</u>	632	11.1	634	11.1	<u>633</u>	<u>11.1</u>
483.xalancbmk	<u>386</u>	<u>17.9</u>	386	17.9	386	17.9	<u>386</u>	<u>17.9</u>	386	17.9	386	17.9

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

Bios settings:

Hardware Prefetcher: Enabled

Adjacent Sector Prefetch: Enabled

ulimit -s unlimited used to set stack size to unlimited

All benchmarks compiled in 32-bit mode except 401.bzip2 and 456.hmmer, for peak, are compiled in 64-bit mode

Base Compiler Invocation

C benchmarks:

icc

C++ benchmarks:

icpc

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32

462.libquantum: -DSPEC_CPU_LINUX

483.xalancbmk: -DSPEC_CPU_LINUX



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Base Optimization Flags

C benchmarks:

-fast

C++ benchmarks:

-xT -ipo -O3 -no-prec-div -Wl,-z,muldefs
-L/spec/cpu2006.1.0/lib -lsmartheap

Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):

icc

401.bzip2: /opt/intel/cce/10.0.023/bin/icc
-L/opt/intel/cce/10.0.023/lib
-I/opt/intel/cce/10.0.023/include

456.hmmer: /opt/intel/cce/10.0.023/bin/icc
-L/opt/intel/cce/10.0.023/lib
-I/opt/intel/cce/10.0.023/include

C++ benchmarks:

icpc

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

Continued on next page



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Peak Optimization Flags (Continued)

400.perlbench: -prof-gen(pass 1) -prof-use(pass 2) -fast -ansi-alias
-prefetch

401.bzip2: -prof-gen(pass 1) -prof-use(pass 2) -fast

403.gcc: basepeak = yes

429.mcf: -fast -prefetch

445.gobmk: -prof-gen(pass 1) -prof-use(pass 2) -xT -O2 -ipo
-no-prec_div -ansi-alias

456.hmmer: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2
-ansi-alias

458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll4

462.libquantum: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll4 -Ob0
-prefetch -opt-streaming-stores always

464.h264ref: Same as 456.hmmer

C++ benchmarks:

471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo
-no-prec_div -ansi-alias -Wl,-z,muldefs
-L/spec/cpu2006.1.0/lib -lsmartheap

473.astar: Same as 471.omnetpp

483.xalancbmk: basepeak = yes

Peak Other Flags

Same as Base Other Flags

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090715.00.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090715.00.xml>



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