SPEC® CINT2006 Result

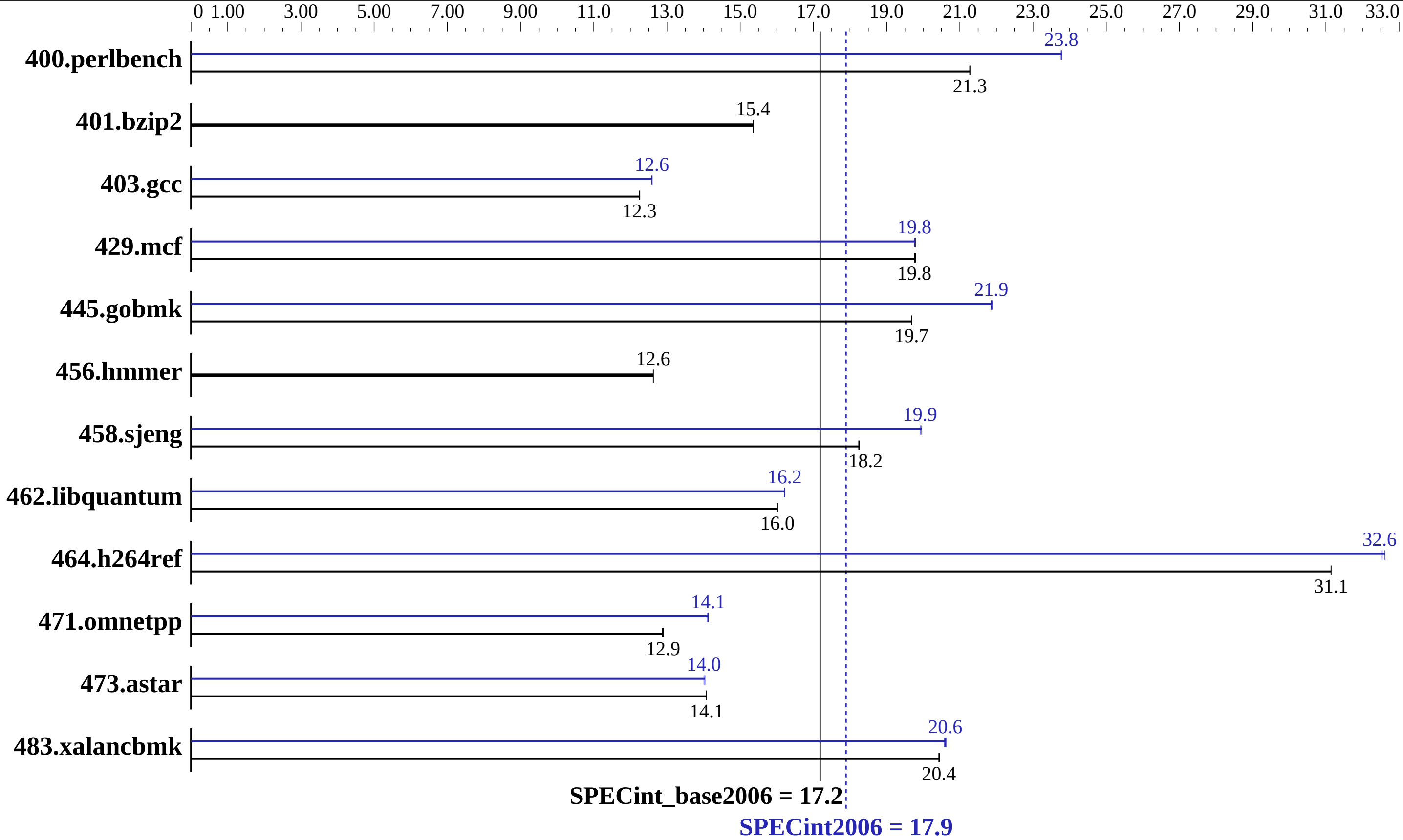
NEC Corporation

Express5800/120Rg-1
(Intel Xeon processor 5160)

SPECint®2006 = 17.9

SPECint_base2006 = 17.2

CPU2006 license: 9006		**Test date:**	May-2007
Test sponsor:	NEC Corporation	**Hardware Availability:**	May-2007
Tested by:	NEC Corporation	**Software Availability:**	Apr-2007



Hardware

CPU Name:	Intel Xeon 5160
CPU Characteristics:	3.00 GHz, 4MB L2, 1333MHz bus
CPU MHz:	3000
FPU:	Integrated
CPU(s) enabled:	4 cores, 2 chips, 2 cores/chip
CPU(s) orderable:	1,2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	4 MB I+D on chip per chip
L3 Cache:	None
Other Cache:	None
Memory:	8 GB (8x1 GB DDR2 5300F, 2 rank, CL5-5-5, ECC)
Disk Subsystem:	1x73.2 GB SAS, 15000RPM
Other Hardware:	None

Software

Operating System:	Windows Server 2003, Standard x64 Edition
Compiler:	Intel C++ Compiler for 32bit version 9.1 Build 20070109, Package-ID W_CC_C_9.1.034 Microsoft Visual Studio 2005 (libr. & linker)
Auto Parallel:	No
File System:	NTFS
System State:	Default
Base Pointers:	32-bit
Peak Pointers:	32-bit
Other Software:	MicroQuill SmartHeap Library 8.1

NEC Corporation

Express5800/120Rg-1
(Intel Xeon processor 5160)

SPECint2006 = 17.9

SPECint_base2006 = 17.2

CPU2006 license: 9006
Test sponsor: NEC Corporation
Tested by: NEC Corporation

Test date: May-2007
Hardware Availability: May-2007
Software Availability: Apr-2007

Results Table

	Base						Peak					
Benchmark	**Seconds**	**Ratio**	**Seconds**	**Ratio**	**Seconds**	**Ratio**	**Seconds**	**Ratio**	**Seconds**	**Ratio**	**Seconds**	**Ratio**
400.perlbench	459	21.3	460	21.2	**459**	**21.3**	411	23.8	411	23.8	**411**	**23.8**
401.bzip2	628	15.4	**628**	**15.4**	629	15.4	628	15.4	**628**	**15.4**	629	15.4
403.gcc	657	12.2	657	12.3	**657**	**12.3**	640	12.6	639	12.6	**639**	**12.6**
429.mcf	462	19.8	**462**	**19.8**	461	19.8	**462**	**19.8**	461	19.8	462	19.8
445.gobmk	533	19.7	**533**	**19.7**	533	19.7	**480**	**21.9**	479	21.9	480	21.9
456.hmmer	739	12.6	739	12.6	**739**	**12.6**	739	12.6	739	12.6	**739**	**12.6**
458.sjeng	663	18.3	664	18.2	**664**	**18.2**	**608**	**19.9**	608	19.9	606	20.0
462.libquantum	1293	16.0	**1293**	**16.0**	1294	16.0	1279	16.2	**1278**	**16.2**	1278	16.2
464.h264ref	711	31.1	**711**	**31.1**	711	31.1	**679**	**32.6**	680	32.5	679	32.6
471.omnetpp	485	12.9	**485**	**12.9**	485	12.9	**442**	**14.1**	442	14.1	443	14.1
473.astar	498	14.1	**498**	**14.1**	499	14.1	**501**	**14.0**	500	14.0	501	14.0
483.xalancbmk	337	20.4	**338**	**20.4**	338	20.4	335	20.6	**335**	**20.6**	335	20.6

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

```
The Express5800/120Rg-1 and the Express5800/120Ri-2 models are
electronically equivalent.
The results have been measured on a Express5800/120Ri-2 model.
```

Base Compiler Invocation

C benchmarks:
`icl -Qc99`

C++ benchmarks:
`icl`

Base Portability Flags

403.gcc: `-DSPEC_CPU_WIN32`
464.h264ref: `-DSPEC_CPU_NO_INTTYPES -DWIN32`
483.xalancbmk: `-Qoption,cpp,--no_wchar_t_keyword`

Base Optimization Flags

C benchmarks:
`-fast -F512000000 shlW32M.lib -link -FORCE:MULTIPLE`

Continued on next page

Base Optimization Flags (Continued)

C++ benchmarks:
-fast -Qcxx-features -F512000000 shlW32M.lib
-link -FORCE:MULTIPLE

Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks:
icl -Qc99

C++ benchmarks:
icl

Peak Portability Flags

403.gcc: -DSPEC_CPU_WIN32
464.h264ref: -DSPEC_CPU_NO_INTTYPES -DWIN32
483.xalancbmk: -Qoption,cpp,--no_wchar_t_keyword

Peak Optimization Flags

C benchmarks:

400.perlbench: ONESTEP -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast
-F512000000 shlW32M.lib -link -FORCE:MULTIPLE

401.bzip2: basepeak = yes

403.gcc: Same as 400.perlbench

429.mcf: ONESTEP -fast -F512000000 shlW32M.lib
-link -FORCE:MULTIPLE

445.gobmk: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -F512000000
shlW32M.lib -link -FORCE:MULTIPLE

Continued on next page

Peak Optimization Flags (Continued)

456.hmmer: `basepeak = yes`

458.sjeng: `Same as 400.perlbench`

462.libquantum: `Same as 400.perlbench`

464.h264ref: `Same as 400.perlbench`

C++ benchmarks:

471.omnetpp: `ONESTEP -Qprof_gen`(pass 1) `-Qprof_use`(pass 2) `-fast -Qcxx-features -F512000000 shlW32M.lib -link -FORCE:MULTIPLE`

473.astar: `Same as 471.omnetpp`

483.xalancbmk: `ONESTEP -fast -Qcxx-features -F512000000 shlW32M.lib -link -FORCE:MULTIPLE`

Peak Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/NEC-cpu2006-ic91-flags.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/NEC-cpu2006-ic91-flags.xml

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.0.
Report generated on Tue Jul 22 13:00:34 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 10 July 2007.