SPEC® CINT2006 Result

NEC Corporation
Express5800/120Ri-2
(Intel Xeon processor 5160)

CPU2006 license: 9006
Test date: May-2007
Test sponsor: NEC Corporation
Hardware Availability: May-2007
Tested by: NEC Corporation
Software Availability: Apr-2007

SPECint®2006 = 17.9
SPECint_base2006 = 17.2

Hardware
CPU Name: Intel Xeon 5160
CPU Characteristics: 3.00 GHz, 4MB L2, 1333MHz bus
CPU MHz: 3000
FPU: Integrated
CPU(s) enabled: 4 cores, 2 chips, 2 cores/chip
CPU(s) orderable: 1.2 chips
Primary Cache: 32 KB I + 32 KB D on chip per core
Secondary Cache: 4 MB I+D on chip per chip
L3 Cache: None
Other Cache: None
Memory: 8 GB (8x1 GB DDR2 5300F, 2 rank, CL5-5-5, ECC)
Disk Subsystem: 1x73.2 GB SAS, 15000RPM
Other Hardware: None

Software
Operating System: Windows Server 2003, Standard x64 Edition
Compiler: Intel C++ Compiler for 32bit version 9.1
Build 20070109, Package-ID W_CC_C_9.1.034
Microsoft Visual Studio 2005 (libr. & linker)
Auto Parallel: No
File System: NTFS
System State: Default
Base Pointers: 32-bit
Peak Pointers: 32-bit
Other Software: MicroQuill SmartHeap Library 8.1

0 1.00 3.00 5.00 7.00 9.00 11.0 13.0 15.0 17.0 19.0 21.0 23.0 25.0 27.0 29.0 31.0 33.0

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Results Table

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<th>Benchmark</th>
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<th>Ratio</th>
<th>Base</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Peak</th>
<th>Seconds</th>
<th>Ratio</th>
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<td>21.3</td>
<td>460</td>
<td>21.2</td>
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</table>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

The Express5800/120Rg-1 and the Express5800/120Ri-2 models are electronically equivalent.
The results have been measured on a Express5800/120Ri-2 model.

Base Compiler Invocation

C benchmarks:

icl -qC99

C++ benchmarks:

icl

Base Portability Flags

403.gcc: -DSPEC_CPU_WIN32
464.h264ref: -DSPEC_CPU_NO_INTTYPES -DWIN32
483.xalancbmk: -Qoption,cpp,--no_wchar_t_keyword

Base Optimization Flags

C benchmarks:

-fast -F512000000 sh1W32M.lib -link -FORCE:_MULTIPLE

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Base Optimization Flags (Continued)

C++ benchmarks:
- fast -Qcxx-features -F512000000 shlw32m.lib
  -link -FORCE:_MULTIPLE

Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks:
icl -Qc99

C++ benchmarks:
icl

Peak Portability Flags

403.gcc: -DSPEC_CPU_WIN32
464.h264ref: -DSPEC_CPU_NO_INTTYPES -DWIN32
483.xalancbmk: -Qoption,cpp,-no_wchar_t_keyword

Peak Optimization Flags

C benchmarks:

400.perlbench: ONESTEP -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast
  -F512000000 shlw32m.lib
  -link -FORCE:_MULTIPLE

401.bzip2: basepeak = yes

403.gcc: Same as 400.perlbench

429.mcf: ONESTEP -fast -F512000000 shlw32m.lib
  -link -FORCE:_MULTIPLE

445.gobmk: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -F512000000
  shlw32m.lib
  -link -FORCE:_MULTIPLE

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Peak Optimization Flags (Continued)

456.hmmer: basepeak = yes
458.sjeng: Same as 400.perlbench
462.libquantum: Same as 400.perlbench
464.h264ref: Same as 400.perlbench

C++ benchmarks:

471.omnetpp: ONESTEP -Qprof_gen(pass1) -Qprof_use(pass2) -fast
-@qcxx-features -F512000000 shlW32M.lil
-@link -FORCE:MULTIPLE

473.astar: Same as 471.omnetpp

483.xalancbmk: ONESTEP -fast -@qcxx-features -F512000000 shlW32M.lil
-@link -FORCE:MULTIPLE

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at

You can also download the XML flags source by saving the following link:

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For other inquiries, please contact webmaster@spec.org.

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