SPEC® CINT2006 Result

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Intel Corporation

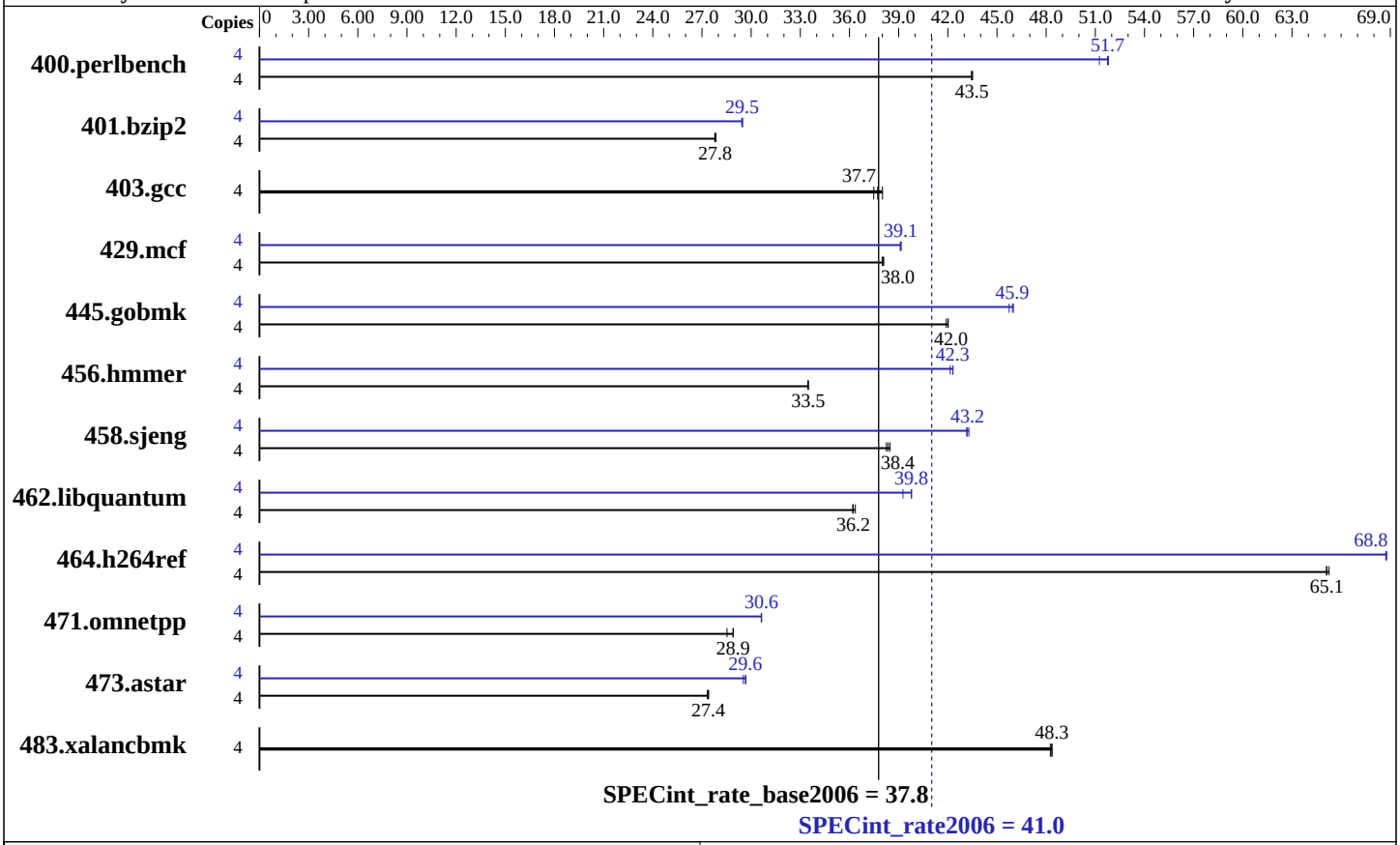
Supermicro X7DB8+ (Intel Xeon processor 5110, 1.60 GHz)

SPECint®_rate2006 = 41.0

SPECint_rate_base2006 = 37.8

CPU2006 license: 13
Test sponsor: Intel Corporation
Tested by: Intel Corporation

Test date: Jun-2007
Hardware Availability: Nov-2006
Software Availability: Jun-2007



Hardware

CPU Name:	Intel Xeon 5110
CPU Characteristics:	Dual Core, 1.60 GHz
CPU MHz:	1600
FPU:	Integrated
CPU(s) enabled:	4 cores, 2 chips, 2 cores/chip
CPU(s) orderable:	1,2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	4 MB I+D on chip per chip
L3 Cache:	None
Other Cache:	None
Memory:	8 GB (8 * 1GB Samsung DDR2 5300F, 2 rank, CL5-5-5, ECC)
Disk Subsystem:	Seagate, SCSI, 73GB, 10Krpm, 1 disk only
Other Hardware:	None

Software

Operating System:	64-Bit SUSE LINUX Enterprise Server 10, Kernel 2.6.16.21-0.8-smp for x86_64
Compiler:	Intel C++ Compiler for Linux32 version 10.0 Build 20070426 Package ID: l_cc_p_10.0.023
Auto Parallel:	No
File System:	ReiserFS
System State:	Multi-user, run level 3
Base Pointers:	32-bit
Peak Pointers:	32/64-bit
Other Software:	SmartHeap library V8.1 Binutils 2.17.50.0.15

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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	4	900	43.4	898	43.5	**899**	**43.5**	4	754	51.8	**755**	**51.7**	763	51.2
401.bzip2	4	**1388**	**27.8**	1390	27.8	1386	27.9	4	1309	29.5	1312	29.4	**1310**	**29.5**
403.gcc	4	**854**	**37.7**	859	37.5	847	38.0	4	**854**	**37.7**	859	37.5	847	38.0
429.mcf	4	**959**	**38.0**	958	38.1	960	38.0	4	**932**	**39.1**	932	39.1	933	39.1
445.gobmk	4	1001	41.9	**999**	**42.0**	998	42.0	4	**913**	**45.9**	917	45.7	912	46.0
456.hmmer	4	**1115**	**33.5**	1114	33.5	1115	33.5	4	**882**	**42.3**	882	42.3	885	42.1
458.sjeng	4	1258	38.5	**1262**	**38.4**	1265	38.3	4	1122	43.2	1118	43.3	**1120**	**43.2**
462.libquantum	4	**2288**	**36.2**	2280	36.4	2290	36.2	4	2083	39.8	2111	39.3	**2084**	**39.8**
464.h264ref	4	**1359**	**65.1**	1356	65.3	1360	65.1	4	1287	68.8	**1287**	**68.8**	1288	68.7
471.omnetpp	4	865	28.9	**865**	**28.9**	876	28.5	4	816	30.6	**816**	**30.6**	817	30.6
473.astar	4	**1025**	**27.4**	1028	27.3	1025	27.4	4	**947**	**29.6**	951	29.5	946	29.7
483.xalancbmk	4	**571**	**48.3**	571	48.4	572	48.3	4	**571**	**48.3**	571	48.4	572	48.3

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

```
Bios settings:
Hardware Prefetcher:      Enabled
Adjacent Sector Prefetch: Disabled
All benchmarks compiled in 32-bit mode except 401.bzip2 and 456.hmmer,
for peak, are compiled in 64-bit mode
taskset was used to bind processes to cores
```

Base Compiler Invocation

C benchmarks:
`icc`

C++ benchmarks:
`icpc`

Base Portability Flags

400.perlbench: `-DSPEC_CPU_LINUX_IA32`
462.libquantum: `-DSPEC_CPU_LINUX`
483.xalancbmk: `-DSPEC_CPU_LINUX`

Base Optimization Flags

C benchmarks:
`-fast`

C++ benchmarks:
`-xT -ipo -O3 -no-prec-div -Wl,-z,muldefs`
`-L/spec/cpu2006.1.0/lib -lsmartheap`

Base Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

Peak Compiler Invocation

C benchmarks (except as noted below):
`icc`

401.bzip2: `/opt/intel/cce/10.0.023/bin/icc`
`-L/opt/intel/cce/10.0.023/lib`
`-I/opt/intel/cce/10.0.023/include`

456.hmmer: `/opt/intel/cce/10.0.023/bin/icc`
`-L/opt/intel/cce/10.0.023/lib`
`-I/opt/intel/cce/10.0.023/include`

C++ benchmarks:
`icpc`

Peak Portability Flags

400.perlbench: `-DSPEC_CPU_LINUX_IA32`
401.bzip2: `-DSPEC_CPU_LP64`
456.hmmer: `-DSPEC_CPU_LP64`
462.libquantum: `-DSPEC_CPU_LINUX`
483.xalancbmk: `-DSPEC_CPU_LINUX`

Peak Optimization Flags

C benchmarks:

Continued on next page

Peak Optimization Flags (Continued)

400.perlbench: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-fast -ansi-alias -prefetch`

401.bzip2: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-fast`

403.gcc: `basepeak = yes`

429.mcf: `-fast -prefetch`

445.gobmk: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-xT -O2 -ipo -no-prec_div -ansi-alias`

456.hmmer: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-fast -unroll2 -ansi-alias`

458.sjeng: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-fast -unroll4`

462.libquantum: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-fast -unroll4 -Ob0 -prefetch -opt-streaming-stores always`

464.h264ref: `Same as 456.hmmer`

C++ benchmarks:

471.omnetpp: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-xT -O3 -ipo -no-prec_div -ansi-alias -Wl,-z,muldefs -L/spec/cpu2006.1.0/lib -lsmartheap`

473.astar: `Same as 471.omnetpp`

483.xalancbmk: `basepeak = yes`

Peak Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

The flags file that was used to format this result can be browsed at
`http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090714.35.html`

You can also download the XML flags source by saving the following link:
`http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090714.35.xml`

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