Supermicro Motherboard X7DA8

SPECI\textsubscript{nt\_}rate\textsubscript{2006} = 80.2
SPECI\textsubscript{nt\_}rate\textsubscript{base\_}2006 = 77.6

CPU2006 license: 001176
Test date: Apr-2007

Test sponsor: Supermicro
Hardware Availability: May-2007

Tested by: Supermicro
Software Availability: Apr-2007

Copy

\begin{tabular}{|l|c|c|c|c|c|c|c|c|c|}
\hline
Copy & 10.0 & 20.0 & 30.0 & 40.0 & 50.0 & 60.0 & 70.0 & 80.0 & 90.0 & 100 & 110 & 120 & 130 & 140 & 150 & 160 & 170 & 180 & 190 & 200 & 215 \\
\hline
400.perlbench & 8 & 8 & 158 \\
401.bzip2 & 8 & 65.8 & 143 \\
403.gcc & 8 & 32.3 & 65.1 \\
429.mcf & 8 & 32.1 & 58.6 \\
445.gobmk & 8 & 145 \\
456.hmmer & 8 & 90.4 & 134 \\
458.sjeng & 8 & 88.4 & 134 \\
462.libquantum & 8 & 35.0 & 126 \\
464.h264ref & 8 & 34.9 & 207 \\
471.omnetpp & 8 & 45.3 & 212 \\
473.astar & 8 & 43.7 & 66.9 \\
483.xalancbmk & 8 & 63.6 & 92.2 \\
\hline
\end{tabular}

\begin{align*}
\text{SPECint\_}rate\textsubscript{2006} &= 80.2 \\
\text{SPECint\_}rate\textsubscript{base\_}2006 &= 77.6
\end{align*}

Hardware

\begin{itemize}
\item CPU Name: Intel Xeon X5355
\item CPU Characteristics: 2.66GHz, 1333 MHz Bus
\item CPU MHz: 2660
\item FPU: Integrated
\item CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip
\item CPU(s) orderable: 1, 2 chips
\item Primary Cache: 32 KB I + 32 KB D on chip per core
\item Secondary Cache: 8 MB I+D on chip per chip, 4 MB shared / 2 cores
\item L3 Cache: None
\item Other Cache: None
\item Memory: 16 GB (8 X 2GB ECC PC2-5300, CL5, FBDIMM)
\item Disk Subsystem: 750GB IDE, 7200RPM
\item Other Hardware: None
\end{itemize}

Software

\begin{itemize}
\item Operating System: Windows Server 2003 Enterprise Edition W/ SP1
\item Compiler: Intel C++ Compiler for IA32 version 9.1 Build no 20070322Z
\item Auto Parallel: No
\item File System: NTFS
\item System State: Default
\item Base Pointers: 32-bit
\item Peak Pointers: 32-bit
\item Other Software: SmartHeap Library Version 8.0
\end{itemize}
Supermicro
Motherboard X7DA8

SPECint_rate2006 = 80.2
SPECint_rate_base2006 = 77.6

Results Table

<table>
<thead>
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<th>Seconds</th>
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<td>92.3</td>
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</tbody>
</table>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

Tested systems can be used with SC816S-R700 case,
To ensure system stability, a 500W (minimum) ATX power supply [4-pin (+12V), 8-pin (+12V) and 24-pin are required]
Product description located as of http://www.supermicro.com/products/motherboard/Xeon1333/5000X/X7DA8.cfm
The system bus runs at 1333 MHz

Base Compiler Invocation

C benchmarks:
icl -Qvc7.1 -Qc99

C++ benchmarks:
icl -Qvc7.1

Base Portability Flags

403.gcc: -DSPEC_CPU_WIN32
464.h264ref: -DSPEC_CPU_NO_INTTYPES -DWIN32

Base Optimization Flags

C benchmarks:
-fast /F512000000 shlw32m.lib -link /FORCE:MULTIPLE

Continued on next page
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Base Optimization Flags (Continued)

C++ benchmarks:
- fast -Qcxx_features /F512000000 shlw32m.lib
- link /FORCE:MULTIPLE

Base Other Flags

C benchmarks:
403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks:
icl -Qvc7.1 -Qc99
C++ benchmarks:
icl -Qvc7.1

Peak Portability Flags

403.gcc: -DSPEC_CPU_WIN32
464.h264ref: -DSPEC_CPU_NO_INTTYPES -DWIN32

Peak Optimization Flags

C benchmarks:
400.perlbench: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast /F512000000 shlw32m.lib -link /FORCE:MULTIPLE
401.bzip2: Same as 400.perlbench
403.gcc: Same as 400.perlbench
429.mcf: basepeak = yes
445.gobmk: Same as 400.perlbench
456.hmmer: Same as 400.perlbench
458.sjeng: Same as 400.perlbench

Continued on next page
Supermicro
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Peak Optimization Flags (Continued)

462.libquantum: Same as 400.perlbench
464.h264ref: Same as 400.perlbench

C++ benchmarks:

471.omnetpp: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qcxx_features
/F512000000 shlw32m.lib -link /FORCE:MULTIPLE

473.astar: -Qprof_gen(pass 1) -Qprof_use(pass 2) -QxP -O2 -Qipo
-@Qprec-div -@Qunroll4 -Ob2 -Qsfalign16 -Qcxx_features
/F512000000 shlw32m.lib -link /FORCE:MULTIPLE

483.xalancbmk: Same as 471.omnetpp

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic91-ia32-flags.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/Intel-ic91-ia32-flags.xml

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holders.

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.0.1.