



# SPEC® CINT2006 Result

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## Fujitsu Siemens Computers

PRIMERGY TX120, Intel Celeron processor 440, 2.0 GHz

SPECint®2006 = 11.7

SPECint\_base2006 = 11.1

CPU2006 license: 22

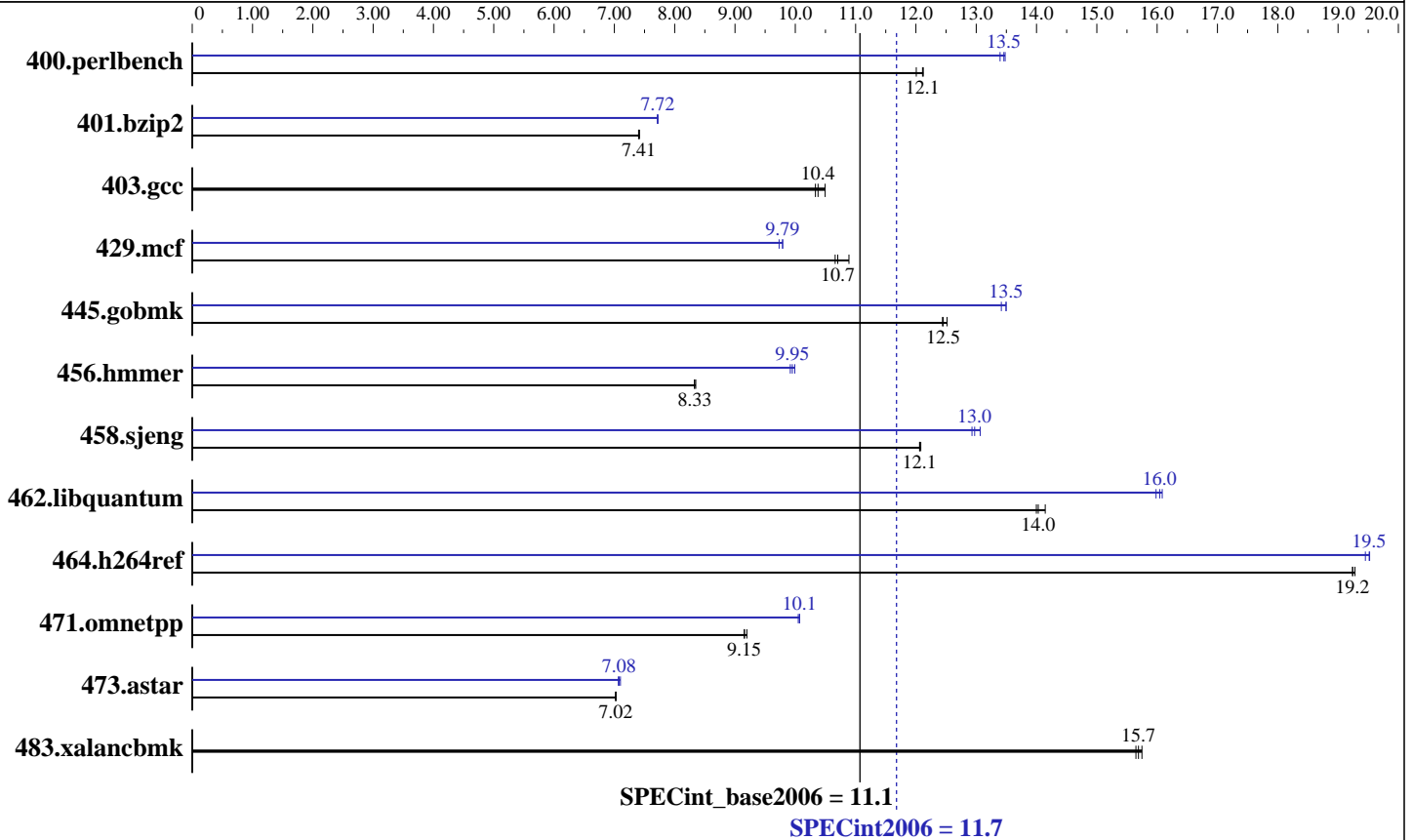
Test sponsor: Fujitsu Siemens Computers

Tested by: Fujitsu Siemens Computers

Test date: Jun-2007

Hardware Availability: Jul-2007

Software Availability: Mar-2007



### Hardware

CPU Name: Intel Celeron 440  
 CPU Characteristics: 800 MHz system bus  
 CPU MHz: 2000  
 FPU: Integrated  
 CPU(s) enabled: 1 core, 1 chip, 1 core/chip  
 CPU(s) orderable: 1 chip  
 Primary Cache: 32 KB I + 32 KB D on chip per chip  
 Secondary Cache: 512 KB I+D on chip per chip  
 L3 Cache: None  
 Other Cache: None  
 Memory: 8 GB (4x2 GB DDR2 PC2-4200E, 2 rank, CAS 4-4-4, with ECC)  
 Disk Subsystem: Seagate ST973401SS (SAS, 73GB 10000rpm)  
 Other Hardware: None

### Software

Operating System: 64-Bit SUSE LINUX Enterprise Server 10, Kernel 2.6.16.21-0.8-smp on an x86\_64  
 Compiler: Intel C++ Compiler for IA32/EM64T application, Version 9.1 - Build 20070215, Package-ID: l\_cc\_p\_9.1.047  
 Auto Parallel: No  
 File System: ReiserFS  
 System State: Multiuser, Runlevel 3  
 Base Pointers: 32-bit  
 Peak Pointers: 32/64-bit  
 Other Software: Smart Heap Library, Version 8.1



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## Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	806	12.1	814	12.0	<u>807</u>	<u>12.1</u>	729	13.4	725	13.5	<u>726</u>	<u>13.5</u>
401.bzip2	1301	7.42	<u>1303</u>	<u>7.41</u>	1304	7.40	<u>1251</u>	<u>7.72</u>	1250	7.72	1251	7.71
403.gcc	767	10.5	779	10.3	<u>776</u>	<u>10.4</u>	767	10.5	779	10.3	<u>776</u>	<u>10.4</u>
429.mcf	837	10.9	<u>852</u>	<u>10.7</u>	855	10.7	931	9.79	<u>932</u>	<u>9.79</u>	937	9.73
445.gobmk	838	12.5	843	12.4	<u>842</u>	<u>12.5</u>	<u>777</u>	<u>13.5</u>	782	13.4	777	13.5
456.hmmmer	1117	8.35	1120	8.33	<u>1120</u>	<u>8.33</u>	934	9.99	941	9.92	<u>938</u>	<u>9.95</u>
458.sjeng	<u>1003</u>	<u>12.1</u>	1003	12.1	1002	12.1	926	13.1	<u>933</u>	<u>13.0</u>	936	12.9
462.libquantum	1465	14.1	1480	14.0	<u>1477</u>	<u>14.0</u>	1288	16.1	<u>1292</u>	<u>16.0</u>	1296	16.0
464.h264ref	1148	19.3	1150	19.2	<u>1150</u>	<u>19.2</u>	<u>1134</u>	<u>19.5</u>	1133	19.5	1138	19.5
471.omnetpp	680	9.20	683	9.15	<u>683</u>	<u>9.15</u>	621	10.1	622	10.0	<u>621</u>	<u>10.1</u>
473.astar	1000	7.02	999	7.03	<u>999</u>	<u>7.02</u>	989	7.10	<u>992</u>	<u>7.08</u>	994	7.07
483.xalancbmk	438	15.7	441	15.6	<u>440</u>	<u>15.7</u>	438	15.7	441	15.6	<u>440</u>	<u>15.7</u>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run

## General Notes

The system bus runs at 800 MHz

All binaries were built with 32-bit Intel compiler except:  
401.bzip2, 456.hmmmer and 462.libquantum in peak were built with  
64-bit Intel compiler by changing the path for include and library files.

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## Base Compiler Invocation

C benchmarks:

icc

C++ benchmarks:

icpc



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## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_X64  
462.libquantum: -DSPEC\_CPU\_LINUX  
483.xalancbmk: -DSPEC\_CPU\_LINUX

## Base Optimization Flags

C benchmarks:

-fast

C++ benchmarks:

-xP -O3 -ipo -no-prec-div -L/opt/SmartHeap\_8\_1/lib -lsmartheap

## Peak Compiler Invocation

C benchmarks (except as noted below):

icc

401.bzip2: /opt/intel/cce/9.1.047/bin/icc  
-I/opt/intel/cce/9.1.047/include  
-L/opt/intel/cce/9.1.047/lib

456.hmmer: /opt/intel/cce/9.1.047/bin/icc  
-I/opt/intel/cce/9.1.047/include  
-L/opt/intel/cce/9.1.047/lib

462.libquantum: /opt/intel/cce/9.1.047/bin/icc  
-I/opt/intel/cce/9.1.047/include  
-L/opt/intel/cce/9.1.047/lib

C++ benchmarks:

icpc

## Peak Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_X64  
401.bzip2: -DSPEC\_CPU\_LP64  
456.hmmer: -DSPEC\_CPU\_LP64  
462.libquantum: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX  
483.xalancbmk: -DSPEC\_CPU\_LINUX



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## Peak Optimization Flags

C benchmarks:

400.perlbench: -prof\_gen(pass 1) -prof\_use(pass 2) -fast

401.bzip2: -fast

403.gcc: basepeak = yes

429.mcf: -prof\_gen(pass 1) -prof\_use(pass 2) -fast  
-L/opt/SmartHeap\_8\_1/lib -lsmartheap

445.gobmk: Same as 429.mcf

456.hmmmer: Same as 400.perlbench

458.sjeng: Same as 429.mcf

462.libquantum: Same as 400.perlbench

464.h264ref: Same as 429.mcf

C++ benchmarks:

471.omnetpp: -prof\_gen(pass 1) -prof\_use(pass 2) -xP -O3 -ipo  
-no-prec-div -L/opt/SmartHeap\_8\_1/lib -lsmartheap

473.astar: -prof\_gen(pass 1) -prof\_use(pass 2) -fast  
-L/opt/SmartHeap\_8\_1/lib -lsmartheap

483.xalancbmk: basepeak = yes

The flags file that was used to format this result can be browsed at

[http://www.spec.org/cpu2006/flags/CPU2006\\_flags.20090714.09.html](http://www.spec.org/cpu2006/flags/CPU2006_flags.20090714.09.html)

You can also download the XML flags source by saving the following link:

[http://www.spec.org/cpu2006/flags/CPU2006\\_flags.20090714.09.xml](http://www.spec.org/cpu2006/flags/CPU2006_flags.20090714.09.xml)

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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

Tested with SPEC CPU2006 v1.0.  
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