Fujitsu Siemens Computers
PRIMERGY TX120, Intel Celeron processor 440, 2.0 GHz

CPU2006 license: 22
Test sponsor: Fujitsu Siemens Computers
Tested by: Fujitsu Siemens Computers

Hardware

- CPU Name: Intel Celeron 440
- CPU Characteristics: 800 MHz system bus
- CPU MHz: 2000
- FPU: Integrated
- CPU(s) enabled: 1 core, 1 chip, 1 core/chip
- Primary Cache: 32 KB I + 32 KB D on chip per chip
- Secondary Cache: 512 KB I+D on chip per chip
- L3 Cache: None
- Other Cache: None
- Memory: 8 GB (4x2 GB DDR2 PC2-4200E, 2 rank, CAS 4-4-4, with ECC)
- Disk Subsystem: Seagate ST973401SS (SAS, 73GB 10000rpm)
- Other Hardware: None

Software

- Operating System: 64-Bit SUSE LINUX Enterprise Server 10, Kernel 2.6.16.21-0.8-smp on an x86_64
- Auto Parallel: No
- File System: ReiserFS
- System State: Multiuser, Runlevel 3
- Base Pointers: 32-bit
- Peak Pointers: 32/64-bit
- Other Software: Smart Heap Library, Version 8.1
SPEC CINT2006 Result

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SPECint2006 = 11.7
SPECint_base2006 = 11.1

Results Table

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Seconds</th>
<th>Ratio</th>
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<td>12.1</td>
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<td>10.3</td>
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<td>429.mcf</td>
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<td>19.5</td>
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<td>15.6</td>
<td>440</td>
<td>15.7</td>
</tr>
</tbody>
</table>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run

General Notes

The system bus runs at 800 MHz

All binaries were built with 32-bit Intel compiler except:
401.bzip2, 456.hmmer and 462.libquantum in peak were built with
64-bit Intel compiler by changing the path for include and library files.

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http://www.fujitsu-siemens.com/countries

Base Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc
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Test date: Jun-2007
Hardware Availability: Jul-2007
Software Availability: Mar-2007

Base Portability Flags

- 400.perlbench: -DSPEC_CPU_LINUX_X64
- 462.libquantum: -DSPEC_CPU_LINUX
- 483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
- -fast

C++ benchmarks:
- -xP -O3 -ipo -no-prec-div -L/opt/SmartHeap_8_1/lib -lsmartheap

Peak Compiler Invocation

C benchmarks (except as noted below):
- icc

  401.bzip2: /opt/intel/cce/9.1.047/bin/icc
  -I/opt/intel/cce/9.1.047/include
  -L/opt/intel/cce/9.1.047/lib

  456.hmmer: /opt/intel/cce/9.1.047/bin/icc
  -I/opt/intel/cce/9.1.047/include
  -L/opt/intel/cce/9.1.047/lib

  462.libquantum: /opt/intel/cce/9.1.047/bin/icc
  -I/opt/intel/cce/9.1.047/include
  -L/opt/intel/cce/9.1.047/lib

C++ benchmarks:
- icpc

Peak Portability Flags

- 400.perlbench: -DSPEC_CPU_LINUX_X64
- 401.bzip2: -DSPEC_CPU_LP64
- 456.hmmer: -DSPEC_CPU_LP64
- 462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
- 483.xalancbmk: -DSPEC_CPU_LINUX
# SPEC CINT2006 Result

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## Peak Optimization Flags

### C benchmarks:

- 400.perlbench: `-prof_gen(pass 1) -prof_use(pass 2) -fast`
- 401.bzip2: `-fast`
- 403.gcc: `basepeak = yes`
- 429.mcf: `-prof_gen(pass 1) -prof_use(pass 2) -fast`  
  `-L/opt/SmartHeap_8_1/lib -lsmartheap`
- 445.gobmk: `Same as 429.mcf`
- 456.hmmer: `Same as 400.perlbench`
- 458.sjeng: `Same as 429.mcf`
- 462.libquantum: `Same as 400.perlbench`
- 464.h264ref: `Same as 429.mcf`

### C++ benchmarks:

- 471.omnetpp: `-prof_gen(pass 1) -prof_use(pass 2) -xP -O3 -ipo`  
  `-no-prec-div -L/opt/SmartHeap_8_1/lib -lsmartheap`
- 473.astar: `-prof_gen(pass 1) -prof_use(pass 2) -fast`  
  `-L/opt/SmartHeap_8_1/lib -lsmartheap`
- 483.xalancbmk: `basepeak = yes`

The flags file that was used to format this result can be browsed at [http://www.spec.org/cpu2006/flags/CPU2006_flags.20090714.09.html](http://www.spec.org/cpu2006/flags/CPU2006_flags.20090714.09.html)

You can also download the XML flags source by saving the following link:  

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For questions about this result, please contact the tester. For other inquiries, please contact webmaster@spec.org.