SPEC® CINT2006 Result

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Fujitsu Siemens Computers

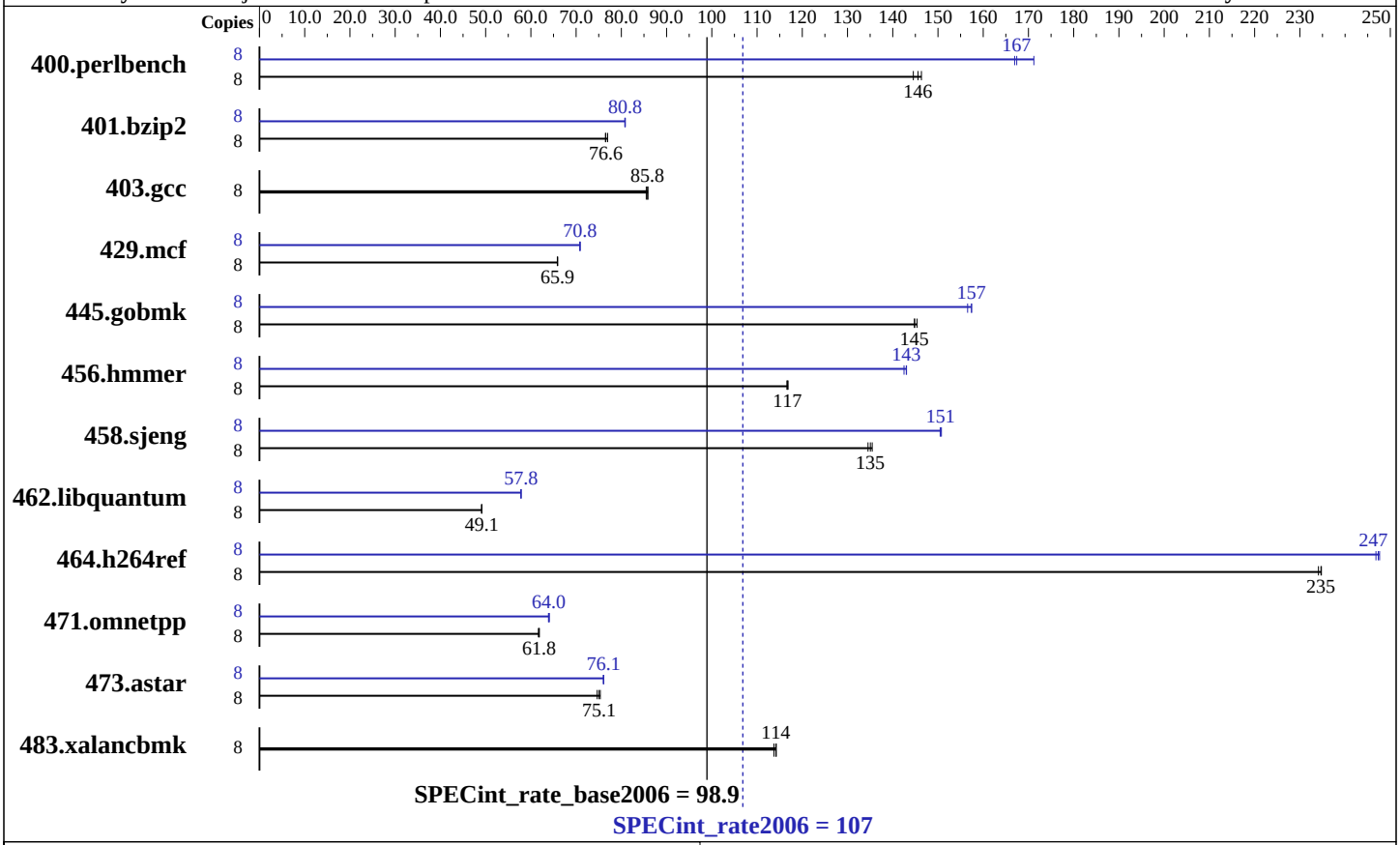
PRIMERGY TX300 S3, Intel Xeon processor X5365, 3.0 GHz

SPECint®_rate2006 = 107

SPECint_rate_base2006 = 98.9

CPU2006 license: 22
Test sponsor: Fujitsu Siemens Computers
Tested by: Fujitsu Siemens Computers

Test date: Jul-2007
Hardware Availability: Aug-2007
Software Availability: Jun-2007



Hardware

CPU Name:	Intel Xeon X5365
CPU Characteristics:	1333 MHz system bus
CPU MHz:	3000
FPU:	Integrated
CPU(s) enabled:	8 cores, 2 chips, 4 cores/chip
CPU(s) orderable:	1,2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	8 MB I+D on chip per chip, 4 MB shared / 2 cores
L3 Cache:	None
Other Cache:	None
Memory:	8 GB (8x1 GB DDR2 PC2-5300F, 2 rank, CAS 5-5-5, with ECC)
Disk Subsystem:	Seagate ST3146854SS (SAS, 146GB, 15000rpm)
Other Hardware:	None

Software

Operating System:	SUSE LINUX Enterprise Server 10 (x86_64), Kernel 2.6.16.21-0.8-smp
Compiler:	Intel C++ Compiler for IA32/EM64T application, Version 10.0 - Build 20070308, Package-ID: l_cc_p_10.0.023
Auto Parallel:	No
File System:	ext2
System State:	Multiuser, Runlevel 3
Base Pointers:	32-bit
Peak Pointers:	32/64-bit
Other Software:	Smart Heap Library, Version 8.1 binutils-2.17.tar.gz, Version 2.17

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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	8	534	146	**537**	**146**	541	145	8	457	171	468	167	**467**	**167**
401.bzip2	8	1003	77.0	**1008**	**76.6**	1009	76.5	8	**955**	**80.8**	954	80.9	956	80.8
403.gcc	8	750	85.9	753	85.5	**751**	**85.8**	8	750	85.9	753	85.5	**751**	**85.8**
429.mcf	8	1108	65.8	**1108**	**65.9**	1107	65.9	8	**1030**	**70.8**	1029	70.9	1030	70.8
445.gobmk	8	577	145	**579**	**145**	579	145	8	533	157	**533**	**157**	536	157
456.hmmer	8	**640**	**117**	640	117	639	117	8	522	143	524	142	**522**	**143**
458.sjeng	8	715	135	720	135	**717**	**135**	8	**643**	**151**	642	151	643	150
462.libquantum	8	3373	49.1	**3374**	**49.1**	3374	49.1	8	2868	57.8	**2867**	**57.8**	2866	57.8
464.h264ref	8	756	234	**754**	**235**	754	235	8	715	248	**716**	**247**	717	247
471.omnetpp	8	**809**	**61.8**	811	61.7	808	61.8	8	783	63.9	780	64.1	**781**	**64.0**
473.astar	8	752	74.7	**748**	**75.1**	745	75.3	8	738	76.1	739	76.0	**738**	**76.1**
483.xalancbmk	8	**484**	**114**	483	114	485	114	8	**484**	**114**	483	114	485	114

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

```
All binaries were built with 32-bit Intel compiler except:
401.bzip2 and 456.hmmer in peak were built with 64-bit Intel
compiler by changing the path for include and library files.

BIOS configuration:
Hardware Prefetch = Disable, Adjacent Sector Prefetch = Disable

This result was measured on the PRIMERGY RX300 S3. The PRIMERGY RX300 S3 and
the PRIMERGY TX300 S3 are electronically equivalent.

For information about Fujitsu Siemens Computers please see:
http://www.fujitsu-siemens.com
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Base Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
-fast

C++ benchmarks:
-xT -O3 -ipo -no-prec-div -ansi-alias
-L/opt/SmartHeap_8_1/lib -lsmartheap

Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):
icc

401.bzip2: /opt/intel/cce/10.0.023/bin/icc
-I/opt/intel/cce/10.0.023/include
-L/opt/intel/cce/10.0.023/lib

456.hmmer: /opt/intel/cce/10.0.023/bin/icc
-I/opt/intel/cce/10.0.023/include
-L/opt/intel/cce/10.0.023/lib

C++ benchmarks:
icpc

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX

Continued on next page

Peak Portability Flags (Continued)

483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -prof_gen(pass 1) -prof_use(pass 2) -fast

401.bzip2: -fast

403.gcc: basepeak = yes

429.mcf: -prof_gen(pass 1) -prof_use(pass 2) -fast -prefetch -L/opt/SmartHeap_8_1/lib -lsmartheap

445.gobmk: Same as 400.perlbench

456.hmmer: -prof_gen(pass 1) -prof_use(pass 2) -fast -unroll2

458.sjeng: -prof_gen(pass 1) -prof_use(pass 2) -fast -unroll4

462.libquantum: -prof_gen(pass 1) -prof_use(pass 2) -fast -prefetch -opt-streaming-stores always

464.h264ref: -prof_gen(pass 1) -prof_use(pass 2) -fast -unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -prof_gen(pass 1) -prof_use(pass 2) -fast -ansi-alias -L/opt/SmartHeap_8_1/lib -lsmartheap

473.astar: Same as 471.omnetpp

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/FSC_Intel_flags.html

You can also download the XML flags source by saving the following link:
`http://www.spec.org/cpu2006/flags/FSC_Intel_flags.xml`

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For other inquiries, please contact webmaster@spec.org.

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