# SPEC® CINT2006 Result

**Fujitsu Siemens Computers**

**PRIMERGY RX300 S3, Intel Xeon processor X5365, 3.0 GHz**

| SPECint®_rate2006 | 107 |
| SPECint_rate_base2006 | 98.9 |

**CPU2006 license: 22**

**Test date:** Jul-2007  
**Test sponsor:** Fujitsu Siemens Computers  
**Tested by:** Fujitsu Siemens Computers

**Hardware**

- **CPU Name:** Intel Xeon X5365  
- **CPU Characteristics:** 1333 MHz system bus  
- **CPU MHz:** 3000  
- **FPU:** Integrated  
- **CPU(s) enabled:** 8 cores, 2 chips, 4 cores/chip  
- **Primary Cache:** 32 KB I + 32 KB D on chip per core  
- **Secondary Cache:** 8 MB I+D on chip per chip, 4 MB shared / 2 cores  
- **Other Cache:** None  
- **Memory:** 8 GB (8x1 GB DDR2 PC2-5300F, 2 rank, CAS 5-5-5, with ECC)  
- **Disk Subsystem:** Seagate ST3146854SS (SAS, 146GB, 15000rpm)  
- **Other Hardware:** None

**Software**

- **Operating System:** SUSE LINUX Enterprise Server 10 (x86_64), Kernel 2.6.16.21-0.8-smp  
- **Compiler:** Intel C++ Compiler for IA32/EM64T application, Version 10.0 - Build 20070308, Package-ID: l_cc_p_10.0.023  
- **Auto Parallel:** No  
- **File System:** ext2  
- **System State:** Multiuser, Runlevel 3  
- **Base Pointers:** 32-bit  
- **Peak Pointers:** 32/64-bit  
- **Other Software:** Smart Heap Library, Version 8.1  
  - binutils-2.17.tar.gz, Version 2.17

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## Results Table

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</table>

**Base Compiler Invocation**

C benchmarks: `icc`

C++ benchmarks: `icpc`

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Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

### General Notes

All binaries were built with 32-bit Intel compiler except:

401.bzip2 and 456.hmmer in peak were built with 64-bit Intel compiler by changing the path for include and library files.

**BIOS configuration:**

Hardware Prefetch = Disable, Adjacent Sector Prefetch = Disable

The PRIMERGY RX300 S3 and the PRIMERGY TX300 S3 are electronically equivalent.

For information about Fujitsu Siemens Computers please see:

http://www.fujitsu-siemens.com
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Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
- -fast
C++ benchmarks:
- -xT -O3 -ipo -no-prec-div -ansi-alias
- -L/opt/SmartHeap_8_1/lib -lsmartheap

Base Other Flags

C benchmarks:
403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):
icc

401.bzip2: /opt/intel/cce/10.0.023/bin/icc
- I/opt/intel/cce/10.0.023/include
- L/opt/intel/cce/10.0.023/lib

456.hmmer: /opt/intel/cce/10.0.023/bin/icc
- I/opt/intel/cce/10.0.023/include
- L/opt/intel/cce/10.0.023/lib

C++ benchmarks:
icpc

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX

Continued on next page
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Peak Portability Flags (Continued)

483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -prof_gen(pass 1) -prof_use(pass 2) -fast
401.bzip2: -fast
403.gcc: basepeak = yes
429.mcf: -prof_gen(pass 1) -prof_use(pass 2) -fast -prefetch
-L/opt/SmartHeap_8_i/lib -lsmartheap
445.gobmk: Same as 400.perlbench
456.hmmer: -prof_gen(pass 1) -prof_use(pass 2) -fast -unroll2
458.sjeng: -prof_gen(pass 1) -prof_use(pass 2) -fast -unroll4
462.libquantum: -prof_gen(pass 1) -prof_use(pass 2) -fast -prefetch
-opt-streaming-stores always
464.h264ref: -prof_gen(pass 1) -prof_use(pass 2) -fast -unroll2
-ansi-alias

C++ benchmarks:

471.omnetpp: -prof_gen(pass 1) -prof_use(pass 2) -fast -ansi-alias
-L/opt/SmartHeap_8_i/lib -lsmartheap
473.astar: Same as 471.omnetpp
483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at http://www.spec.org/cpu2006/flags/FSC_Intel_flags.html
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You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/FSC_Intel_flags.xml

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For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.0.