



SPEC® CINT2006 Result

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Fujitsu Siemens Computers

PRIMERGY RX300 S3, Intel Xeon processor X5365,
3.0 GHz

SPECint®_rate2006 = 107

SPECint_rate_base2006 = 98.9

CPU2006 license: 22

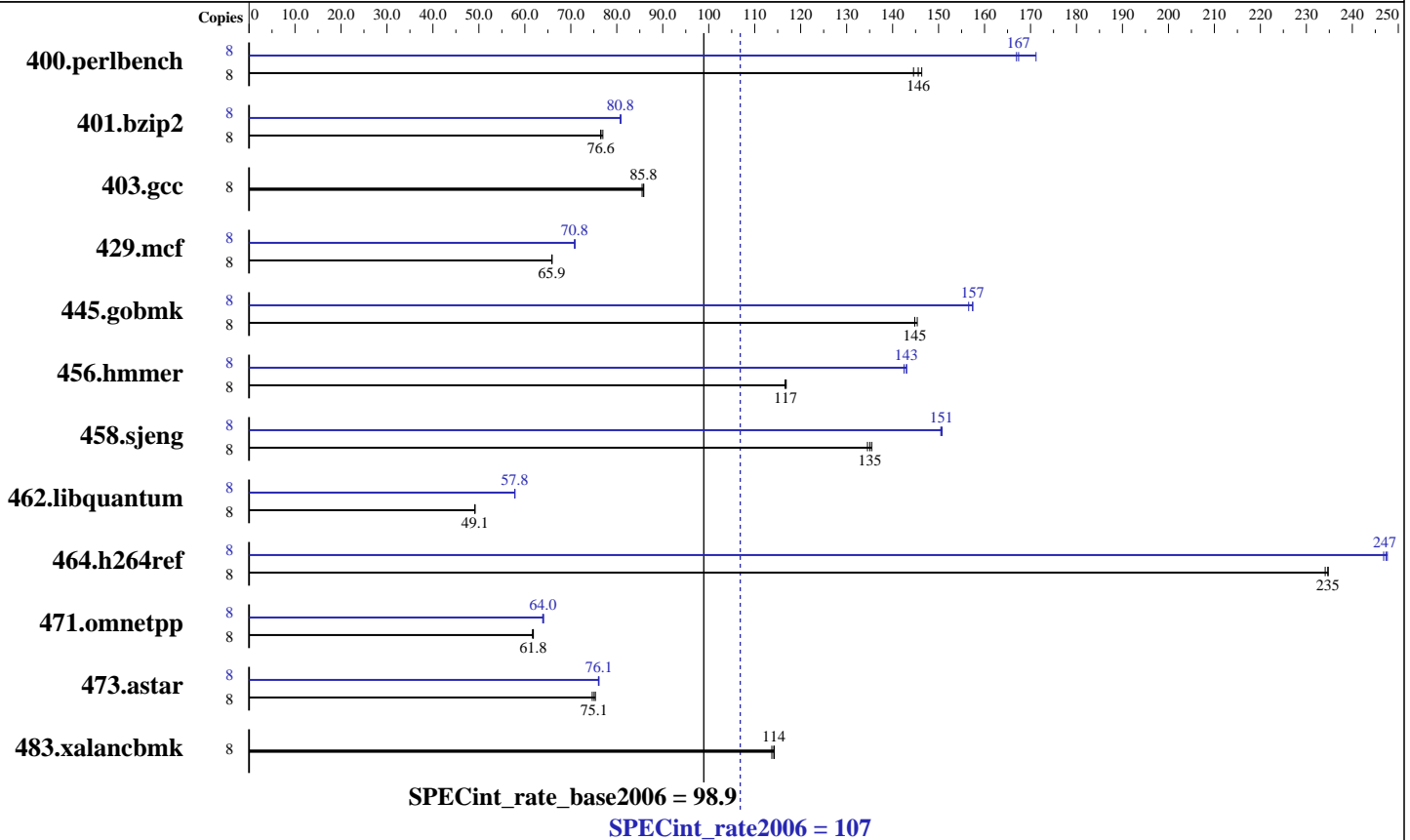
Test sponsor: Fujitsu Siemens Computers

Tested by: Fujitsu Siemens Computers

Test date: Jul-2007

Hardware Availability: Aug-2007

Software Availability: Jun-2007



Hardware

CPU Name: Intel Xeon X5365
 CPU Characteristics: 1333 MHz system bus
 CPU MHz: 3000
 FPU: Integrated
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip
 CPU(s) orderable: 1,2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 8 MB I+D on chip per chip, 4 MB shared / 2 cores
 L3 Cache: None
 Other Cache: None
 Memory: 8 GB (8x1 GB DDR2 PC2-5300F, 2 rank, CAS 5-5-5, with ECC)
 Disk Subsystem: Seagate ST3146854SS (SAS, 146GB, 15000rpm)
 Other Hardware: None

Software

Operating System: SUSE LINUX Enterprise Server 10 (x86_64), Kernel 2.6.16.21-0.8-smp
 Compiler: Intel C++ Compiler for IA32/EM64T application, Version 10.0 - Build 20070308, Package-ID: I_cc_p_10.0.023
 Auto Parallel: No
 File System: ext2
 System State: Multiuser, Runlevel 3
 Base Pointers: 32-bit
 Peak Pointers: 32/64-bit
 Other Software: Smart Heap Library, Version 8.1
 binutils-2.17.tar.gz, Version 2.17



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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	8	534	146	<u>537</u>	<u>146</u>	541	145	8	457	171	468	167	<u>467</u>	<u>167</u>
401.bzip2	8	1003	77.0	<u>1008</u>	<u>76.6</u>	1009	76.5	8	<u>955</u>	<u>80.8</u>	954	80.9	956	80.8
403.gcc	8	750	85.9	753	85.5	<u>751</u>	<u>85.8</u>	8	750	85.9	753	85.5	<u>751</u>	<u>85.8</u>
429.mcf	8	1108	65.8	<u>1108</u>	<u>65.9</u>	1107	65.9	8	<u>1030</u>	<u>70.8</u>	1029	70.9	1030	70.8
445.gobmk	8	577	145	<u>579</u>	<u>145</u>	579	145	8	533	157	<u>533</u>	<u>157</u>	536	157
456.hmmr	8	<u>640</u>	<u>117</u>	640	117	639	117	8	522	143	524	142	<u>522</u>	<u>143</u>
458.sjeng	8	715	135	720	135	<u>717</u>	<u>135</u>	8	<u>643</u>	<u>151</u>	642	151	643	150
462.libquantum	8	3373	49.1	<u>3374</u>	<u>49.1</u>	3374	49.1	8	2868	57.8	<u>2867</u>	<u>57.8</u>	2866	57.8
464.h264ref	8	756	234	<u>754</u>	<u>235</u>	754	235	8	715	248	<u>716</u>	<u>247</u>	717	247
471.omnetpp	8	<u>809</u>	<u>61.8</u>	811	61.7	808	61.8	8	783	63.9	780	64.1	<u>781</u>	<u>64.0</u>
473.astar	8	752	74.7	<u>748</u>	<u>75.1</u>	745	75.3	8	738	76.1	739	76.0	<u>738</u>	<u>76.1</u>
483.xalancbmk	8	<u>484</u>	<u>114</u>	483	114	485	114	8	<u>484</u>	<u>114</u>	483	114	485	114

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

All binaries were built with 32-bit Intel compiler except:
401.bzip2 and 456.hmmr in peak were built with 64-bit Intel
compiler by changing the path for include and library files.

BIOS configuration:

Hardware Prefetch = Disable, Adjacent Sector Prefetch = Disable

The PRIMERGY RX300 S3 and the PRIMERGY TX300 S3 are electronically equivalent.

For information about Fujitsu Siemens Computers please see:
<http://www.fujitsu-siemens.com>

Base Compiler Invocation

C benchmarks:

icc

C++ benchmarks:

icpc



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Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
473.astar: -DSPEC_CPU_LITTLE_ENDIAN
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:

-fast

C++ benchmarks:

-xT -O3 -ipo -no-prec-div -ansi-alias
-L/opt/SmartHeap_8_1/lib -lsmarheap

Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):

icc

401.bzip2: /opt/intel/cce/10.0.023/bin/icc
-I/opt/intel/cce/10.0.023/include
-L/opt/intel/cce/10.0.023/lib

456.hmmer: /opt/intel/cce/10.0.023/bin/icc
-I/opt/intel/cce/10.0.023/include
-L/opt/intel/cce/10.0.023/lib

C++ benchmarks:

icpc

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64

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Peak Portability Flags (Continued)

462.libquantum: -DSPEC_CPU_LINUX
473.astar: -DSPEC_CPU_LITTLE_ENDIAN
483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -prof_gen(pass 1) -prof_use(pass 2) -fast
401.bzip2: -fast
403.gcc: basepeak = yes
429.mcf: -prof_gen(pass 1) -prof_use(pass 2) -fast -prefetch
-L/opt/SmartHeap_8_1/lib -lsmarheap
445.gobmk: Same as 400.perlbench
456.hmmer: -prof_gen(pass 1) -prof_use(pass 2) -fast -unroll2
458.sjeng: -prof_gen(pass 1) -prof_use(pass 2) -fast -unroll4
462.libquantum: -prof_gen(pass 1) -prof_use(pass 2) -fast -prefetch
-opt-streaming-stores always
464.h264ref: -prof_gen(pass 1) -prof_use(pass 2) -fast -unroll2
-ansi-alias

C++ benchmarks:

471.omnetpp: -prof_gen(pass 1) -prof_use(pass 2) -fast -ansi-alias
-L/opt/SmartHeap_8_1/lib -lsmarheap
473.astar: Same as 471.omnetpp
483.xalancbmk: basepeak = yes

Peak Other Flags

Same as Base Other Flags

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/FSC_Intel_flags.xml.html



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You can also download the XML flags source by saving the following link:

http://www.spec.org/cpu2006/flags/FSC_Intel_flags.xml.xml

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For other inquiries, please contact webmaster@spec.org.

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