Supermicro
Motherboard X7DA3

SPEClnt®2006 = 14.5
SPEClnt_base2006 = 13.8

Hardware
CPU Name: Intel Xeon 5148 LV
CPU Characteristics: 2.33GHz 1333 System Bus
CPU MHz: 2333
FPU: Integrated
CPU(s) enabled: 4 cores, 2 chips, 2 cores/chip
CPU(s) orderable: 1, 2 chips
Primary Cache: 32 KB I + 32 KB D on chip per core
Secondary Cache: 4 MB I+D on chip per chip
L3 Cache: None
Other Cache: None
Memory: 4 GB (2 X 2GB ECC, CL5, FBDIMM)
Disk Subsystem: Seagate ST3750640AS 750GB SATA II, 7200RPM
Other Hardware: None

Software
Compiler: Intel C++ Compiler for IA32 version 9.1
Build no 20070322Z
Microsoft Visual Studio .Net 2003 (for libraries)
Auto Parallel: No
File System: NTFS
System State: Default
Base Pointers: 32-bit
Peak Pointers: 32-bit
Other Software: SmartHeap Library Version 8.0

Test date: Jun-2007
Hardware Availability: May-2007
Software Availability: Apr-2007
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SPECint_base2006 = 13.8

CPU2006 license: 001176
Test date: Jun-2007
Test sponsor: Supermicro
Hardware Availability: May-2007
Tested by: Supermicro
Software Availability: Apr-2007

Results Table

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
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<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
</tr>
</thead>
<tbody>
<tr>
<td>Base</td>
<td>591</td>
<td>16.5</td>
<td>591</td>
<td>16.5</td>
<td>591</td>
<td>16.5</td>
<td>534</td>
<td>18.3</td>
<td>533</td>
<td>18.3</td>
</tr>
<tr>
<td>400.perlbench</td>
<td>825</td>
<td>11.7</td>
<td>839</td>
<td>11.5</td>
<td>813</td>
<td>11.9</td>
<td>794</td>
<td>12.2</td>
<td>791</td>
<td>12.2</td>
</tr>
<tr>
<td>401.bzip2</td>
<td>746</td>
<td>10.8</td>
<td>751</td>
<td>10.7</td>
<td>751</td>
<td>10.7</td>
<td>724</td>
<td>11.1</td>
<td>719</td>
<td>11.2</td>
</tr>
<tr>
<td>403.gcc</td>
<td>566</td>
<td>16.1</td>
<td>566</td>
<td>16.1</td>
<td>566</td>
<td>16.1</td>
<td>566</td>
<td>16.1</td>
<td>566</td>
<td>16.1</td>
</tr>
<tr>
<td>445.gobmk</td>
<td>686</td>
<td>15.3</td>
<td>686</td>
<td>15.3</td>
<td>686</td>
<td>15.3</td>
<td>622</td>
<td>16.9</td>
<td>623</td>
<td>16.8</td>
</tr>
<tr>
<td>456.hmmer</td>
<td>959</td>
<td>9.73</td>
<td>958</td>
<td>9.73</td>
<td>959</td>
<td>9.73</td>
<td>933</td>
<td>10.0</td>
<td>933</td>
<td>10.0</td>
</tr>
<tr>
<td>458.sjeng</td>
<td>840</td>
<td>14.4</td>
<td>840</td>
<td>14.4</td>
<td>840</td>
<td>14.4</td>
<td>791</td>
<td>15.3</td>
<td>791</td>
<td>15.3</td>
</tr>
<tr>
<td>462.libquantum</td>
<td>1567</td>
<td>13.2</td>
<td>1568</td>
<td>13.2</td>
<td>1567</td>
<td>13.2</td>
<td>1554</td>
<td>13.3</td>
<td>1554</td>
<td>13.3</td>
</tr>
<tr>
<td>464.h264ref</td>
<td>917</td>
<td>24.1</td>
<td>918</td>
<td>24.1</td>
<td>917</td>
<td>24.1</td>
<td>896</td>
<td>24.7</td>
<td>893</td>
<td>24.8</td>
</tr>
<tr>
<td>471.omnetpp</td>
<td>583</td>
<td>10.7</td>
<td>584</td>
<td>10.7</td>
<td>583</td>
<td>10.7</td>
<td>534</td>
<td>11.7</td>
<td>534</td>
<td>11.7</td>
</tr>
<tr>
<td>473.astar</td>
<td>629</td>
<td>11.2</td>
<td>630</td>
<td>11.1</td>
<td>629</td>
<td>11.2</td>
<td>574</td>
<td>12.2</td>
<td>573</td>
<td>12.2</td>
</tr>
<tr>
<td>483.xalancbmk</td>
<td>389</td>
<td>17.8</td>
<td>390</td>
<td>17.7</td>
<td>389</td>
<td>17.7</td>
<td>397</td>
<td>17.4</td>
<td>396</td>
<td>17.4</td>
</tr>
</tbody>
</table>

Base Compiler Invocation

C benchmarks:
icl -Qvc7.1 -Qc99

C++ benchmarks:
icl -Qvc7.1

Base Portability Flags

403.gcc: -DSPEC_CPU_WIN32
464.h264ref: -DSPEC_CPU_NO_INTTYPES -DWIN32

Base Optimization Flags

C benchmarks:
-fast /F512000000 shlw32m.lib -link /FORCE:MULTIPLE

General Notes

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Tested systems can be used with CSE-825TQ-R700LPV case,
Product description located as of:http://www.supermicro.com/products/motherboard/Xeon1333/5000X/X7DA3.cfm
The system bus runs at 1333 MHz
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Base Optimization Flags (Continued)

C++ benchmarks:
- fast -Qcxx_features /F512000000 shlw32m.lib
- link /FORCE:MULTIPLE

Base Other Flags

C benchmarks:
403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks:
icl -Qvc7.1 -Qc99
C++ benchmarks:
icl -Qvc7.1

Peak Portability Flags

403.gcc: -DSPEC_CPU_WIN32
464.h264ref: -DSPEC_CPU_NO_INTTYPES -DWIN32

Peak Optimization Flags

C benchmarks:
400.perlbench: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast /F512000000
shlw32m.lib -link /FORCE:MULTIPLE
401.bzip2: Same as 400.perlbench
403.gcc: Same as 400.perlbench
429.mcf: basepeak = yes
445.gobmk: Same as 400.perlbench
456.hmmer: Same as 400.perlbench
458.sjeng: Same as 400.perlbench

Continued on next page
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Peak Optimization Flags (Continued)

462.libquantum: Same as 400.perlbench
464.h264ref: Same as 400.perlbench

C++ benchmarks:

471.omnetpp: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qcxx_features /F512000000 shlw32m.lib -link /FORCE:MULTIPLE
473.astar: -Qprof_gen(pass 1) -Qprof_use(pass 2) -QxP -O2 -Qipo -Qprec-div -Qunroll4 -Ob2 -Qsfalign16 -Qcxx_features /F512000000 shlw32m.lib -link /FORCE:MULTIPLE
483.xalancbmk: Same as 471.omnetpp

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at http://www.spec.org/cpu2006/flags/Intel-ic91-ia32-flags.html
You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/Intel-ic91-ia32-flags.xml

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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.0.
Originally published on 8 August 2007.