Acer Incorporated
Acer Altos R520 (Intel Xeon X5355)

CPU2006 license: 97
Test sponsor: Acer Incorporated
Test date: Jun-2007
Hardware Availability: Nov-2006

Tested by: Acer Incorporated
Software Availability: Jun-2007

SPECint_rate2006 = 53.4
SPECint_rate_base2006 = 49.1

CPU Name: Intel Xeon X5355
CPU Characteristics: 1333MHz system bus
CPU MHz: 2660
FPU: Integrated
CPU(s) enabled: 4 cores, 1 chip, 4 cores/chip
CPU(s) orderable: 1, 2 chips
Primary Cache: 32 KB I + 32 KB D on chip per core
Secondary Cache: 8 MB I+D on chip per chip, 4 MB shared / 2 cores
L3 Cache: None
Other Cache: None
Memory: 16 GB (8 x 2048MB ECC FR-DIMM DDR2-667 CL5-5-5)
Disk Subsystem: 1 x 73GB 10000RPM SAS HDD
Other Hardware: None

Software
Operating System: SuSE Linux Enterprise Server 10 EM64T (kernel 2.6.16.21-0.8-smp)
Compiler: Intel C++ Compiler for Linux32 version 10.0 Build 20070426 Package ID: l_cc_p_10.0.023
Auto Parallel: No
File System: ReiserFS
System State: Default
Base Pointers: 32-bit
Peak Pointers: 32-bit
Other Software: None
SPEC CINT2006 Result

Acer Incorporated
Acer Altos R520 (Intel Xeon X5355)

SPECint_rate2006 = 53.4
SPECint_rate_base2006 = 49.1

CPU2006 license: 97
Test sponsor: Acer Incorporated
Tested by: Acer Incorporated
Hardware Availability: Nov-2006
Software Availability: Jun-2007

Results Table

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Copies</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Copies</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
</tr>
</thead>
<tbody>
<tr>
<td>400.perlbench</td>
<td>4</td>
<td>581</td>
<td>67.2</td>
<td>583</td>
<td>67.1</td>
<td>581</td>
<td>67.3</td>
<td>4</td>
<td>493</td>
<td>79.3</td>
<td>494</td>
<td>79.3</td>
<td>496</td>
<td>78.7</td>
</tr>
<tr>
<td>401.bzip2</td>
<td>4</td>
<td>1036</td>
<td>37.3</td>
<td>1047</td>
<td>36.9</td>
<td>1047</td>
<td>36.9</td>
<td>4</td>
<td>1011</td>
<td>38.2</td>
<td>992</td>
<td>38.9</td>
<td>988</td>
<td>39.1</td>
</tr>
<tr>
<td>403.gcc</td>
<td>4</td>
<td>707</td>
<td>45.6</td>
<td>715</td>
<td>45.0</td>
<td>708</td>
<td>45.5</td>
<td>4</td>
<td>707</td>
<td>45.6</td>
<td>715</td>
<td>45.0</td>
<td>708</td>
<td>45.5</td>
</tr>
<tr>
<td>429.mcf</td>
<td>4</td>
<td>926</td>
<td>39.4</td>
<td>926</td>
<td>39.4</td>
<td>927</td>
<td>39.4</td>
<td>4</td>
<td>911</td>
<td>40.1</td>
<td>911</td>
<td>40.0</td>
<td>910</td>
<td>40.1</td>
</tr>
<tr>
<td>445.gobmk</td>
<td>4</td>
<td>645</td>
<td>65.1</td>
<td>644</td>
<td>65.2</td>
<td>644</td>
<td>65.1</td>
<td>4</td>
<td>592</td>
<td>70.9</td>
<td>592</td>
<td>70.8</td>
<td>591</td>
<td>71.0</td>
</tr>
<tr>
<td>456.hmmer</td>
<td>4</td>
<td>713</td>
<td>52.4</td>
<td>714</td>
<td>52.2</td>
<td>715</td>
<td>52.2</td>
<td>4</td>
<td>577</td>
<td>64.7</td>
<td>582</td>
<td>64.1</td>
<td>579</td>
<td>64.5</td>
</tr>
<tr>
<td>458.sjeng</td>
<td>4</td>
<td>795</td>
<td>60.9</td>
<td>792</td>
<td>61.1</td>
<td>800</td>
<td>60.5</td>
<td>4</td>
<td>710</td>
<td>68.2</td>
<td>711</td>
<td>68.0</td>
<td>712</td>
<td>68.0</td>
</tr>
<tr>
<td>462.libquantum</td>
<td>4</td>
<td>2896</td>
<td>28.6</td>
<td>2896</td>
<td>28.6</td>
<td>2894</td>
<td>28.6</td>
<td>4</td>
<td>2375</td>
<td>34.9</td>
<td>2376</td>
<td>34.9</td>
<td>2375</td>
<td>34.9</td>
</tr>
<tr>
<td>464.h264ref</td>
<td>4</td>
<td>844</td>
<td>105</td>
<td>844</td>
<td>105</td>
<td>843</td>
<td>105</td>
<td>4</td>
<td>797</td>
<td>111</td>
<td>799</td>
<td>111</td>
<td>797</td>
<td>111</td>
</tr>
<tr>
<td>471.omnetpp</td>
<td>4</td>
<td>739</td>
<td>33.8</td>
<td>740</td>
<td>33.8</td>
<td>739</td>
<td>33.8</td>
<td>4</td>
<td>700</td>
<td>35.7</td>
<td>699</td>
<td>35.7</td>
<td>700</td>
<td>35.7</td>
</tr>
<tr>
<td>473.astar</td>
<td>4</td>
<td>768</td>
<td>36.6</td>
<td>765</td>
<td>36.7</td>
<td>770</td>
<td>36.5</td>
<td>4</td>
<td>721</td>
<td>38.9</td>
<td>723</td>
<td>38.8</td>
<td>724</td>
<td>38.8</td>
</tr>
<tr>
<td>483.xalancbmk</td>
<td>4</td>
<td>475</td>
<td>58.1</td>
<td>478</td>
<td>57.8</td>
<td>478</td>
<td>57.8</td>
<td>4</td>
<td>475</td>
<td>58.1</td>
<td>478</td>
<td>57.8</td>
<td>478</td>
<td>57.8</td>
</tr>
</tbody>
</table>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

This result was measured on the Acer Altos R520. The Altos R720 and Altos R520 are electronically equivalent.
taskset was used to bind copies to cores

Base Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX
Acer Incorporated
Acer Altos R520 (Intel Xeon X5355)

SPECint_rate2006 = 53.4
SPECint_rate_base2006 = 49.1

CPU2006 license: 97
Test sponsor: Acer Incorporated
Tested by: Acer Incorporated

Base Optimization Flags

C benchmarks:
  -fast

C++ benchmarks:
  -xT -ipo -O3 -no-prec-div -Wl,-z,muldefs
  -L/spec/cpu2006.1.0/lib -lsmartheap

Base Other Flags

C benchmarks:
  403.gcc: Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):
  icc

  401.bzip2: /opt/intel/cce/10.0.023/bin/icc
              -L/opt/intel/cce/10.0.023/lib
              -I/opt/intel/cce/10.0.023/include

  456.hmmer: /opt/intel/cce/10.0.023/bin/icc
             -L/opt/intel/cce/10.0.023/lib
             -I/opt/intel/cce/10.0.023/include

C++ benchmarks:
  icpc

Peak Portability Flags

  400.perlbench: -DSPEC_CPU_LINUX_IA32
  401.bzip2: -DSPEC_CPU_LP64
  456.hmmer: -DSPEC_CPU_LP64
  462.libquantum: -DSPEC_CPU_LINUX
  483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

Continued on next page
Acer Incorporated
Acer Altos R520 (Intel Xeon X5355)

SPECint_rate2006 = 53.4
SPECint_rate_base2006 = 49.1

CPU2006 license: 97
Test sponsor: Acer Incorporated
Tested by: Acer Incorporated

Peak Optimization Flags (Continued)

400.perlbench: -prof-gen(pass 1) -prof-use(pass 2) -fast -ansi-alias
-prefetch

401.bzip2: -prof-gen(pass 1) -prof-use(pass 2) -fast

403.gcc: basepeak = yes

429.mcf: -fast -prefetch

445.gobmk: -prof-gen(pass 1) -prof-use(pass 2) -xT -O2 -ipo
-no-prec_div -ansi-alias

456.hmmer: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2
-ansi-alias

458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll4

462.libquantum: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll4 -Ob0
-prefetch -opt-streaming-stores always

464.h264ref: Same as 456.hmmer

C++ benchmarks:

471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo
-no-prec_div -ansi-alias -Wl,-z,muldefs
-L/spec/cpu2006.1.0/lib -lsmartheap

473.astar: Same as 471.omnetpp

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090714.41.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090714.41.xml
### Acer Incorporated

**Acer Altos R520 (Intel Xeon X5355)**

<table>
<thead>
<tr>
<th>Specification</th>
<th>Value</th>
</tr>
</thead>
<tbody>
<tr>
<td>SPECint_rate2006</td>
<td>53.4</td>
</tr>
<tr>
<td>SPECint_rate_base2006</td>
<td>49.1</td>
</tr>
</tbody>
</table>

**CPU2006 license:** 97  
**Test sponsor:** Acer Incorporated  
**Tested by:** Acer Incorporated  
**Test date:** Jun-2007  
**Hardware Availability:** Nov-2006  
**Software Availability:** Jun-2007

---

SPEC and SPECint are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.  
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.0.  
Originally published on 8 August 2007.