Acer Incorporated
Acer Altos R720 (Intel Xeon E5335)

SPECint\_rate2006 = 84.1
SPECint\_rate_base2006 = 77.7

CPU2006 license: 97
Test date: Jun-2007
Test sponsor: Acer Incorporated
Hardware Availability: Nov-2006
Tested by: Acer Incorporated
Software Availability: Jun-2007

CPU Name: Intel Xeon E5335
CPU Characteristics: 1333MHz system bus
CPU MHz: 2000
FPU: Integrated
CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip
CPU(s) orderable: 1, 2 chips
Primary Cache: 32 KB I + 32 KB D on chip per core
Secondary Cache: 8 MB I+D on chip per chip, 4 MB shared / 2 cores
L3 Cache: None
Other Cache: None
Memory: 16 GB (8 x 2048MB ECC FR-DIMM DDR2-667 CL5-5-5)
Disk Subsystem: 1 x 73GB 10000RPM SAS HDD
Other Hardware: None

Operating System: SuSE Linux Enterprise Server 10 EM64T (kernel 2.6.16.21-0.8-smp)
Compiler: Intel C++ Compiler for Linux32 version 10.0 Build 20070426 Package ID: l_cc_p_10.0.023
Auto Parallel: No
File System: ReiserFS
System State: Default
Base Pointers: 32-bit
Peak Pointers: 32-bit
Other Software: None
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Results Table

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<td>593</td>
<td>93.1</td>
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</table>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

This result was measured on the Acer Altos R520
The Altos R720 and Altos R520 are electronically equivalent.
taskset was used to bind copies to cores

Base Compiler Invocation

C benchmarks:
  icc

C++ benchmarks:
  icpc

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX
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Base Optimization Flags

C benchmarks:
- fast

C++ benchmarks:
- -xT
- -ipo
- -03
- -no-prec-div
- -Wl,-z,muldefs
- -L/spec/cpu2006.1.0/lib
- -lsmartheap

Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):

icc

401.bzip2: /opt/intel/cce/10.0.023/bin/icc
- L/opt/intel/cce/10.0.023/lib
- L/opt/intel/cce/10.0.023/include

456.hmmer: /opt/intel/cce/10.0.023/bin/icc
- L/opt/intel/cce/10.0.023/lib
- L/opt/intel/cce/10.0.023/include

C++ benchmarks:

icpc

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

Continued on next page
SPEC CINT2006 Result

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Peak Optimization Flags (Continued)

400.perlbench: -prof-gen(pass 1) -prof-use(pass 2) -fast -ansi-alias
-prefetch
401.bzip2: -prof-gen(pass 1) -prof-use(pass 2) -fast
403.gcc: basepeak = yes
429.mcf: -fast -prefetch
445.gobmk: -prof-gen(pass 1) -prof-use(pass 2) -xT -O2 -ipo
-no-prec_div -ansi-alias
456.hmmer: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2
-ansi-alias
458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll4
462.libquantum: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll4 -Ob0
-prefetch -opt-streaming-stores always
464.h264ref: Same as 456.hmmer

C++ benchmarks:

471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo
-no-prec_div -ansi-alias -Wl,-z,muldefs
-L/spec/cpu2006.1.0/lib -lsmartheap
473.astar: Same as 471.omnetpp
483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090714.41.html
You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090714.41.xml
### SPEC CINT2006 Result

**Acer Incorporated**

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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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