Fujitsu Siemens Computers

PRIMERGY RX300 S3, Intel Xeon processor E5310, 1.60 GHz

**SPECint® rate2006 = 40.0**
SPECint rate base2006 = 37.3

---

**Hardware**

CPU Name: Intel Xeon E5310  
CPU Characteristics: 1067 MHz system bus  
CPU MHz: 1600  
FPU: Integrated  
CPU(s) enabled: 4 cores, 1 chip, 4 cores/chip  
CPU(s) orderable: 1.2 chips  
Primary Cache: 32 KB I + 32 KB D on chip per core  
Secondary Cache: 8 MB I+D on chip per chip, 4 MB shared / 2 cores  
L3 Cache: None  
Other Cache: None  
Memory: 8 GB (8x1 GB DDR2 PC2-5300F, 2 rank, CAS 5-5-5, with ECC)  
Disk Subsystem: Seagate ST3146854SS (SAS, 146GB, 15000rpm)  
Other Hardware: None

---

**Software**

Operating System: SUSE LINUX Enterprise Server 10 (x86_64), Kernel 2.6.16.21-0.8-smp  
Compiler: Intel C++ Compiler for IA32/EM64T application, Version 10.0 - Build 20070708, Package-ID: l_cc_p_10.0.023  
Auto Parallel: No  
File System: ext2  
System State: Multiuser, Runlevel 3  
Base Pointers: 32-bit  
Peak Pointers: 32/64-bit  
Other Software: Smart Heap Library, Version 8.1, binutils-2.17.tar.gz, Version 2.17
## SPEC CINT2006 Result

### Fujitsu Siemens Computers

PRIMERGY RX300 S3, Intel Xeon processor E5310, 1.60 GHz

**SPECint_rate2006** = 40.0

**SPECint_rate_base2006** = 37.3

**CPU2006 license:** 22

**Test sponsor:** Fujitsu Siemens Computers

**Tested by:** Fujitsu Siemens Computers

**Test date:** Jul-2007

**Hardware Availability:** Nov-2006

**Software Availability:** Jun-2007

---

### Results Table

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Copies</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Copies</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
</tr>
</thead>
<tbody>
<tr>
<td>perlbench</td>
<td>4</td>
<td>907</td>
<td>43.1</td>
<td>910</td>
<td>42.9</td>
<td>915</td>
<td>42.7</td>
<td>4</td>
<td>761</td>
<td>51.3</td>
<td>763</td>
<td>51.2</td>
<td>760</td>
<td>51.4</td>
<td></td>
<td></td>
</tr>
<tr>
<td>bzip2</td>
<td>4</td>
<td>1289</td>
<td>30.0</td>
<td>1300</td>
<td>29.7</td>
<td>1294</td>
<td>29.8</td>
<td>4</td>
<td>1200</td>
<td>32.2</td>
<td>1197</td>
<td>32.2</td>
<td>1197</td>
<td>32.2</td>
<td></td>
<td></td>
</tr>
<tr>
<td>gcc</td>
<td>4</td>
<td>865</td>
<td>37.2</td>
<td>869</td>
<td>37.1</td>
<td>860</td>
<td>37.5</td>
<td>4</td>
<td>865</td>
<td>37.2</td>
<td>869</td>
<td>37.1</td>
<td>860</td>
<td>37.5</td>
<td></td>
<td></td>
</tr>
<tr>
<td>mcf</td>
<td>4</td>
<td>964</td>
<td>37.8</td>
<td>964</td>
<td>37.8</td>
<td>964</td>
<td>37.8</td>
<td>4</td>
<td>958</td>
<td>38.1</td>
<td>959</td>
<td>38.0</td>
<td>960</td>
<td>38.0</td>
<td></td>
<td></td>
</tr>
<tr>
<td>gobmk</td>
<td>4</td>
<td>1000</td>
<td>41.9</td>
<td>1001</td>
<td>41.9</td>
<td>1001</td>
<td>41.9</td>
<td>4</td>
<td>917</td>
<td>45.7</td>
<td>916</td>
<td>45.8</td>
<td>917</td>
<td>45.7</td>
<td></td>
<td></td>
</tr>
<tr>
<td>hmer</td>
<td>4</td>
<td>1119</td>
<td>33.4</td>
<td>1116</td>
<td>33.4</td>
<td>1117</td>
<td>33.4</td>
<td>4</td>
<td>883</td>
<td>42.3</td>
<td>887</td>
<td>42.1</td>
<td>886</td>
<td>42.1</td>
<td></td>
<td></td>
</tr>
<tr>
<td>sjeng</td>
<td>4</td>
<td>1265</td>
<td>38.3</td>
<td>1268</td>
<td>38.2</td>
<td>1266</td>
<td>38.2</td>
<td>4</td>
<td>1124</td>
<td>43.0</td>
<td>1127</td>
<td>43.0</td>
<td>1123</td>
<td>43.1</td>
<td></td>
<td></td>
</tr>
<tr>
<td>libquantum</td>
<td>4</td>
<td>2866</td>
<td>28.9</td>
<td>2862</td>
<td>29.0</td>
<td>2862</td>
<td>29.0</td>
<td>4</td>
<td>2878</td>
<td>28.8</td>
<td>2884</td>
<td>28.7</td>
<td>2888</td>
<td>28.7</td>
<td></td>
<td></td>
</tr>
<tr>
<td>h264ref</td>
<td>4</td>
<td>1356</td>
<td>65.3</td>
<td>1353</td>
<td>65.4</td>
<td>1355</td>
<td>65.3</td>
<td>4</td>
<td>1285</td>
<td>68.9</td>
<td>1290</td>
<td>68.6</td>
<td>1283</td>
<td>69.0</td>
<td></td>
<td></td>
</tr>
<tr>
<td>omnetpp</td>
<td>4</td>
<td>878</td>
<td>28.5</td>
<td>883</td>
<td>28.3</td>
<td>897</td>
<td>27.9</td>
<td>4</td>
<td>815</td>
<td>30.7</td>
<td>816</td>
<td>30.6</td>
<td>817</td>
<td>30.6</td>
<td></td>
<td></td>
</tr>
<tr>
<td>astar</td>
<td>4</td>
<td>984</td>
<td>28.5</td>
<td>983</td>
<td>28.6</td>
<td>984</td>
<td>28.5</td>
<td>4</td>
<td>965</td>
<td>29.1</td>
<td>968</td>
<td>29.0</td>
<td>964</td>
<td>29.1</td>
<td></td>
<td></td>
</tr>
<tr>
<td>xalancbmk</td>
<td>4</td>
<td>565</td>
<td>48.8</td>
<td>568</td>
<td>48.6</td>
<td>571</td>
<td>48.4</td>
<td>4</td>
<td>565</td>
<td>48.8</td>
<td>568</td>
<td>48.6</td>
<td>571</td>
<td>48.4</td>
<td></td>
<td></td>
</tr>
</tbody>
</table>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

---

### General Notes

All binaries were built with 32-bit Intel compiler except:

- 401.bzip2 and 456.hmmer in peak were built with 64-bit Intel compiler by changing the path for include and library files.

**BIOS configuration:**

Adjacent Sector Prefetch = Disable

This result was measured on the PRIMERGY RX300 S3. The PRIMERGY RX300 S3 and the PRIMERGY TX300 S3 are electronically equivalent.

For information about Fujitsu Siemens Computers in your country please see:

http://www.fujitsu-siemens.com

---

### Base Compiler Invocation

**C benchmarks:**

- icc

**C++ benchmarks:**

- icpc

---

Standard Performance Evaluation Corporation

info@spec.org

http://www.spec.org/
SPEC CINT2006 Result

Fujitsu Siemens Computers
PRIMERGY RX300 S3, Intel Xeon processor E5310, 1.60 GHz

SPECint_rate2006 = 40.0
SPECint_rate_base2006 = 37.3

CPU2006 license: 22
Test sponsor: Fujitsu Siemens Computers
Test by: Fujitsu Siemens Computers

Test date: Jul-2007
Hardware Availability: Nov-2006
Software Availability: Jun-2007

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
- fast
C++ benchmarks:
  -xT -O3 -ipo -no-prec-div -ansi-alias
  -L/opt/SmartHeap_8_1/lib -lsmartheap

Base Other Flags

C benchmarks:
  403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):
  icc
    401.bzip2: /opt/intel/cce/10.0.023/bin/icc
                -I/opt/intel/cce/10.0.023/include
                -L/opt/intel/cce/10.0.023/lib
    456.hmmer: /opt/intel/cce/10.0.023/bin/icc
                -I/opt/intel/cce/10.0.023/include
                -L/opt/intel/cce/10.0.023/lib

C++ benchmarks:
  icpc

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX

Continued on next page
Peak Portability Flags (Continued)

483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -prof_gen(pass 1) -prof_use(pass 2) -fast

401.bzip2: -fast

403.gcc: basepeak = yes

429.mcf: -prof_gen(pass 1) -prof_use(pass 2) -fast -prefetch
-L/opt/SmartHeap_8_1/lib -lsmartheap

445.gobmk: Same as 400.perlbench

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/FSC_Intel_flags.html
Fujitsu Siemens Computers
PRIMERGY RX300 S3, Intel Xeon processor E5310, 1.60 GHz

SPECint_rate2006 = 40.0
SPECint_rate_base2006 = 37.3

CPU2006 license: 22
Test sponsor: Fujitsu Siemens Computers
Tested by: Fujitsu Siemens Computers
Test date: Jul-2007
Hardware Availability: Nov-2006
Software Availability: Jun-2007

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/FSC_Intel_flags.xml

SPEC and SPECint are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.0.
Originally published on 8 August 2007.