SPEC® CINT2006 Result

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Fujitsu Siemens Computers

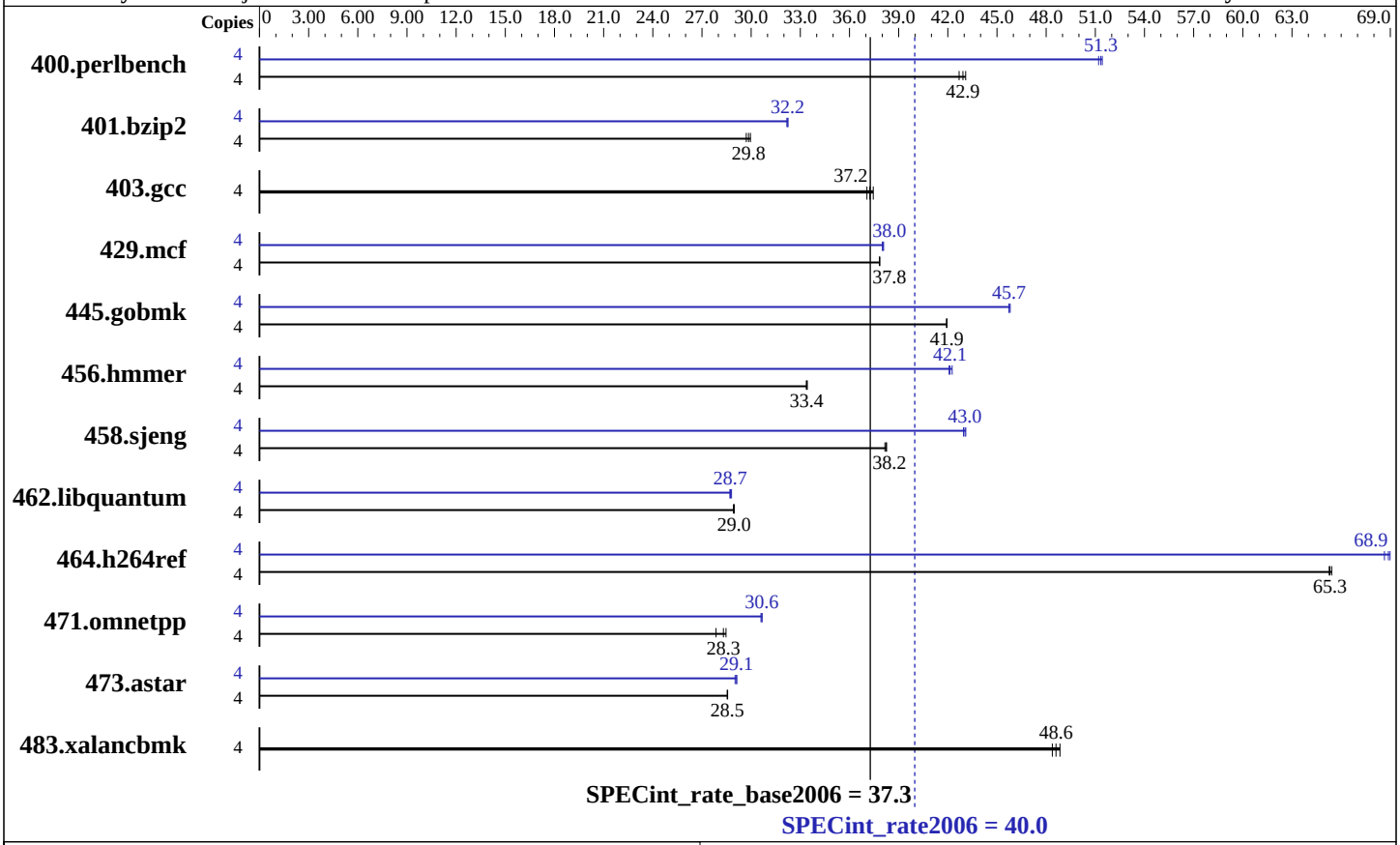
PRIMERGY TX300 S3, Intel Xeon processor E5310, 1.60 GHz

SPECint®_rate2006 = 40.0

SPECint_rate_base2006 = 37.3

CPU2006 license: 22
Test sponsor: Fujitsu Siemens Computers
Tested by: Fujitsu Siemens Computers

Test date: Jul-2007
Hardware Availability: Nov-2006
Software Availability: Jun-2007



Hardware

CPU Name:	Intel Xeon E5310
CPU Characteristics:	1067 MHz system bus
CPU MHz:	1600
FPU:	Integrated
CPU(s) enabled:	4 cores, 1 chip, 4 cores/chip
CPU(s) orderable:	1,2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	8 MB I+D on chip per chip, 4 MB shared / 2 cores
L3 Cache:	None
Other Cache:	None
Memory:	8 GB (8x1 GB DDR2 PC2-5300F, 2 rank, CAS 5-5-5, with ECC)
Disk Subsystem:	Seagate ST3146854SS (SAS, 146GB, 15000rpm)
Other Hardware:	None

Software

Operating System:	SUSE LINUX Enterprise Server 10 (x86_64), Kernel 2.6.16.21-0.8-smp
Compiler:	Intel C++ Compiler for IA32/EM64T application, Version 10.0 - Build 20070308, Package-ID: l_cc_p_10.0.023
Auto Parallel:	No
File System:	ext2
System State:	Multiuser, Runlevel 3
Base Pointers:	32-bit
Peak Pointers:	32/64-bit
Other Software:	Smart Heap Library, Version 8.1 binutils-2.17.tar.gz, Version 2.17

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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	4	907	43.1	**910**	**42.9**	915	42.7	4	**761**	**51.3**	763	51.2	760	51.4
401.bzip2	4	1289	30.0	1300	29.7	**1294**	**29.8**	4	1200	32.2	1197	32.2	**1197**	**32.2**
403.gcc	4	**865**	**37.2**	869	37.1	860	37.5	4	**865**	**37.2**	869	37.1	860	37.5
429.mcf	4	964	37.8	**964**	**37.8**	964	37.8	4	958	38.1	**959**	**38.0**	960	38.0
445.gobmk	4	1000	41.9	1001	41.9	**1001**	**41.9**	4	**917**	**45.7**	916	45.8	917	45.7
456.hmmer	4	1119	33.4	1116	33.4	**1117**	**33.4**	4	883	42.3	887	42.1	**886**	**42.1**
458.sjeng	4	1265	38.3	1268	38.2	**1266**	**38.2**	4	**1124**	**43.0**	1127	43.0	1123	43.1
462.libquantum	4	2866	28.9	**2862**	**29.0**	2862	29.0	4	2878	28.8	**2884**	**28.7**	2888	28.7
464.h264ref	4	1356	65.3	1353	65.4	**1355**	**65.3**	4	**1285**	**68.9**	1290	68.6	1283	69.0
471.omnetpp	4	878	28.5	**883**	**28.3**	897	27.9	4	815	30.7	**816**	**30.6**	817	30.6
473.astar	4	**984**	**28.5**	983	28.6	984	28.5	4	**965**	**29.1**	968	29.0	964	29.1
483.xalancbmk	4	565	48.8	**568**	**48.6**	571	48.4	4	565	48.8	**568**	**48.6**	571	48.4

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

```
All binaries were built with 32-bit Intel compiler except:
401.bzip2 and 456.hmmer in peak were built with 64-bit Intel
compiler by changing the path for include and library files.

BIOS configuration:
Adjacent Sector Prefetch = Disable

This result was measured on the PRIMERGY RX300 S3. The PRIMERGY RX300 S3 and
the PRIMERGY TX300 S3 are electronically equivalent.

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http://www.fujitsu-siemens.com
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Base Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
-fast

C++ benchmarks:
-xT -O3 -ipo -no-prec-div -ansi-alias
-L/opt/SmartHeap_8_1/lib -lsmartheap

Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):
icc

401.bzip2: /opt/intel/cce/10.0.023/bin/icc
-I/opt/intel/cce/10.0.023/include
-L/opt/intel/cce/10.0.023/lib

456.hmmer: /opt/intel/cce/10.0.023/bin/icc
-I/opt/intel/cce/10.0.023/include
-L/opt/intel/cce/10.0.023/lib

C++ benchmarks:
icpc

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX

Continued on next page

Peak Portability Flags (Continued)

483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -prof_gen(pass 1) -prof_use(pass 2) -fast

401.bzip2: -fast

403.gcc: basepeak = yes

429.mcf: -prof_gen(pass 1) -prof_use(pass 2) -fast -prefetch -L/opt/SmartHeap_8_1/lib -lsmartheap

445.gobmk: Same as 400.perlbench

456.hmmer: -prof_gen(pass 1) -prof_use(pass 2) -fast -unroll2

458.sjeng: -prof_gen(pass 1) -prof_use(pass 2) -fast -unroll4

462.libquantum: -prof_gen(pass 1) -prof_use(pass 2) -fast -prefetch -opt-streaming-stores always

464.h264ref: -prof_gen(pass 1) -prof_use(pass 2) -fast -unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -prof_gen(pass 1) -prof_use(pass 2) -fast -ansi-alias -L/opt/SmartHeap_8_1/lib -lsmartheap

473.astar: Same as 471.omnetpp

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/FSC_Intel_flags.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/FSC_Intel_flags.xml

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For other inquiries, please contact webmaster@spec.org.

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