SPEC® CINT2006 Result

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Hewlett-Packard Company

(Test Sponsor: Intel Corporation)

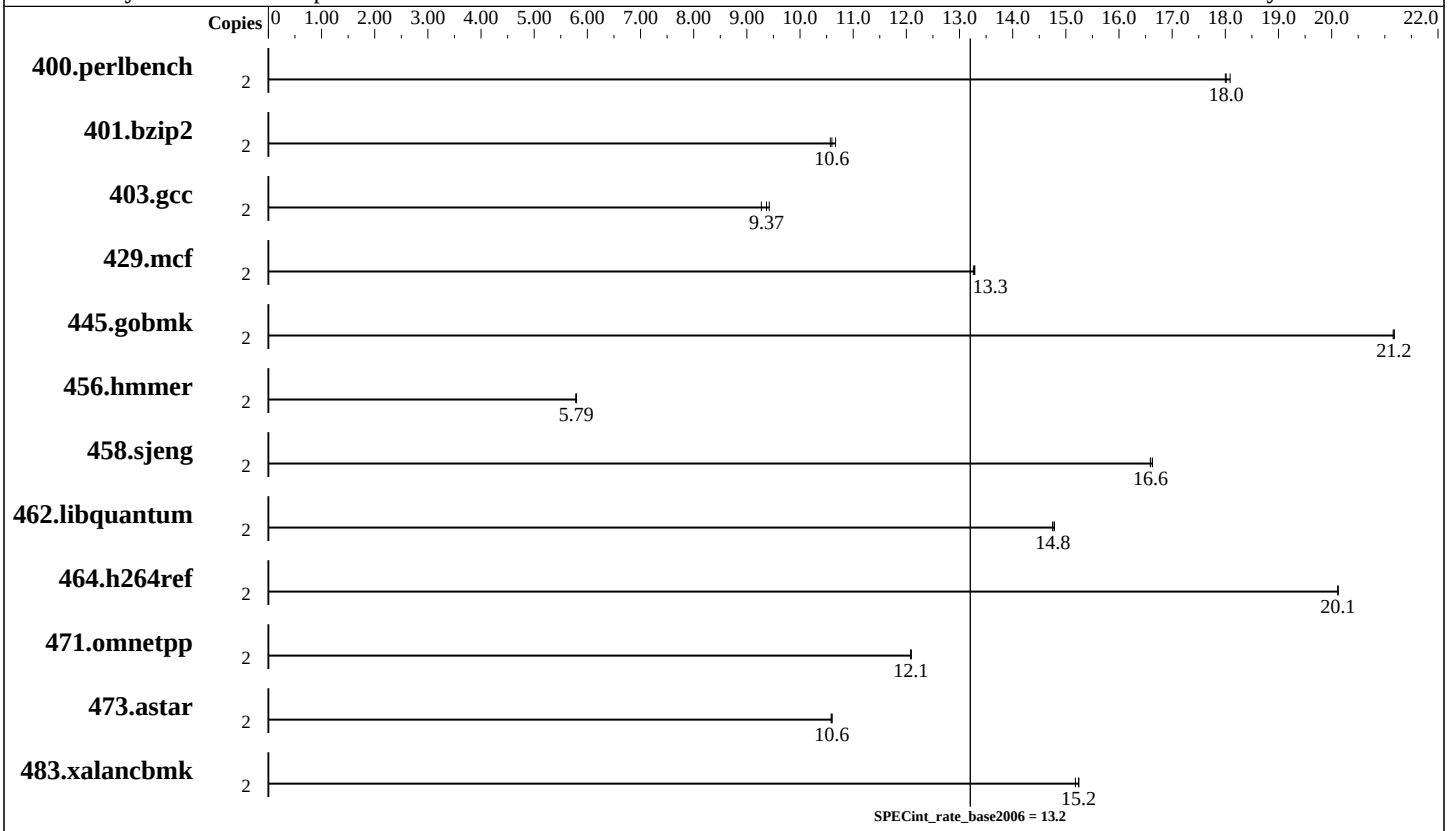
Compaq 6515B (AMD Turion 64 X2 TL-56)

SPECint®_rate2006 = Not Run

SPECint_rate_base2006 = 13.2

CPU2006 license: 13
Test sponsor: Intel Corporation
Tested by: Intel Corporation

Test date: Jun-2007
Hardware Availability: Jun-2007
Software Availability: Feb-2007



Hardware

CPU Name:	AMD Turion 64 X2 TL-56
CPU Characteristics:	1.8 GHz 2x512KB L2
CPU MHz:	1800
FPU:	Integrated
CPU(s) enabled:	2 cores, 1 chip, 2 cores/chip
CPU(s) orderable:	1 chip
Primary Cache:	64 KB I + 64 KB D on chip per core
Secondary Cache:	512 KB I+D on chip per core
L3 Cache:	None
Other Cache:	None
Memory:	2 GB (2x1GB Qimonda HYS64T128021HDL-3S-B DDR2-667 CL5)
Disk Subsystem:	Hitachi 100GB 5400rpm SATA HDD
Other Hardware:	None

Software

Operating System:	Windows Vista32 Business
Compiler:	Intel C++ Compiler for IA32 version 9.1 Build no 20060816 Microsoft Visual Studio .Net 2003 (for libraries)
Auto Parallel:	No
File System:	NTFS
System State:	Default
Base Pointers:	32-bit
Peak Pointers:	Not Applicable
Other Software:	SmartHeap Library Version 8.0 from http://www.microquill.com/

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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	2	1080	18.1	1086	18.0	**1085**	**18.0**							
401.bzip2	2	**1821**	**10.6**	1809	10.7	1826	10.6							
403.gcc	2	1709	9.42	**1718**	**9.37**	1736	9.27							
429.mcf	2	1375	13.3	**1373**	**13.3**	1373	13.3							
445.gobmk	2	991	21.2	992	21.2	**991**	**21.2**							
456.hmmer	2	3227	5.78	**3222**	**5.79**	3222	5.79							
458.sjeng	2	1455	16.6	**1458**	**16.6**	1459	16.6							
462.libquantum	2	2803	14.8	2809	14.8	**2808**	**14.8**							
464.h264ref	2	**2200**	**20.1**	2200	20.1	2200	20.1							
471.omnetpp	2	1034	12.1	**1034**	**12.1**	1035	12.1							
473.astar	2	1324	10.6	**1324**	**10.6**	1326	10.6							
483.xalancbmk	2	909	15.2	**906**	**15.2**	905	15.2							

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

```
Binaries built on Windows XP Professional w/ SP2
Vista Power Profile set to High Performance
```

Base Compiler Invocation

C benchmarks:

```
icl -Qvc7.1 -Qc99
```

C++ benchmarks:

```
icl -Qvc7.1
```

Base Portability Flags

403.gcc: `-DSPEC_CPU_WIN32`
464.h264ref: `-DSPEC_CPU_NO_INTTYPES -DWIN32`

Base Optimization Flags

C benchmarks:

```
-arch:SSE2 -Qprec-div- -O3 -Qipo /F512000000 shlw32m.lib
           -link /FORCE:MULTIPLE
```

Continued on next page

Base Optimization Flags (Continued)

C++ benchmarks:
```
-arch:SSE2 -Qprec-div- -O3 -Qipo -Oi- -Qcxx_features /F512000000
shlw32m.lib            -link /FORCE:MULTIPLE
```

Base Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

The flags file that was used to format this result can be browsed at
`http://www.spec.org/cpu2006/flags/Intel-ic91-ia32-flags.20090714.html`

You can also download the XML flags source by saving the following link:
`http://www.spec.org/cpu2006/flags/Intel-ic91-ia32-flags.20090714.xml`

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For questions about this result, please contact the tester.
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