SPEC® CINT2006 Result

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ASUS Computer International

(Test Sponsor: Intel Corporation)

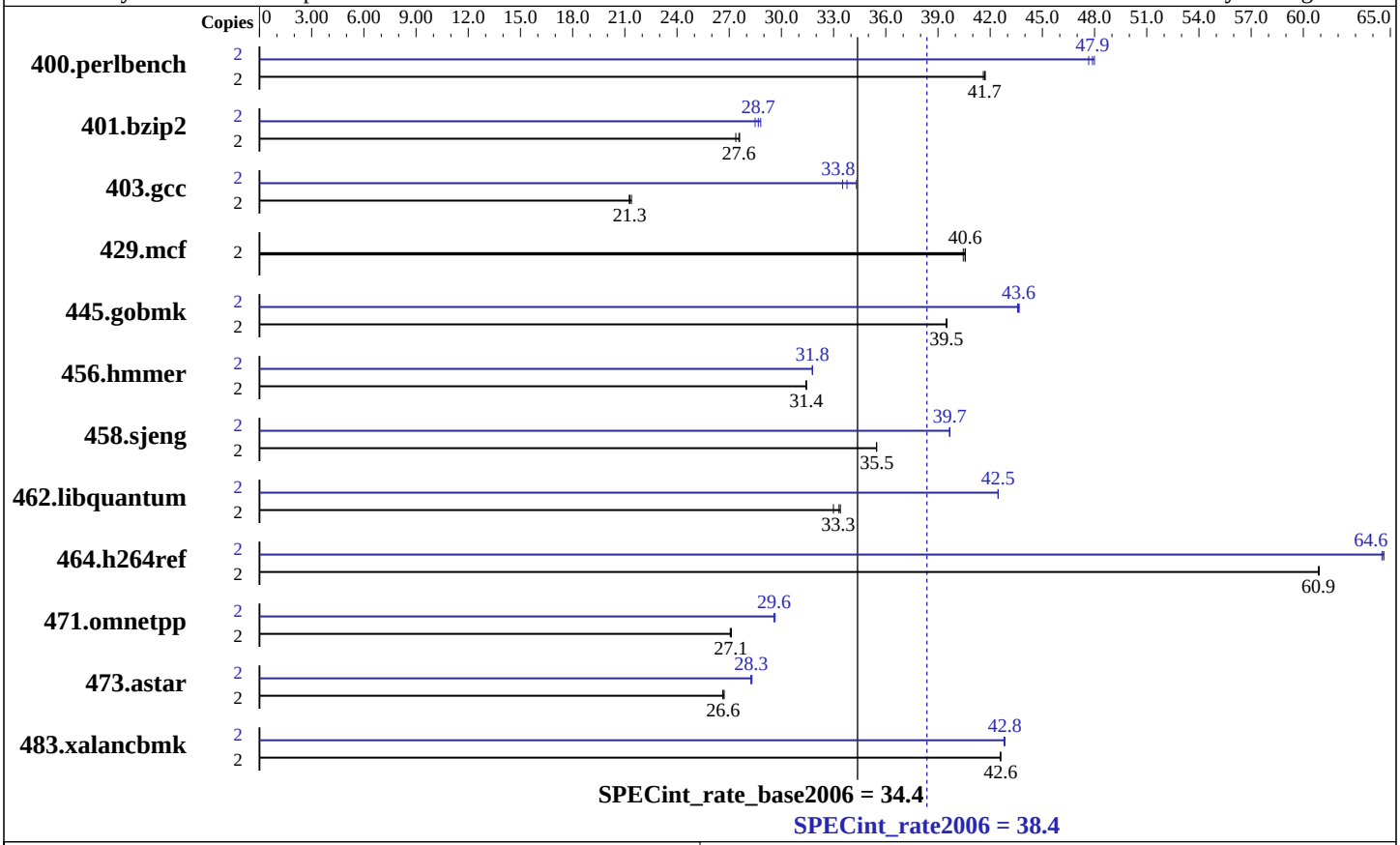
ASUS P5K3 motherboard (Intel Core 2 Duo E6850)

SPECint®_rate2006 = 38.4

SPECint_rate_base2006 = 34.4

CPU2006 license: 13
Test sponsor: Intel Corporation
Tested by: Intel Corporation

Test date: Jul-2007
Hardware Availability: Jul-2007
Software Availability: Aug-2006



Hardware

CPU Name:	Intel Core 2 Duo E6850
CPU Characteristics:	3.00 GHz, 1333 MHz bus
CPU MHz:	3000
FPU:	Integrated
CPU(s) enabled:	2 cores, 1 chip, 2 cores/chip
CPU(s) orderable:	1 chip
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	4 MB I+D on chip per chip
L3 Cache:	None
Other Cache:	None
Memory:	2 GB (2x1GB ELPIDA PC3-8500U-7-00-BP DDR3-1066 7-7-7-20)
Disk Subsystem:	Seagate ST3320620AS 320GB Barracuda 7200.10 NCQ SATA II
Other Hardware:	None

Software

Operating System:	Windows Vista32 Ultimate
Compiler:	Intel C++ Compiler for IA32 version 10.0 Build 20070426 Package ID: W_CC_P_10.0.025 Microsoft Visual Studio .Net 2003 (for libraries)
Auto Parallel:	No
File System:	NTFS
System State:	Default
Base Pointers:	32-bit
Peak Pointers:	32-bit
Other Software:	SmartHeap Library Version 8.0 from http://www.microquill.com/

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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	2	468	41.7	470	41.6	**469**	**41.7**	2	407	48.0	**408**	**47.9**	410	47.7
401.bzip2	2	705	27.4	699	27.6	**700**	**27.6**	2	**673**	**28.7**	677	28.5	670	28.8
403.gcc	2	757	21.3	**756**	**21.3**	753	21.4	2	469	34.3	**477**	**33.8**	480	33.5
429.mcf	2	451	40.5	**450**	**40.6**	450	40.6	2	451	40.5	**450**	**40.6**	450	40.6
445.gobmk	2	531	39.5	**531**	**39.5**	531	39.5	2	**481**	**43.6**	481	43.6	480	43.7
456.hmmer	2	594	31.4	594	31.4	**594**	**31.4**	2	**587**	**31.8**	587	31.8	587	31.8
458.sjeng	2	682	35.5	**682**	**35.5**	682	35.5	2	610	39.7	610	39.7	**610**	**39.7**
462.libquantum	2	**1244**	**33.3**	1256	33.0	1241	33.4	2	976	42.4	976	42.5	**976**	**42.5**
464.h264ref	2	727	60.9	**727**	**60.9**	727	60.9	2	**686**	**64.6**	686	64.5	685	64.6
471.omnetpp	2	462	27.1	461	27.1	**461**	**27.1**	2	**423**	**29.6**	422	29.6	423	29.6
473.astar	2	526	26.7	**527**	**26.6**	527	26.6	2	496	28.3	**497**	**28.3**	497	28.3
483.xalancbmk	2	**324**	**42.6**	324	42.6	324	42.6	2	322	42.8	**322**	**42.8**	322	42.8

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

```
Tested systems can be used with Shin-G ATX case, Antec NeoPower 480W power supply
Product description located as of 7/2007:
http://usa.asus.com/products.aspx?l1=3&l2=11&l3=534&l4=0&model=1645&modelmenu=1
The system bus runs at 1333 MHz
System has a discrete gfx card - Asus EN8800GTX/HTDP/768M w/ nVidia 8800GTX
Binaries were built on Windows XP Professional SP2 with 4GB of RAM and /3GB boot switch
The start command with the /affinity switch was used to bind processes to cores
```

Base Compiler Invocation

C benchmarks:

```
icl -Qvc7.1 -Qc99
```

C++ benchmarks:

```
icl -Qvc7.1
```

Base Portability Flags

403.gcc: `-DSPEC_CPU_WIN32`
464.h264ref: `-DSPEC_CPU_NO_INTTYPES -DWIN32`

Base Optimization Flags

C benchmarks:
```
-fast /F512000000 shlw32m.lib            -link /FORCE:MULTIPLE
```

C++ benchmarks:
```
-fast -Qcxx_features /F512000000 shlw32m.lib
            -link /FORCE:MULTIPLE
```

Base Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

Peak Compiler Invocation

C benchmarks:
```
icl -Qvc7.1 -Qc99
```

C++ benchmarks:
```
icl -Qvc7.1
```

Peak Portability Flags

403.gcc: `-DSPEC_CPU_WIN32`
464.h264ref: `-DSPEC_CPU_NO_INTTYPES -DWIN32`

Peak Optimization Flags

C benchmarks:

400.perlbench: `-Qprof_gen`(pass 1) `-Qprof_use`(pass 2) `-fast -Qansi-alias -Qprefetch /F512000000 shlw32m.lib -link /FORCE:MULTIPLE`

401.bzip2: `-Qprof_gen`(pass 1) `-Qprof_use`(pass 2) `-fast /F512000000 shlw32m.lib -link /FORCE:MULTIPLE`

403.gcc: `-Qprof_gen`(pass 1) `-Qprof_use`(pass 2) `-fast /F512000000 -link /FORCE:MULTIPLE`

429.mcf: `basepeak = yes`

Continued on next page

Peak Optimization Flags (Continued)

445.gobmk: -Qprof_gen(pass 1) -Qprof_use(pass 2) -QxT -O2 -Qipo -Qprec_div- -Qansi-alias /F512000000 -link /FORCE:MULTIPLE

456.hmmer: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qunroll2 -Qansi-alias /F512000000 shlw32m.lib -link /FORCE:MULTIPLE

458.sjeng: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qunroll4 /F512000000 shlw32m.lib -link /FORCE:MULTIPLE

462.libquantum: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qunroll4 -Ob0 -Qprefetch -Qopt-streaming-stores:always /F512000000 shlw32m.lib -link /FORCE:MULTIPLE

464.h264ref: Same as 456.hmmer

C++ benchmarks:
-Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qansi-alias -Qcxx_features /F512000000 shlw32m.lib -link /FORCE:MULTIPLE

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090714.42.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090714.42.xml

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