**Intel DG965WH motherboard (Intel Core 2 Duo E6700)**

**SPECint\_rate2006 = 33.5**  
**SPECint\_rate\_base2006 = 30.0**

<table>
<thead>
<tr>
<th>Test sponsor</th>
<th>Intel Corporation</th>
<th>Hardware Availability: Aug-2006</th>
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<tr>
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<td>Intel Corporation</td>
<td>Software Availability:</td>
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<td></td>
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</tbody>
</table>

**CPU2006 license:** 13

**Test date:** Jun-2007

**CPU Name:** Intel Core 2 Duo E6700

**CPU Characteristics:** 2.67 GHz, 1066 MHz bus

**CPU MHZ:** 2667

**FPU:** Integrated

**CPU(s) enabled:** 2 cores, 1 chip, 2 cores/chip

**CPU(s) orderable:** 1 chip

**Primary Cache:** 32 KB I + 32 KB D on chip per core

**Secondary Cache:** 4 MB I+D on chip per chip

**L3 Cache:** None

**Other Cache:** None

**Memory:** 2 GB (2 1GB Micron MT16HTF12864AY-80ED4 DDR2 800, CL5)

**Disk Subsystem:** Seagate ST3320620AS 320GB Barracuda 7200.10 NCQ SATA II

**Other Hardware:** None

**Operating System:** Windows Vista32 Ultimate

**Compiler:** Intel C++ Compiler for IA32 version 10.0  
Build 20070426 Package ID: W_CC_P_10.0.025  
Microsoft Visual Studio .Net 2003 (for libraries)

**Auto Parallel:** No

**File System:** NTFS

**System State:** Default

**Base Pointers:** 32-bit

**Peak Pointers:** 32-bit

**Other Software:** SmartHeap Library Version 8.0 from  
http://www.microquill.com/
# SPEC CINT2006 Result

## Intel Corporation

Intel DG965WH motherboard (Intel Core 2 Duo E6700)

| SPECint_rate2006 | 33.5 |
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**CPU2006 license:** 13  
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**Hardware Availability:** Aug-2006  
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## Results Table

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<thead>
<tr>
<th>Benchmark</th>
<th>Copies</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Base</th>
<th>Peak</th>
</tr>
</thead>
<tbody>
<tr>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>Base</td>
<td>Peak</td>
</tr>
<tr>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>400.perlbench</td>
<td>2</td>
<td>522</td>
<td>37.5</td>
<td>525</td>
<td>37.2</td>
<td></td>
<td></td>
<td>2</td>
<td>461</td>
</tr>
<tr>
<td>401.bzip2</td>
<td>2</td>
<td>777</td>
<td>24.8</td>
<td>777</td>
<td>24.8</td>
<td></td>
<td></td>
<td>2</td>
<td>749</td>
</tr>
<tr>
<td>403.gcc</td>
<td>2</td>
<td>911</td>
<td>17.7</td>
<td>922</td>
<td>17.5</td>
<td></td>
<td></td>
<td>2</td>
<td>541</td>
</tr>
<tr>
<td>429.mcf</td>
<td>2</td>
<td>524</td>
<td>34.8</td>
<td>524</td>
<td>34.8</td>
<td></td>
<td></td>
<td>2</td>
<td>525</td>
</tr>
<tr>
<td>445.gobmk</td>
<td>2</td>
<td>603</td>
<td>34.8</td>
<td>603</td>
<td>34.8</td>
<td></td>
<td></td>
<td>2</td>
<td>548</td>
</tr>
<tr>
<td>456.hmmer</td>
<td>2</td>
<td>672</td>
<td>27.8</td>
<td>672</td>
<td>27.8</td>
<td></td>
<td></td>
<td>2</td>
<td>666</td>
</tr>
<tr>
<td>458.sjeng</td>
<td>2</td>
<td>768</td>
<td>31.5</td>
<td>768</td>
<td>31.5</td>
<td></td>
<td></td>
<td>2</td>
<td>687</td>
</tr>
<tr>
<td>462.libquantum</td>
<td>2</td>
<td>1472</td>
<td>28.2</td>
<td>1472</td>
<td>28.2</td>
<td></td>
<td></td>
<td>2</td>
<td>1232</td>
</tr>
<tr>
<td>464.h264ref</td>
<td>2</td>
<td>824</td>
<td>53.7</td>
<td>824</td>
<td>53.7</td>
<td></td>
<td></td>
<td>2</td>
<td>775</td>
</tr>
<tr>
<td>471.omnetpp</td>
<td>2</td>
<td>530</td>
<td>23.6</td>
<td>530</td>
<td>23.6</td>
<td></td>
<td></td>
<td>2</td>
<td>489</td>
</tr>
<tr>
<td>473.astar</td>
<td>2</td>
<td>599</td>
<td>23.4</td>
<td>599</td>
<td>23.4</td>
<td></td>
<td></td>
<td>2</td>
<td>566</td>
</tr>
<tr>
<td>483.xalancbmk</td>
<td>2</td>
<td>369</td>
<td>37.4</td>
<td>369</td>
<td>37.4</td>
<td></td>
<td></td>
<td>2</td>
<td>367</td>
</tr>
</tbody>
</table>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## General Notes

Tested systems can be used with Shin-G ATX case, Antec NeoPower 480W power supply

Product description located as of 7/2007:

The system bus runs at 1066 MHz

System has a discrete gfx card - Asus EN8800GTX/HTDP/768M w/ nVidia 8800GTX

Binaries were built on Windows XP Professional SP2 with 4GB of RAM and /3GB boot switch

The start command with the /affinity switch was used to bind processes to cores

## Base Compiler Invocation

C benchmarks:
icl -Qvc7.1 -Qc99

C++ benchmarks:
icl -Qvc7.1

## Base Portability Flags

403.gcc: -DSPEC_CPU_WIN32  
464.h264ref: -DSPEC_CPU_NO_INTTYPES -DWIN32
## SPEC CINT2006 Result

**Intel Corporation**

Intel DG965WH motherboard (Intel Core 2 Duo E6700)

### SPECint Rate

| SPECint rate 2006 | 33.5 |
| SPECint rate base 2006 | 30.0 |

- **CPU2006 license:** 13
- **Test sponsor:** Intel Corporation
- **Test date:** Jun-2007
- **Tested by:** Intel Corporation

### Base Optimization Flags

**C benchmarks**:

- `fast /F512000000 shlw32m.lib`
- `-link /FORCE:MULTIPLE`

**C++ benchmarks**:

- `fast -Qcxx_features /F512000000 shlw32m.lib`
- `-link /FORCE:MULTIPLE`

### Base Other Flags

**C benchmarks**:

- `403.gcc: -Dalloca=_alloca`

### Peak Compiler Invocation

**C benchmarks**:

- `icl -Qvc7.1 -Qc99`

**C++ benchmarks**:

- `icl -Qvc7.1`

### Peak Portability Flags

- `403.gcc: -DSPEC_CPU_WIN32`
- `464.h264ref: -DSPEC_CPU_NO_INTTYPES -DWIN32`

### Peak Optimization Flags

**C benchmarks**:

- `400.perlbench: -Qprof_gen(pass 1) -Qprof_use(pass 2) fast -Qansi-alias`
  - `Qprefetch /F512000000 shlw32m.lib`
  - `-link /FORCE:MULTIPLE`

- `401.bzip2: -Qprof_gen(pass 1) -Qprof_use(pass 2) fast /F5120000000 shlw32m.lib`
  - `-link /FORCE:MULTIPLE`

- `403.gcc: -Qprof_gen(pass 1) -Qprof_use(pass 2) fast /F5120000000 -link /FORCE:MULTIPLE`

- `429.mcf: basepeak = yes`

Continued on next page.
Intel Corporation

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Peak Optimization Flags (Continued)

445.gobmk: -Qprof_gen(pass 1) -Qprof_use(pass 2) -QxT -O2 -Qipo
-prec_div -Qansi-alias /F512000000
    -link /FORCE:MULTIPLE

456.hmmer: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qunroll12
  -Qansi-alias /F512000000 shlw32m.lib
    -link /FORCE:MULTIPLE

458.sjeng: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qunroll14
  /F512000000 shlw32m.lib
    -link /FORCE:MULTIPLE

462.libquantum: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qunroll14
  -Ob0 -Qprefetch -Qopt-streaming-stores:always /F512000000
  shlw32m.lib
    -link /FORCE:MULTIPLE

464.h264ref: Same as 456.hmmer

C++ benchmarks:
- Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qansi-alias
- Qcxx_features /F512000000 shlw32m.lib
    -link /FORCE:MULTIPLE

Peak Other Flags

C benchmarks:
403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090714.42.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090714.42.xml

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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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