SPEC® CINT2006 Result

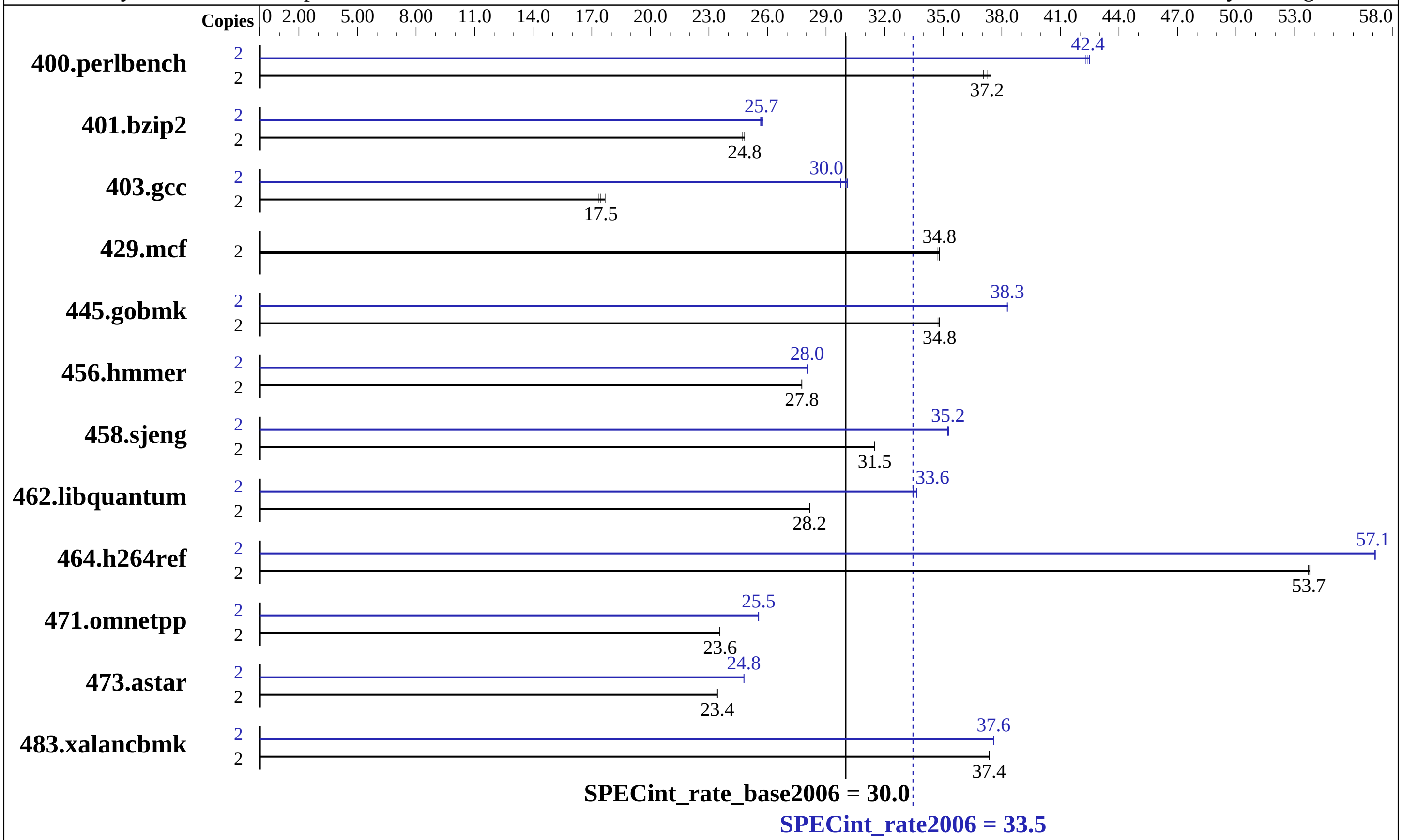
Intel Corporation

Intel DG965WH motherboard (Intel Core 2 Duo E6700)

SPECint®_rate2006 = 33.5

SPECint_rate_base2006 = 30.0

CPU2006 license:	13	**Test date:**	Jun-2007
Test sponsor:	Intel Corporation	**Hardware Availability:**	Aug-2006
Tested by:	Intel Corporation	**Software Availability:**	Aug-2006



Hardware

CPU Name:	Intel Core 2 Duo E6700
CPU Characteristics:	2.67 GHz, 1066 MHz bus
CPU MHz:	2667
FPU:	Integrated
CPU(s) enabled:	2 cores, 1 chip, 2 cores/chip
CPU(s) orderable:	1 chip
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	4 MB I+D on chip per chip
L3 Cache:	None
Other Cache:	None
Memory:	2 GB (2 1GB Micron MT16HTF12864AY-80ED4 DDR2 800, CL5)
Disk Subsystem:	Seagate ST3320620AS 320GB Barracuda 7200.10 NCQ SATA II
Other Hardware:	None

Software

Operating System:	Windows Vista32 Ultimate
Compiler:	Intel C++ Compiler for IA32 version 10.0 Build 20070426 Package ID: W_CC_P_10.0.025 Microsoft Visual Studio .Net 2003 (for libraries)
Auto Parallel:	No
File System:	NTFS
System State:	Default
Base Pointers:	32-bit
Peak Pointers:	32-bit
Other Software:	SmartHeap Library Version 8.0 from http://www.microquill.com/

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	2	527	37.1	522	37.5	**525**	**37.2**	2	**461**	**42.4**	460	42.5	462	42.3
401.bzip2	2	780	24.7	**777**	**24.8**	777	24.8	2	749	25.8	753	25.6	**751**	**25.7**
403.gcc	2	927	17.4	911	17.7	**922**	**17.5**	2	541	29.7	**537**	**30.0**	535	30.1
429.mcf	2	525	34.7	524	34.8	**524**	**34.8**	2	525	34.7	524	34.8	**524**	**34.8**
445.gobmk	2	604	34.7	**603**	**34.8**	603	34.8	2	548	38.3	548	38.3	**548**	**38.3**
456.hmmer	2	672	27.8	672	27.8	**672**	**27.8**	2	666	28.0	665	28.1	**665**	**28.0**
458.sjeng	2	768	31.5	768	31.5	**768**	**31.5**	2	687	35.2	686	35.3	**687**	**35.2**
462.libquantum	2	1472	28.2	**1472**	**28.2**	1473	28.1	2	1232	33.6	**1232**	**33.6**	1238	33.5
464.h264ref	2	823	53.8	824	53.7	**824**	**53.7**	2	**775**	**57.1**	775	57.1	775	57.1
471.omnetpp	2	530	23.6	**530**	**23.6**	531	23.6	2	**489**	**25.5**	489	25.6	490	25.5
473.astar	2	599	23.4	**599**	**23.4**	599	23.4	2	566	24.8	**566**	**24.8**	566	24.8
483.xalancbmk	2	369	37.4	**369**	**37.4**	370	37.3	2	**367**	**37.6**	367	37.6	367	37.6

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

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Tested systems can be used with Shin-G ATX case, Antec NeoPower 480W power supply
Product description located as of 7/2007:
http://www.intel.com/products/motherboard/DG965WH/index.htm
The system bus runs at 1066 MHz
System has a discrete gfx card - Asus EN8800GTX/HTDP/768M w/ nVidia 8800GTX
Binaries were built on Windows XP Professional SP2 with 4GB of RAM and /3GB boot switch
The start command with the /affinity switch was used to bind processes to cores
```

Base Compiler Invocation

C benchmarks:
`icl -Qvc7.1 -Qc99`

C++ benchmarks:
`icl -Qvc7.1`

Base Portability Flags

403.gcc: `-DSPEC_CPU_WIN32`
464.h264ref: `-DSPEC_CPU_NO_INTTYPES -DWIN32`

Base Optimization Flags

C benchmarks:
`-fast /F512000000 shlw32m.lib -link /FORCE:MULTIPLE`

C++ benchmarks:
`-fast -Qcxx_features /F512000000 shlw32m.lib -link /FORCE:MULTIPLE`

Base Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

Peak Compiler Invocation

C benchmarks:
`icl -Qvc7.1 -Qc99`

C++ benchmarks:
`icl -Qvc7.1`

Peak Portability Flags

403.gcc: `-DSPEC_CPU_WIN32`
464.h264ref: `-DSPEC_CPU_NO_INTTYPES -DWIN32`

Peak Optimization Flags

C benchmarks:

400.perlbench: `-Qprof_gen`(pass 1) `-Qprof_use`(pass 2) `-fast -Qansi-alias -Qprefetch /F512000000 shlw32m.lib -link /FORCE:MULTIPLE`

401.bzip2: `-Qprof_gen`(pass 1) `-Qprof_use`(pass 2) `-fast /F512000000 shlw32m.lib -link /FORCE:MULTIPLE`

403.gcc: `-Qprof_gen`(pass 1) `-Qprof_use`(pass 2) `-fast /F512000000 -link /FORCE:MULTIPLE`

429.mcf: `basepeak = yes`

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Peak Optimization Flags (Continued)

445.gobmk: -Qprof_gen(pass 1) -Qprof_use(pass 2) -QxT -O2 -Qipo -Qprec_div- -Qansi-alias /F512000000 -link /FORCE:MULTIPLE

456.hmmer: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qunroll2 -Qansi-alias /F512000000 shlw32m.lib -link /FORCE:MULTIPLE

458.sjeng: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qunroll4 /F512000000 shlw32m.lib -link /FORCE:MULTIPLE

462.libquantum: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qunroll4 -Ob0 -Qprefetch -Qopt-streaming-stores:always /F512000000 shlw32m.lib -link /FORCE:MULTIPLE

464.h264ref: Same as 456.hmmer

C++ benchmarks:

-Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qansi-alias -Qcxx_features /F512000000 shlw32m.lib -link /FORCE:MULTIPLE

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090714.42.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090714.42.xml

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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