SPEC® CFP2006 Result

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Intel Corporation

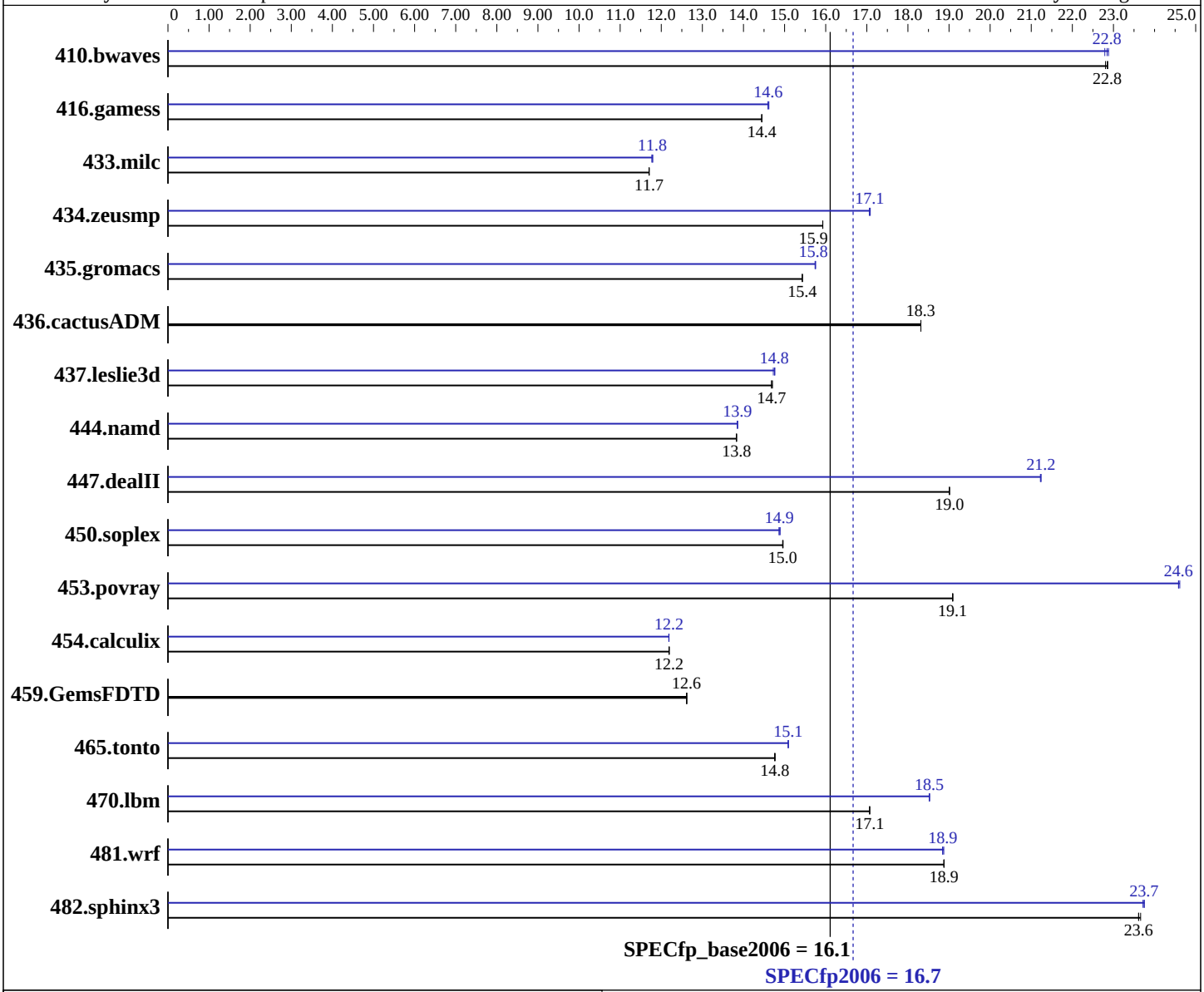
Intel DQ965GF motherboard (Intel Core 2 Duo E6700)

SPECfp®2006 = 16.7

SPECfp_base2006 = 16.1

CPU2006 license: 13
Test sponsor: Intel Corporation
Tested by: Intel Corporation

Test date: Jun-2007
Hardware Availability: Aug-2006
Software Availability: Aug-2006



Hardware

CPU Name:	Intel Core 2 Duo E6700
CPU Characteristics:	2.67 GHz, 1066 MHz bus
CPU MHz:	2667
FPU:	Integrated
CPU(s) enabled:	2 cores, 1 chip, 2 cores/chip
CPU(s) orderable:	1 chip
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	4 MB I+D on chip per chip

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Software

Operating System:	Windows Vista32 Ultimate
Compiler:	Intel C++ Compiler for IA32 version 10.0 Build 20070426 Package ID: W_CC_P_10.0.025 Intel Fortran Compiler for IA32 version 10.0 Build 20070426 Package ID: W_FC_P_10.0.025 Microsoft Visual Studio .Net 2003 (for libraries)
Auto Parallel:	No
File System:	NTFS
System State:	Default

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L3 Cache: None
Other Cache: None
Memory: 2 GB (2 1GB Micron MT16HTF12864AY-80ED4 DDR2 800, CL5)
Disk Subsystem: Seagate ST3320620AS 320GB Barracuda 7200.10 NCQ SATA II
Other Hardware: None

Base Pointers: 32-bit
Peak Pointers: 32-bit
Other Software: SmartHeap Library Version 8.0 from http://www.microquill.com/

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	596	22.8	**595**	**22.8**	594	22.9	596	22.8	594	22.9	**595**	**22.8**
416.gamess	1355	14.4	**1355**	**14.4**	1356	14.4	1339	14.6	**1341**	**14.6**	1341	14.6
433.milc	784	11.7	**784**	**11.7**	785	11.7	780	11.8	778	11.8	**779**	**11.8**
434.zeusmp	571	15.9	**571**	**15.9**	571	15.9	533	17.1	**533**	**17.1**	533	17.1
435.gromacs	463	15.4	463	15.4	**463**	**15.4**	453	15.8	**453**	**15.8**	454	15.7
436.cactusADM	652	18.3	**652**	**18.3**	652	18.3	652	18.3	**652**	**18.3**	652	18.3
437.leslie3d	639	14.7	**640**	**14.7**	640	14.7	637	14.8	638	14.7	**637**	**14.8**
444.namd	580	13.8	**580**	**13.8**	580	13.8	579	13.9	**579**	**13.9**	579	13.8
447.dealII	602	19.0	602	19.0	**602**	**19.0**	**539**	**21.2**	539	21.2	539	21.2
450.soplex	557	15.0	**558**	**15.0**	558	15.0	561	14.9	560	14.9	**561**	**14.9**
453.povray	279	19.1	**279**	**19.1**	279	19.1	**216**	**24.6**	216	24.6	216	24.6
454.calculix	677	12.2	677	12.2	**677**	**12.2**	677	12.2	**677**	**12.2**	677	12.2
459.GemsFDTD	841	12.6	**841**	**12.6**	840	12.6	841	12.6	**841**	**12.6**	840	12.6
465.tonto	667	14.8	**667**	**14.8**	666	14.8	**652**	**15.1**	652	15.1	652	15.1
470.lbm	805	17.1	**805**	**17.1**	805	17.1	741	18.5	**742**	**18.5**	742	18.5
481.wrf	**592**	**18.9**	592	18.9	592	18.9	592	18.9	593	18.8	**593**	**18.9**
482.sphinx3	825	23.6	**825**	**23.6**	824	23.7	821	23.8	822	23.7	**821**	**23.7**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

```
Tested systems can be used with Shin-G ATX case, Antec NeoPower 480W power supply
Product description located as of 7/2007:
http://www.intel.com/products/motherboard/DQ965GF/index.htm
The system bus runs at 1066 MHz
System was configured with integrated graphics card
Binaries were built on Windows XP Professional SP2 with 4GB of RAM and /3GB boot switch
```

Base Compiler Invocation

C benchmarks:

```
icl -Qvc7.1 -Qc99
```

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Base Compiler Invocation (Continued)

C++ benchmarks:
icl -Qvc7.1

Fortran benchmarks:
ifort

Benchmarks using both Fortran and C:
icl -Qvc7.1 -Qc99 ifort

Base Portability Flags

436.cactusADM: -Qlowercase /assume:underscore
444.namd: -TP
447.dealII: -DDEAL_II_MEMBER_VAR_SPECIALIZATION_BUG -DBOOST_NO_INTRINSIC_WCHAR_T
453.povray: -DSPEC_CPU_WINDOWS_ICL
454.calculix: -DSPEC_CPU_NOZMODIFIER -Qlowercase
481.wrf: -DSPEC_CPU_WINDOWS_ICL

Base Optimization Flags

C benchmarks:
-fast /F950000000 shlw32m.lib -link /FORCE:MULTIPLE

C++ benchmarks:
-fast -Qcxx_features /F950000000 shlw32m.lib -link /FORCE:MULTIPLE

Fortran benchmarks:
-fast /F950000000

Benchmarks using both Fortran and C:
-fast /F950000000

Peak Compiler Invocation

C benchmarks:
icl -Qvc7.1 -Qc99

C++ benchmarks:
icl -Qvc7.1

Fortran benchmarks:
ifort

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Peak Compiler Invocation (Continued)

Benchmarks using both Fortran and C:
icl -Qvc7.1 -Qc99 ifort

Peak Portability Flags

436.cactusADM: -Qlowercase /assume:underscore
444.namd: -TP
447.dealII: -DDEAL_II_MEMBER_VAR_SPECIALIZATION_BUG
-DBOOST_NO_INTRINSIC_WCHAR_T
453.povray: -DSPEC_CPU_WINDOWS_ICL
454.calculix: -DSPEC_CPU_NOZMODIFIER -Qlowercase
481.wrf: -DSPEC_CPU_WINDOWS_ICL

Peak Optimization Flags

C benchmarks:

433.milc: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qunroll2 -Oa
/F950000000 shlw32m.lib -link /FORCE:MULTIPLE

470.lbm: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qunroll2
-Qscalar-rep- -Qprefetch /F950000000 shlw32m.lib
-link /FORCE:MULTIPLE

482.sphinx3: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qunroll2
/F950000000 shlw32m.lib -link /FORCE:MULTIPLE

C++ benchmarks:

444.namd: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Oa
-Qcxx_features /F950000000 shlw32m.lib
-link /FORCE:MULTIPLE

447.dealII: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qprefetch
-Qcxx_features /F950000000 shlw32m.lib
-link /FORCE:MULTIPLE

450.soplex: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qcxx_features
/F950000000 shlw32m.lib -link /FORCE:MULTIPLE

453.povray: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qansi-alias
-Qcxx_features /F950000000 shlw32m.lib
-link /FORCE:MULTIPLE

Fortran benchmarks:

Continued on next page

Peak Optimization Flags (Continued)

410.bwaves: `-fast /F950000000`

416.gamess: `-Qprof_gen`(pass 1) `-Qprof_use`(pass 2) `-fast -Qunroll2 -Ob0 -Qansi-alias -Qscalar-rep- /F950000000`

434.zeusmp: `-Qprof_gen`(pass 1) `-Qprof_use`(pass 2) `-QxT -O2 -Qprec_div- -Qunroll0 -Qscalar-rep- /F950000000`

437.leslie3d: `-Qprof_gen`(pass 1) `-Qprof_use`(pass 2) `-fast /F950000000`

459.GemsFDTD: `basepeak = yes`

465.tonto: `Same as 437.leslie3d`

Benchmarks using both Fortran and C:

435.gromacs: `-Qprof_gen`(pass 1) `-Qprof_use`(pass 2) `-fast -Oa /F950000000`

436.cactusADM: `basepeak = yes`

454.calculix: `-fast /F950000000`

481.wrf: `Same as 454.calculix`

The flags file that was used to format this result can be browsed at
`http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090714.42.html`

You can also download the XML flags source by saving the following link:
`http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090714.42.xml`

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