Intel Corporation
Intel DQ965GF motherboard (Intel Core 2 Duo E6700)

CPU2006 license: 13
Test sponsor: Intel Corporation
Tested by: Intel Corporation

Test date: Jun-2007
Hardware Availability: Aug-2006
Software Availability: Aug-2006

SPECint®2006 = 19.8
SPECint_base2006 = 17.8

Hardware

CPU Name: Intel Core 2 Duo E6700
CPU Characteristics: 2.67 GHz, 1066 MHz bus
CPU MHz: 2667
FPU: Integrated
CPU(s) enabled: 2 cores, 1 chip, 2 cores/chip
CPU(s) orderable: 1 chip
Primary Cache: 32 KB I + 32 KB D on chip per core
Secondary Cache: 4 MB I+D on chip per chip
L3 Cache: None
Other Cache: None
Memory: 2 GB (2 1GB Micron MT16HTF12864AY-80ED4 DDR2 800, CL5)
Disk Subsystem: Seagate ST3320620AS 320GB Barracuda 7200.10 NCQ SATA II
Other Hardware: None

Software

Operating System: Windows Vista32 Ultimate
Compiler: Intel C++ Compiler for IA32 version 10.0
Build 20070426 Package ID: W_CC_P_10.0.025
Microsoft Visual Studio .Net 2003 (for libraries)
Auto Parallel: No
File System: NTFS
System State: Default
Base Pointers: 32-bit
Peak Pointers: 32-bit
Other Software: SmartHeap Library Version 8.0 from http://www.microquill.com/
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General Notes
Tested systems can be used with Shin-G ATX case, Antec NeoPower 480W power supply
Product description located as of 7/2007:
The system bus runs at 1066 MHz
System was configured with integrated graphics card
Binaries were built on Windows XP Professional SP2 with 4GB of RAM and /3GB boot switch

Base Compiler Invocation
C benchmarks:
icl -Qvc7.1 -Qc99

C++ benchmarks:
icl -Qvc7.1

Base Portability Flags
403.gcc: -DSPEC_CPU_WIN32
464.h264ref: -DSPEC_CPU_NO_INTTYPES -DWIN32

Results Table

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Seconds</th>
<th>Ratio</th>
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Results appear in the order in which they were run. Bold underlined text indicates a median measurement.
# SPEC CINT2006 Result

## Intel Corporation

Intel DQ965GF motherboard (Intel Core 2 Duo E6700)

<table>
<thead>
<tr>
<th>SPECint2006</th>
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<td>SPECint_base2006</td>
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**Intel Corporation**

**SPECint2006 = 19.8**

**SPECint_base2006 = 17.8**

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<tr>
<td><strong>Test date:</strong></td>
<td>Jun-2007</td>
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<td>Intel Corporation</td>
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<td><strong>Software Availability:</strong></td>
<td>Aug-2006</td>
</tr>
</tbody>
</table>

### Base Optimization Flags

C benchmarks:
- `fast` `/F5120000000 shlw32m.lib`
- `-link` `/FORCE:MULTIPLE`

C++ benchmarks:
- `fast` `-Qcxx_features` `/F5120000000 shlw32m.lib`
- `-link` `/FORCE:MULTIPLE`

### Base Other Flags

C benchmarks:
- `403.gcc`: `-Dalloca=_alloca`

### Peak Compiler Invocation

C benchmarks:
- `icl` `-Qvc7.1` `-Qc99`

C++ benchmarks:
- `icl` `-Qvc7.1`

### Peak Portability Flags

- `403.gcc`: `-DSPEC_CPU_WIN32`
- `464.h264ref`: `-DSPEC_CPU_NO_INTTYPES -DWIN32`

### Peak Optimization Flags

C benchmarks:
- `400.perlbench`: `-Qprof_gen(pass 1)` `-Qprof_use(pass 2)` `-fast` `-Qansi-alias`
  - `-Qprefetch` `/F5120000000 shlw32m.lib`
  - `-link` `/FORCE:MULTIPLE`
- `401.bzip2`: `-Qprof_gen(pass 1)` `-Qprof_use(pass 2)` `-fast` `/F5120000000 shlw32m.lib`
  - `-link` `/FORCE:MULTIPLE`
- `403.gcc`: `-Qprof_gen(pass 1)` `-Qprof_use(pass 2)` `-fast` `/F5120000000`
  - `-link` `/FORCE:MULTIPLE`
- `429.mcf`: basepeak = yes

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Continued on next page
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**Test date:** Jun-2007

**Hardware Availability:** Aug-2006

**Software Availability:** Aug-2006

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**Peak Optimization Flags (Continued)**

445.gobmk:
- `-Qprof_gen(pass 1)`
- `-Qprof_use(pass 2)`
- `-QxT` -O2 -Qipo
- `-Qprec_div` -Qansi-alias /F512000000
  -link /FORCE:MULTIPLE

456.hmmer:
- `-Qprof_gen(pass 1)`
- `-Qprof_use(pass 2)`
- `-fast` -Qunroll12
- `-Qansi-alias /F512000000 shlw32m.lib`
  -link /FORCE:MULTIPLE

458.sjeng:
- `-Qprof_gen(pass 1)`
- `-Qprof_use(pass 2)`
- `-fast` -Qunroll14
- `/F512000000 shlw32m.lib`
  -link /FORCE:MULTIPLE

462.libquantum:
- `-Qprof_gen(pass 1)`
- `-Qprof_use(pass 2)`
- `-fast` -Qunroll14
- `-Ob0` -Qprefetch -Qopt-streaming-stores:always /F512000000
  shlw32m.lib
  -link /FORCE:MULTIPLE

464.h264ref:
- Same as 456.hmmer

**C++ benchmarks:**

- `-Qprof_gen(pass 1)`
- `-Qprof_use(pass 2)`
- `-fast` -Qansi-alias

- `-Qcxx_features /F512000000 shlw32m.lib`
  -link /FORCE:MULTIPLE

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**Peak Other Flags**

**C benchmarks:**

403.gcc:
- `-Dalloca=_alloca`

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The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090714.42.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090714.42.xml

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For other inquiries, please contact webmaster@spec.org.

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